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(54) Title: A COMPARATOR FOR CONTROLLING DEAD-TIME BETWEEN SWITCHING TRANSISTORS

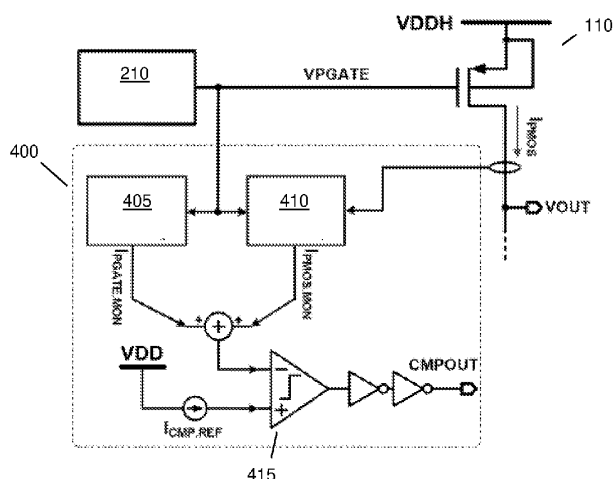


FIGURE 4

(57) Abstract: This document discloses a comparator that is configured to control dead-time between two or more switching transistors. In particular, it is disclosed that the comparator is configured to generate a suitable delay between the switching "OFF" of a transistor and the switching "ON" of another transistor so that the amount of shoot through current flowing between these two transistors are greatly minimized.

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