

DATA ERROR CORRECTION CIRCUIT AND DATA TRANSMISSION METHOD

ABSTRACT

Provided are a data error correction circuit and a data transmission method. The data transmission circuit includes: a data error correction module, configured to receive first data and a first check code corresponding to the first data, perform error correction on the first data according to the first check code to generate second data, and output the second data; and a check code generation module, configured to receive the first data and the first check code, generate a second check code according to the first data and the first check code, and output the second check code. Embodiments of the disclosure can reduce elements and a layout area required by a data error correction function.

[Fig. 1]