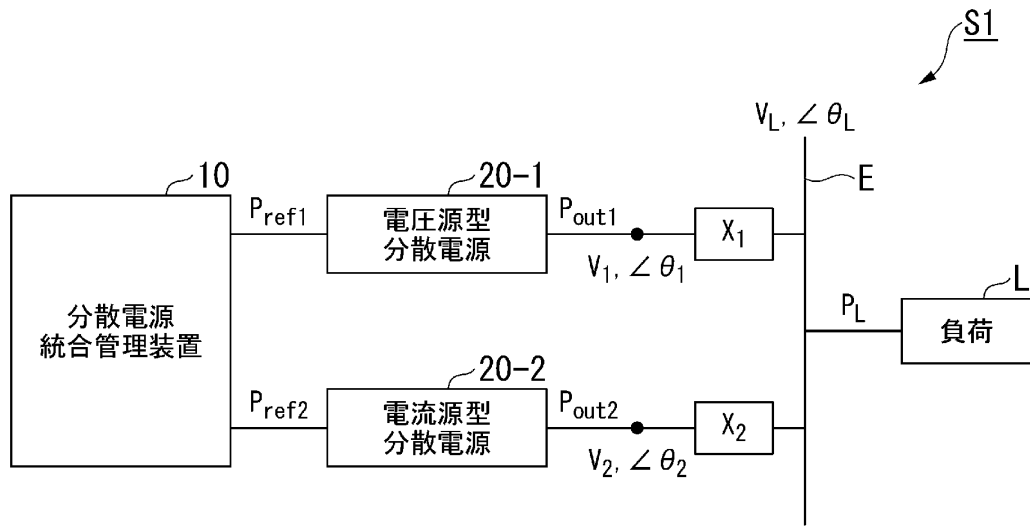
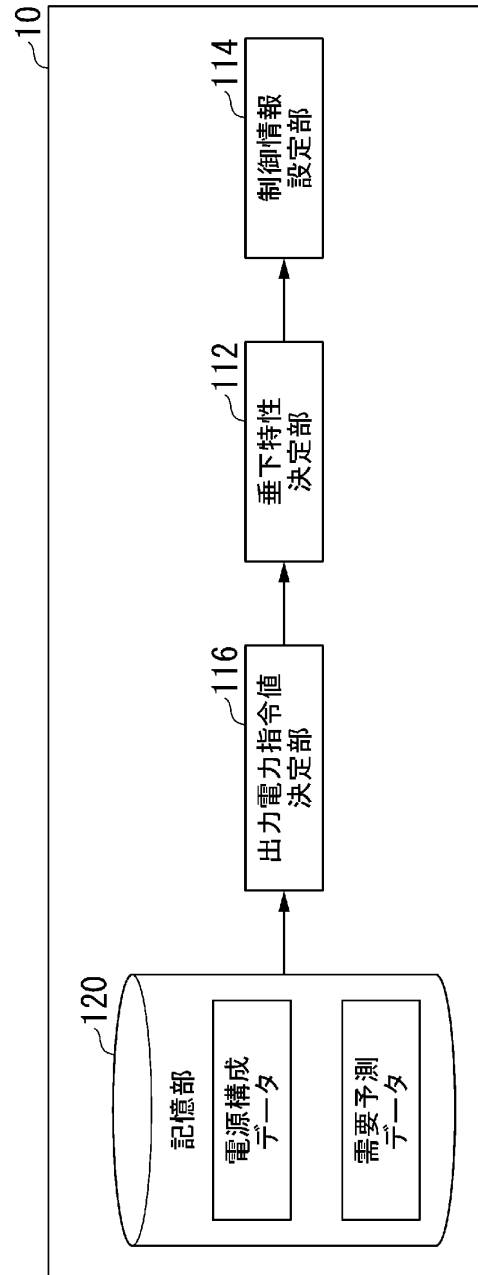


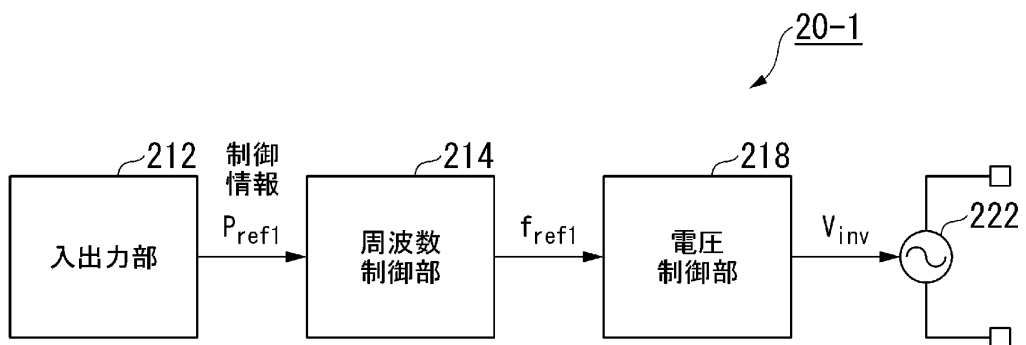
[図1]



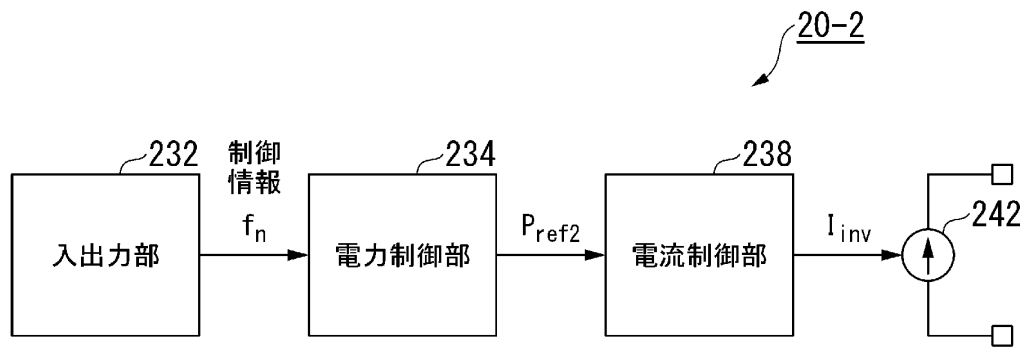
[図2]



[図3]

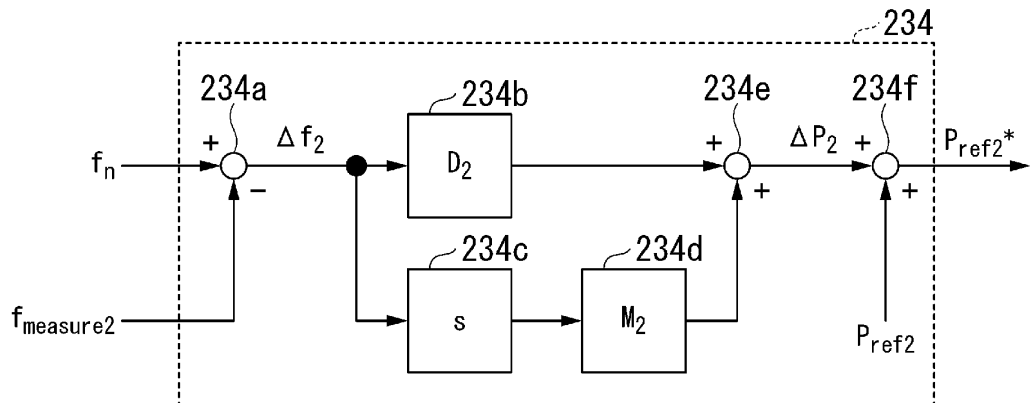


[図4]

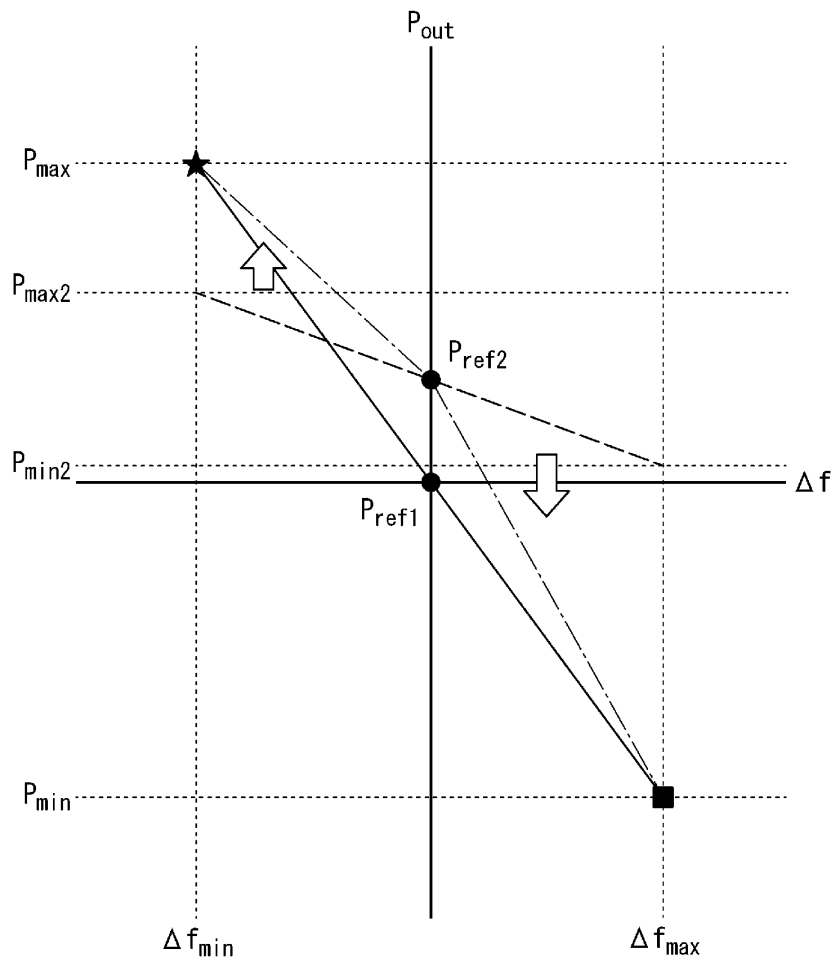


Block diagram of a control system 212. The system has two inputs: P_{ref1} and P_{out1} . P_{ref1} is summed at junction 214a with a negative feedback signal from P_{out1} . The result goes to junction 214b, which is summed with a negative feedback signal from block 214f. The output of 214b goes to block 214c ($\frac{1}{M_1 s}$). The output of 214c goes to junction 214d, which is summed with a negative feedback signal from block 214e. The output of 214d goes to junction 214g, which is summed with a disturbance f_n . The output of 214g is f_1 . f_1 goes to block 218a (2π) and then to block 218b ($\frac{1}{s}$). The output of 218b is θ_1 . There are also feedback paths from f_1 to blocks 214e and 214f.

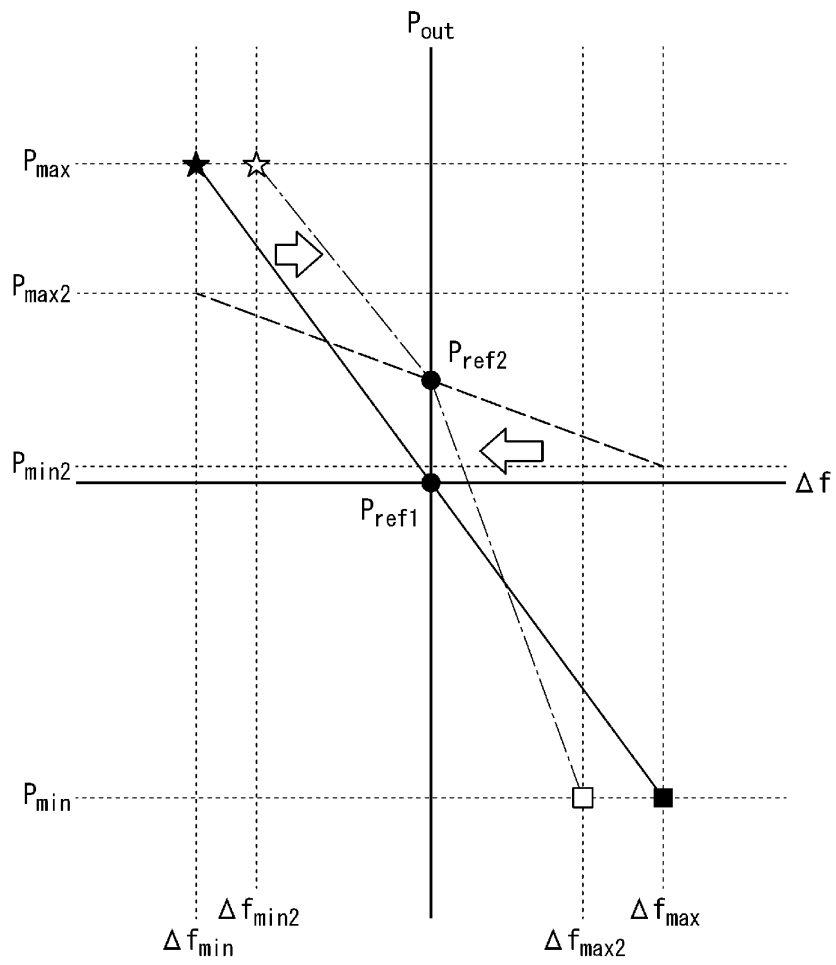
[図6]



[図7]



[図8]



[図9]

