

FIG. 1

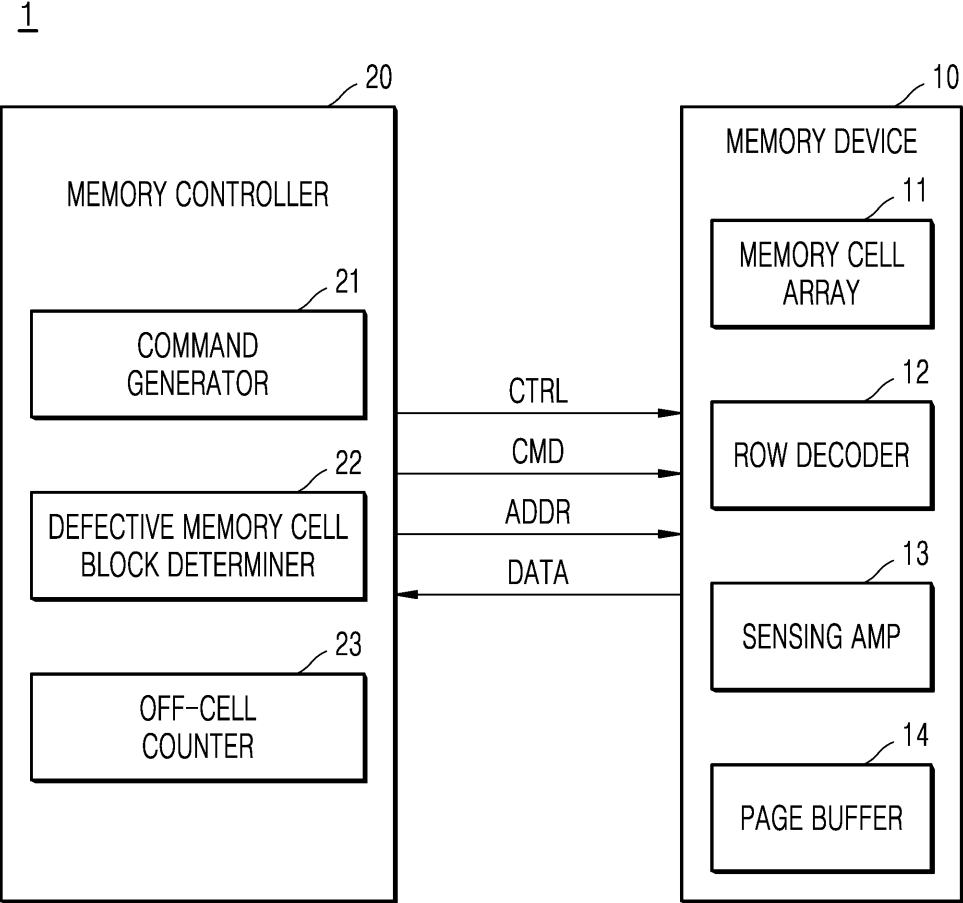


FIG. 2

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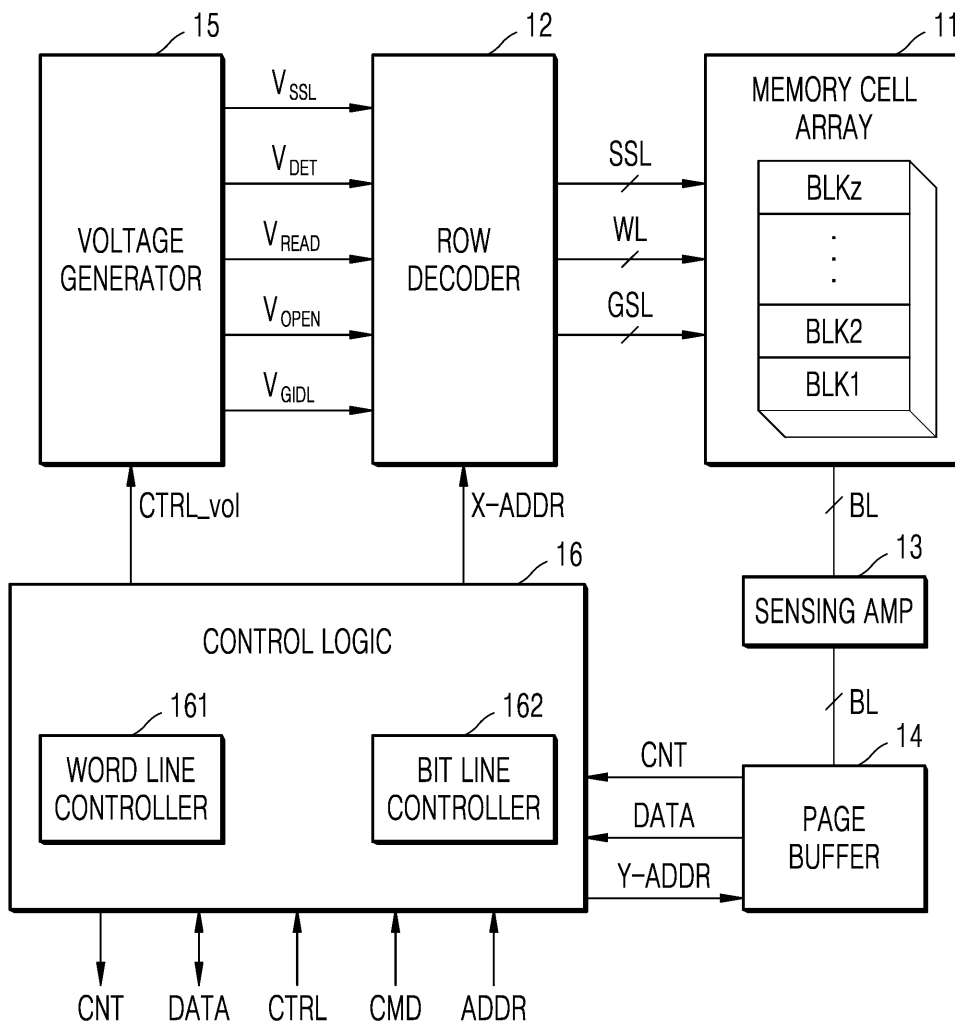
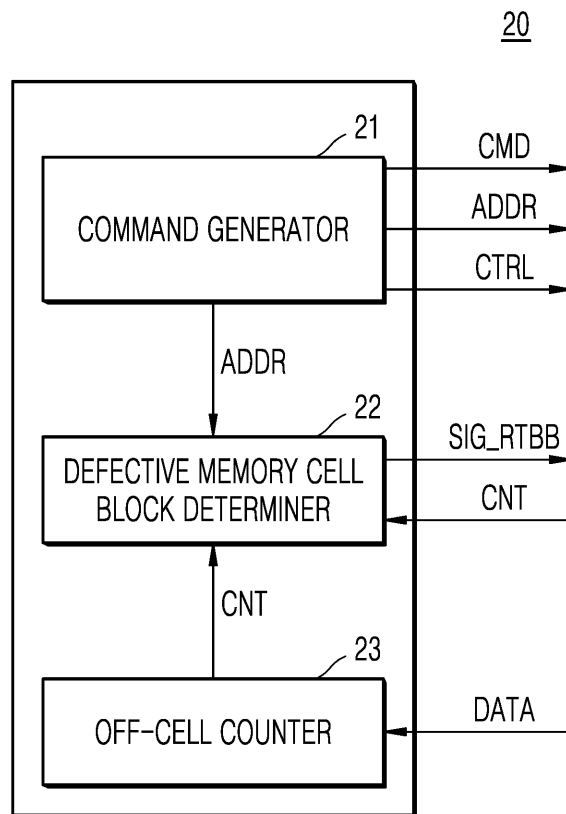


FIG. 3



BLK



FIG. 5

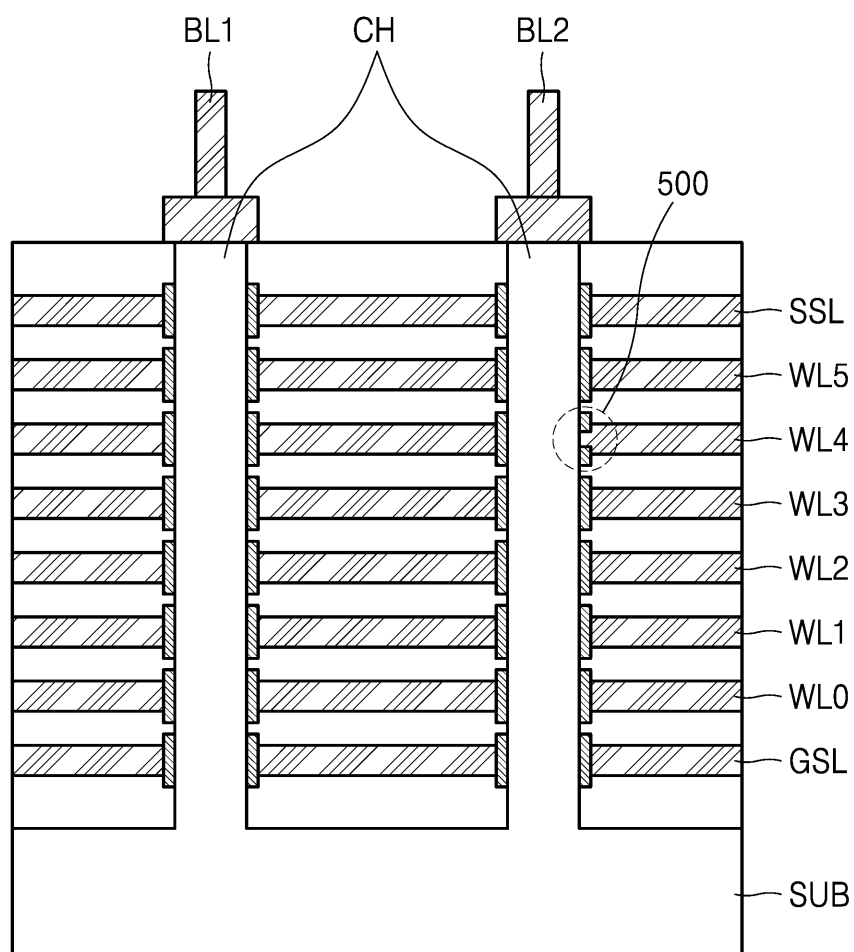


FIG. 6

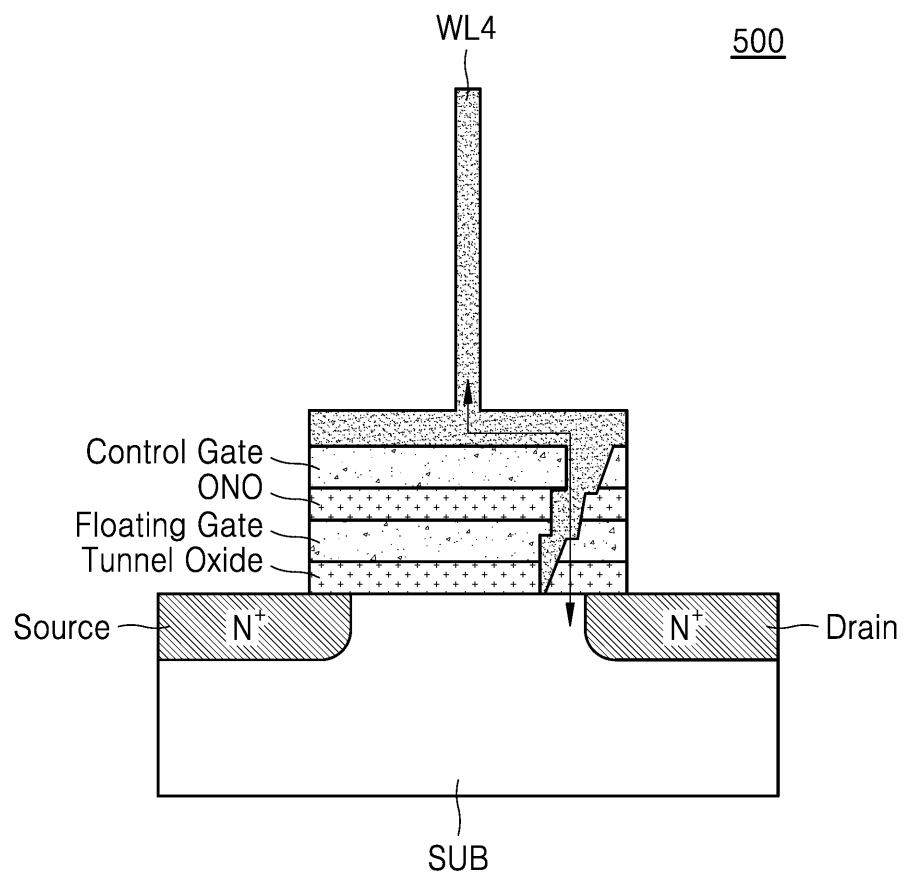


FIG. 7A

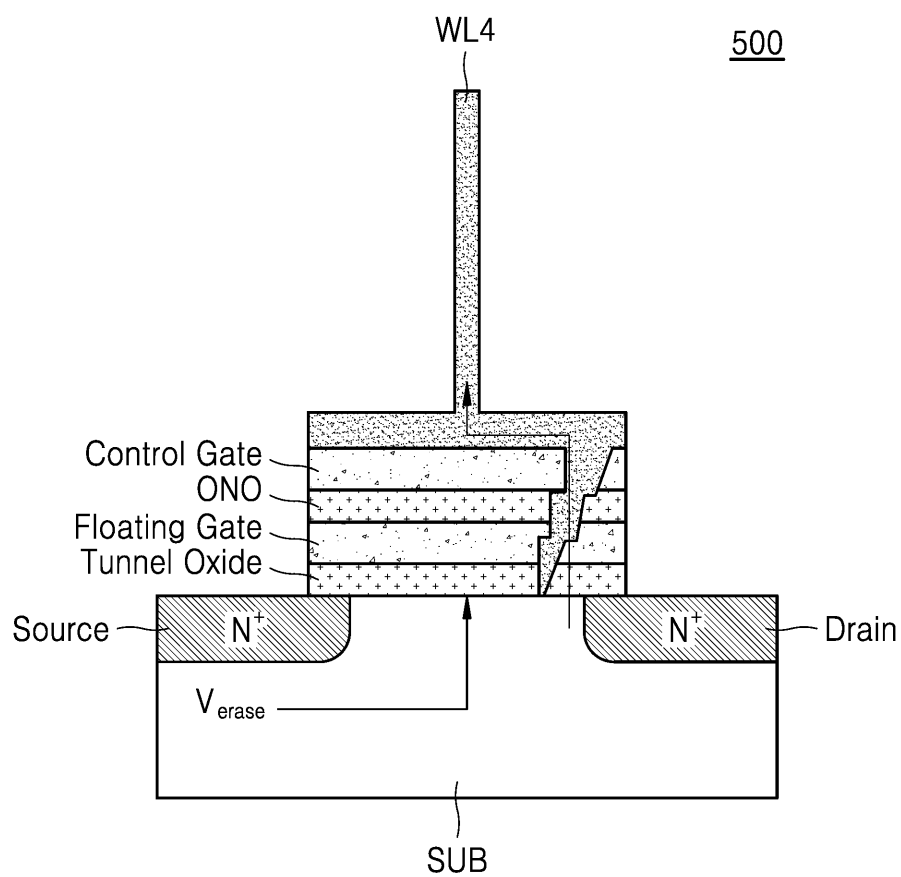


FIG. 7B

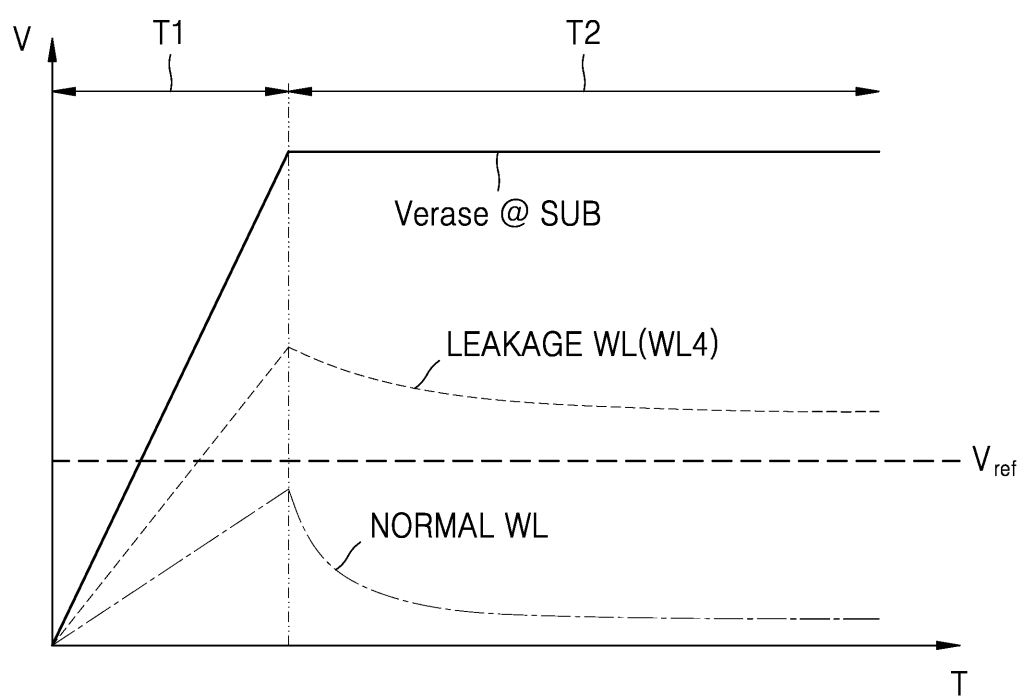


FIG. 8A

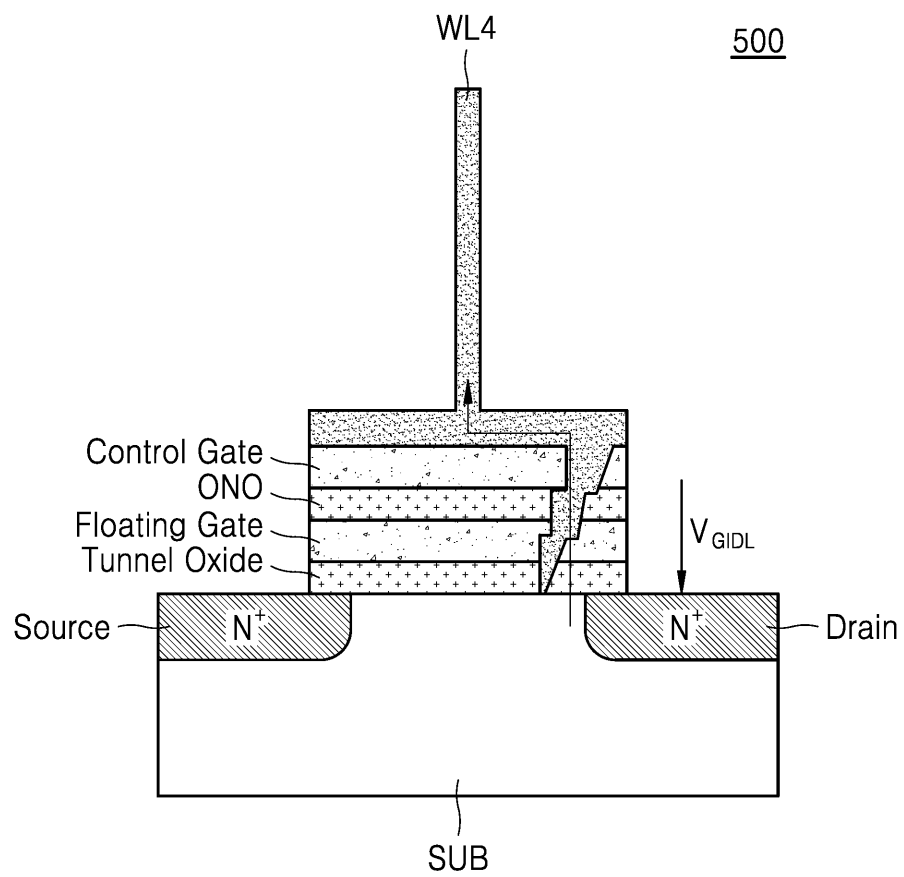


FIG. 8B

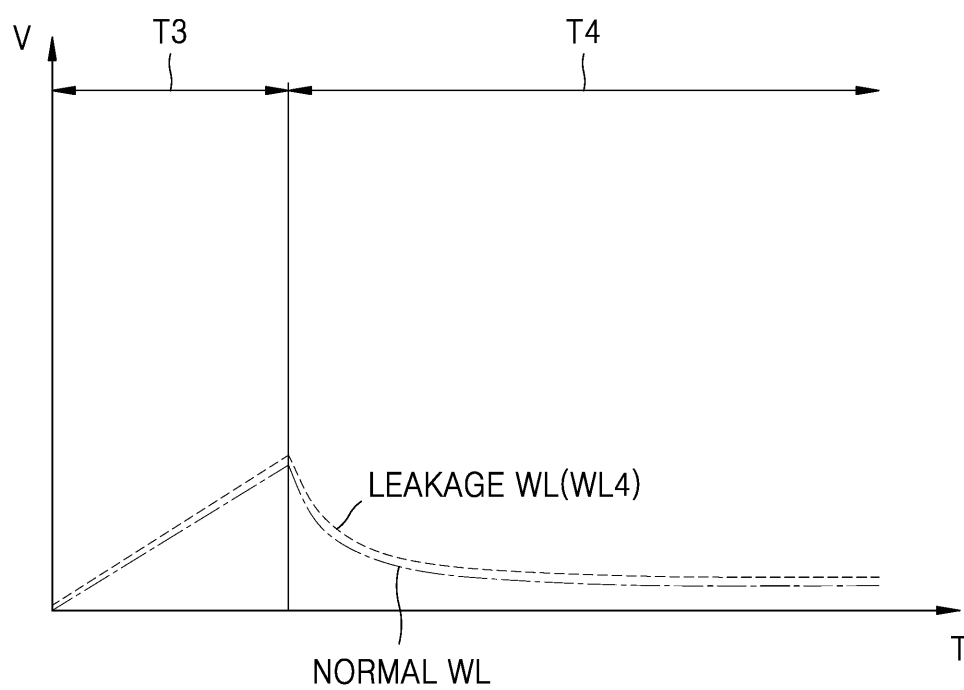


FIG. 9

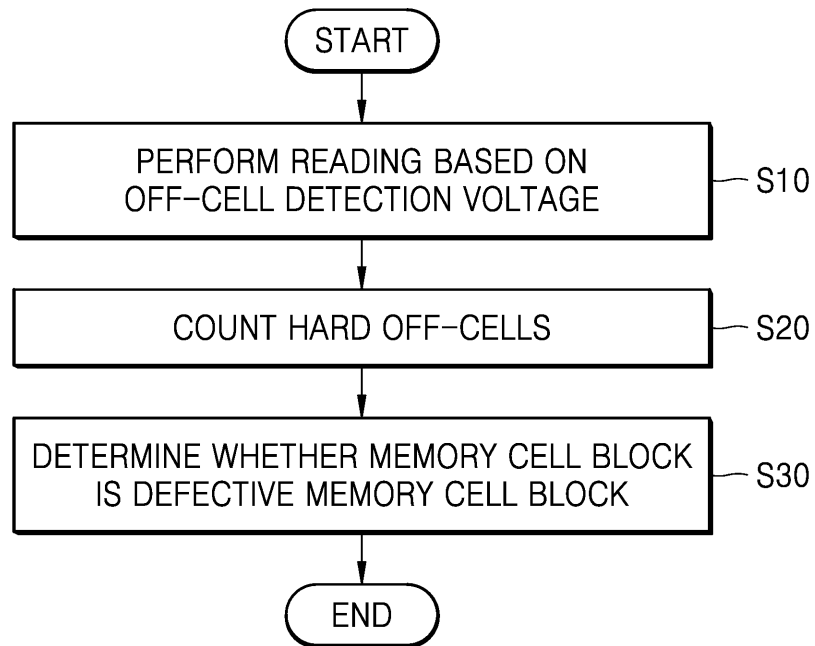


FIG. 10

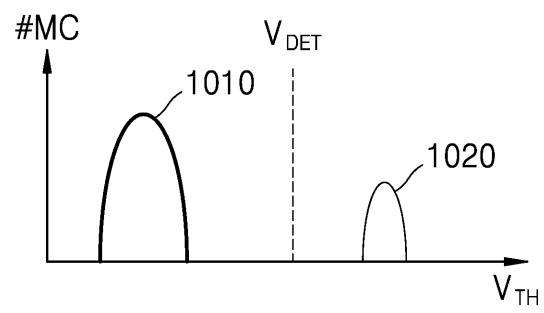


FIG. 11

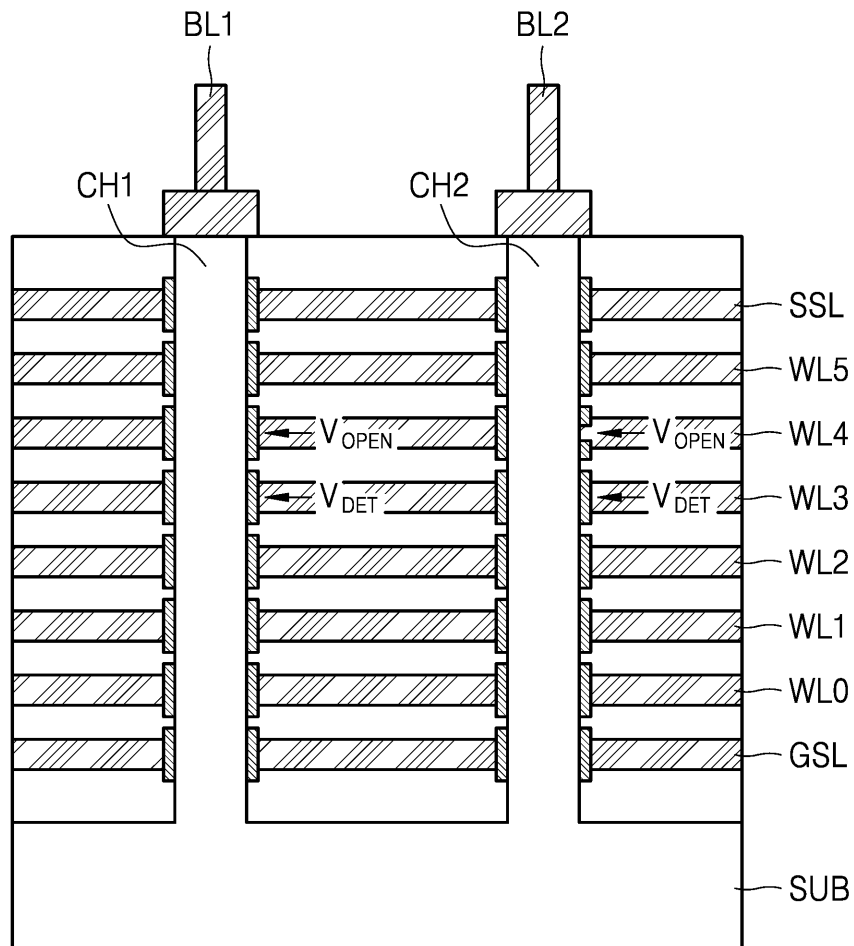


FIG. 12A

	BL1	BL2
WL3(WL _{DET})	V _{DET}	V _{DET}
WL4	V _{OPEN}	V _{OPEN}
Channel	V _{SUB}	V _{OPEN}
WL3-Channel	V _{DET} - V _{SUB}	V _{DET} - V _{OPEN}

FIG. 12B

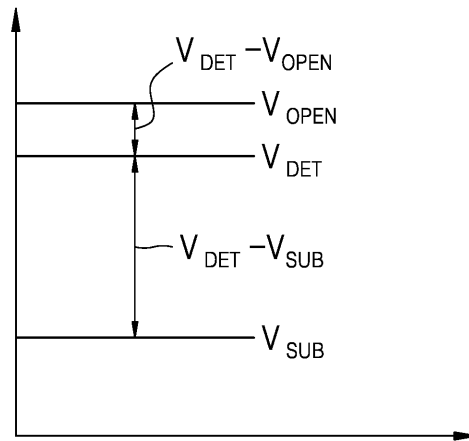


FIG. 13

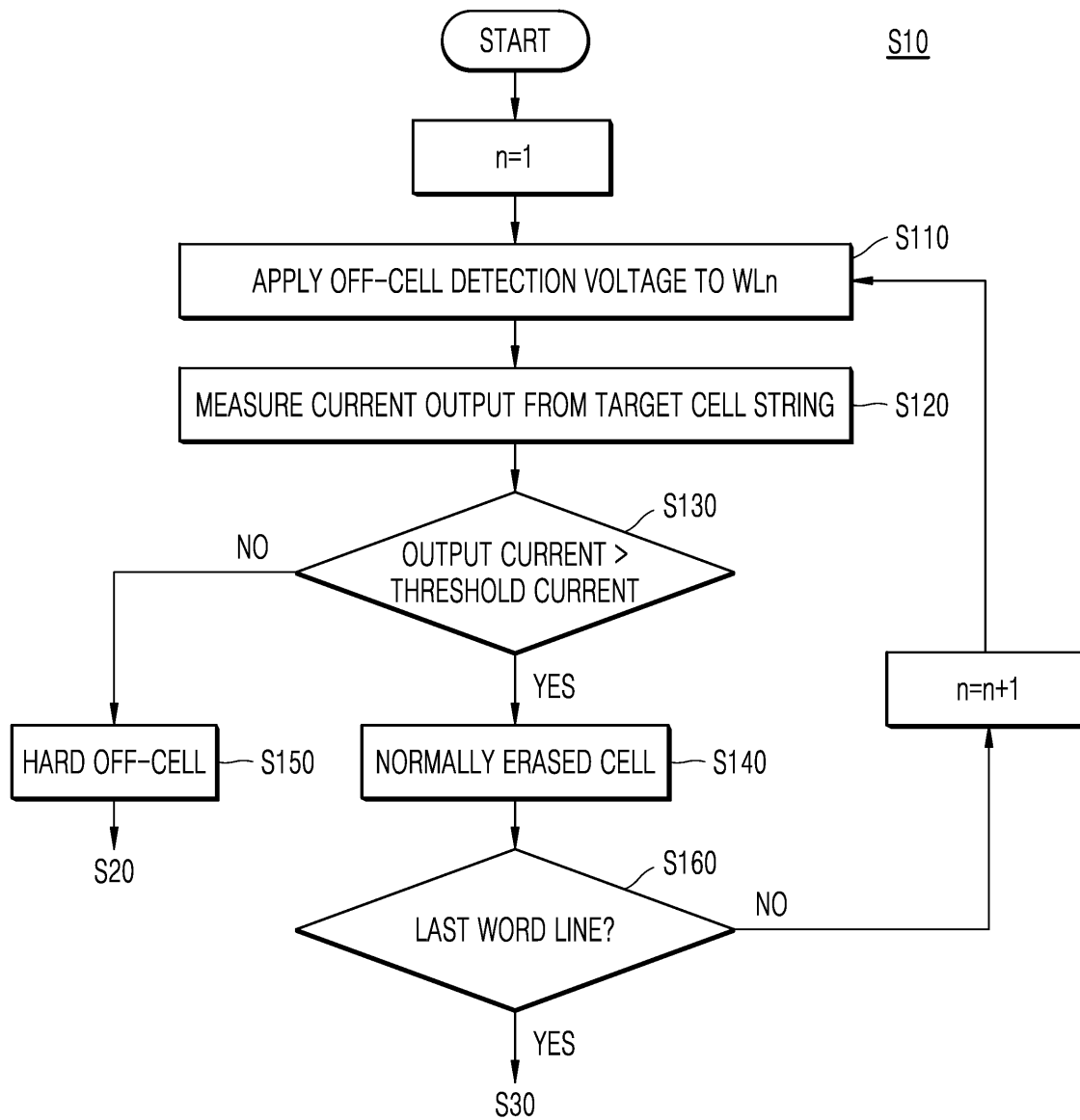


FIG. 14

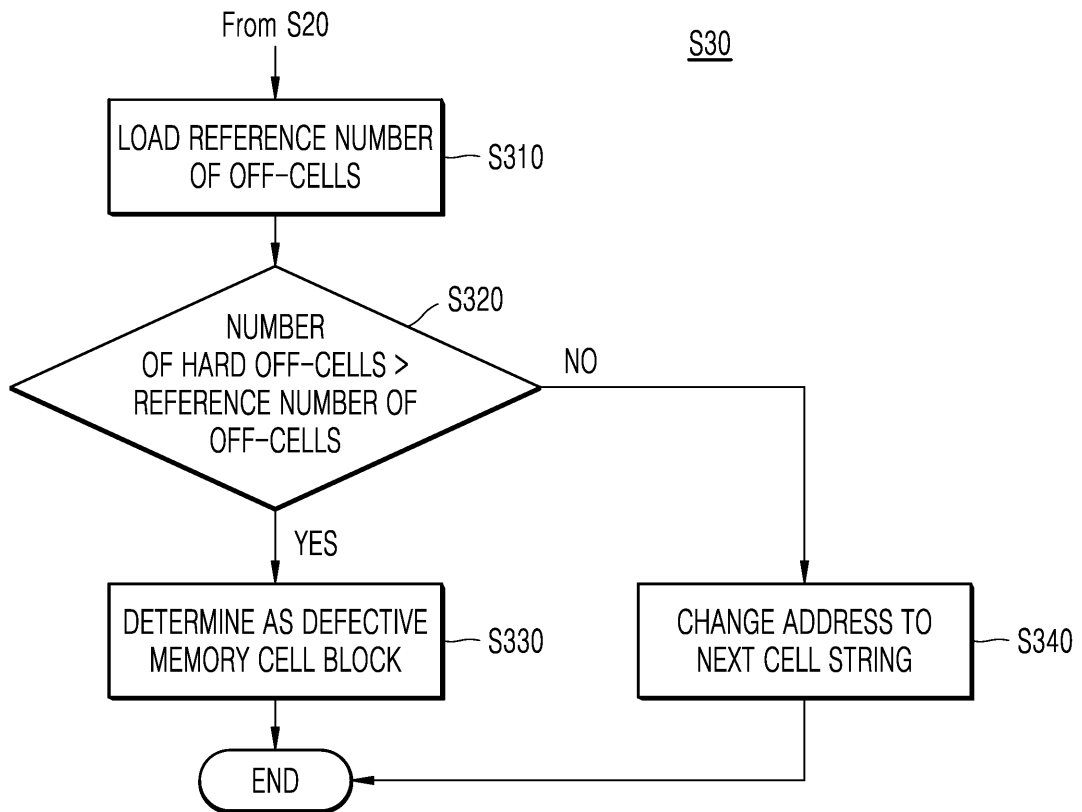


FIG. 15

BLOCK	SSL	#OFF-CELL
1	1	0
	2	2
	3	1
	⋮	⋮
	n	0

BLOCK	SSL	#OFF-CELL
2	1	1
	2	0
	3	2
	⋮	⋮
	n	0

⋮

BLOCK	SSL	#OFF-CELL
m	1	0
	2	0
	3	1
	⋮	⋮
	n	2

FIG. 16

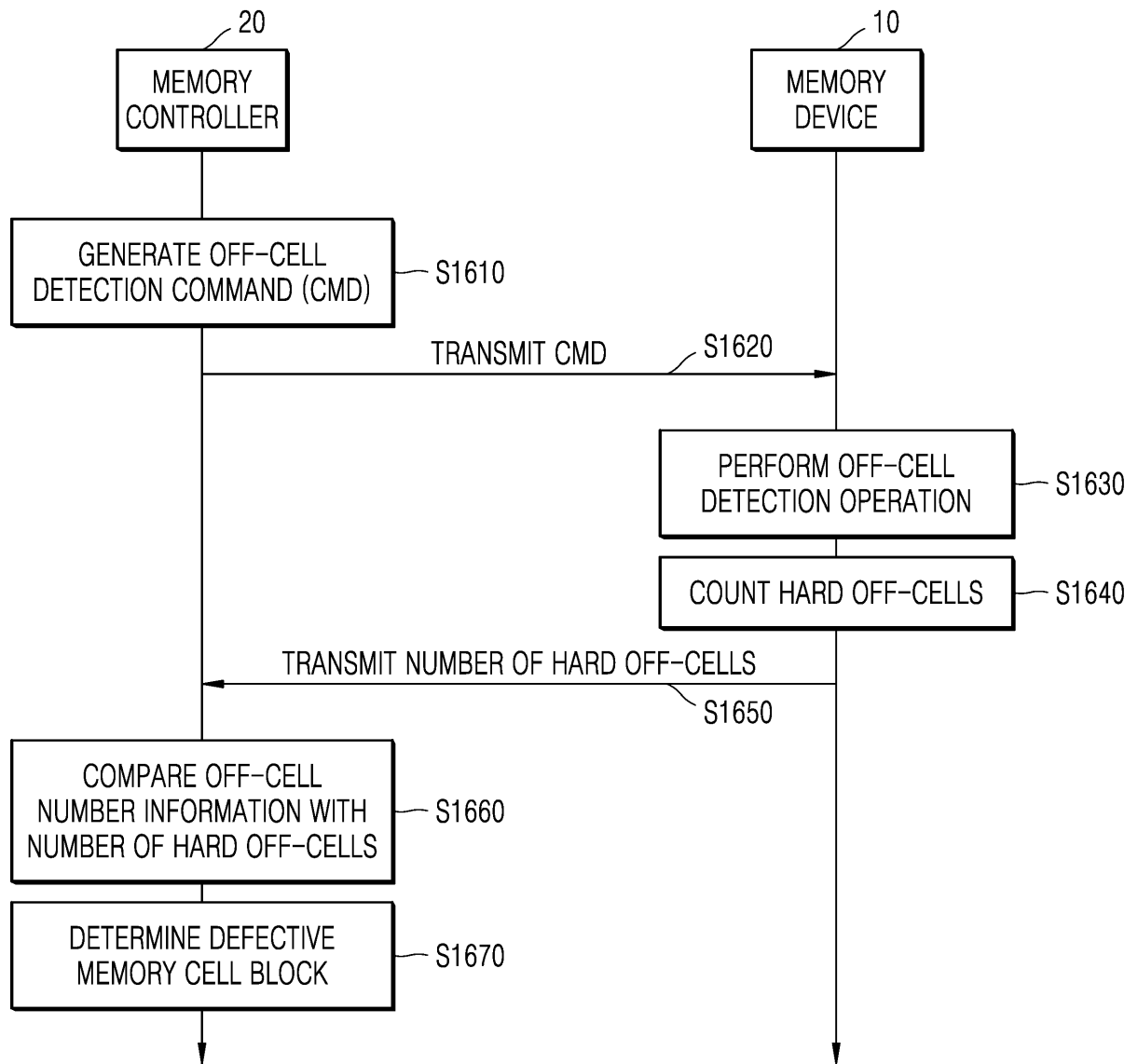


FIG. 17

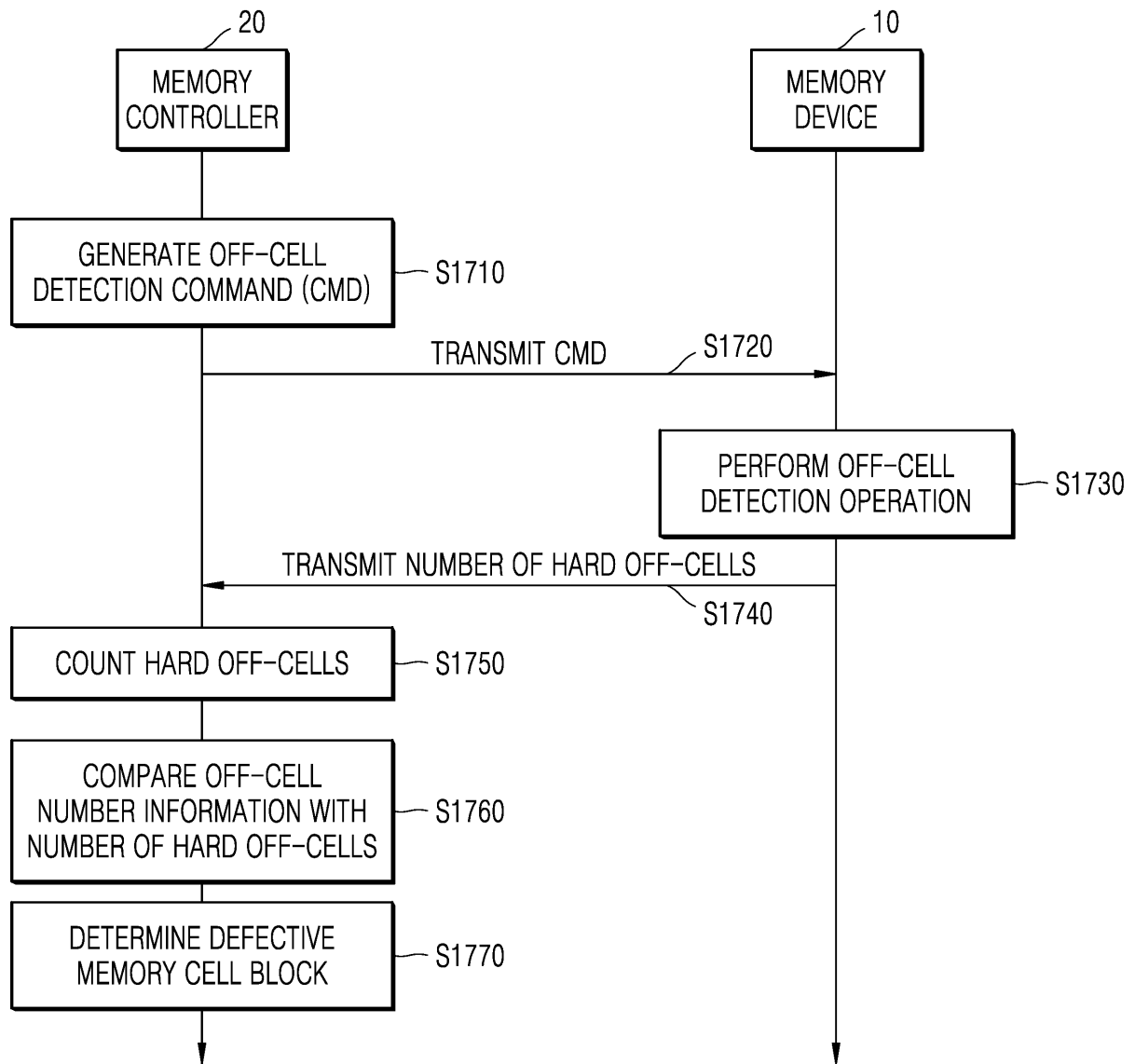


FIG. 18

