Claims

1. An apparatus for determining an estimated pitch lag, comprising:

an input interface (110) for receiving a plurality of original pitch lag values, and

a pitch lag estimator (120) for estimating the estimated pitch lag,

wherein the pitch lag estimator is configured to estimate the estimated pitch lag by minimizing an error function,

wherein the pitch lag estimator (120) is configured to estimate the estimated pitch lag depending on a plurality of original pitch lag values and depending on a plurality of pitch gain values, wherein for each original pitch lag value of the plurality of original pitch lag values, a pitch gain value of the plurality of pitch gain values is assigned to said original pitch lag value, or

wherein the pitch lag estimator (120) is configured to estimate the estimated pitch lag depending on the plurality of original pitch lag values and depending on a plurality of time values, each of the plurality of time values depending on an amount of time that has passed after correctly receiving the pitch lag, wherein for each original pitch lag value of the plurality of original pitch lag values, a time value of the plurality of time values is assigned to said original pitch lag value, or

wherein the pitch lag estimator (120) is configured to estimate the estimated pitch lag depending on said plurality of original pitch lag values and depending on said plurality of pitch gain values and depending on said plurality of time values, each of the plurality of time values depending on the amount of time that has passed after correctly receiving the pitch lag, wherein for each original pitch lag value of the plurality of original pitch lag values, a pitch gain value of the plurality of pitch gain values is assigned to said original pitch lag value, and wherein for each original pitch lag value of the plurality of original pitch lag values, a time value of the plurality of time values is assigned to said original pitch lag value.

2. An apparatus according to claim 1, wherein the pitch lag estimator (120) is configured to estimate the estimated pitch lag depending on said plurality of original pitch lag values and depending on said plurality of pitch gain values, wherein for each original pitch lag value of the plurality of original pitch lag values,

a pitch gain value of the plurality of pitch gain values is assigned to said original pitch lag value.

3. An apparatus according to claim 2, wherein each of the plurality of pitch gain values is an adaptive codebook gain.

4. An apparatus according to claim 2, wherein the pitch lag estimator is configured to estimate the estimated pitch lag by determining two parameters a, b, by minimizing the error function

$$err = \sum_{i=0}^{k} g_p(i) \cdot ((a + b \cdot i) - P(i))^2$$

wherein a is a real number,

wherein b is a real number,

wherein k is an integer with $k \geq 2$, and

wherein $P(i)$ is the i-th original pitch lag value,

wherein $g_p(i)$ is the i-th pitch gain value being assigned to the i -th pitch lag value $P(i)$.

5. An apparatus according to claim 4, wherein $k = 4$.

6. An apparatus according to claim 4 or 5,

wherein the pitch lag estimator (120) is configured to estimate the estimated pitch lag depending on said plurality of original pitch lag values and depending on said plurality of pitch gain values,

wherein the pitch lag estimator is configured to determine the estimated pitch lag p according to

$$p = a \cdot i + b.$$

7. An apparatus according to claim 1, wherein the pitch lag estimator (120) is configured to estimate the estimated pitch lag depending on said plurality of original pitch lag values and depending on said plurality of time values, each of the plurality of time values depending on the amount of time that has passed after correctly receiving the pitch lag, wherein for each original pitch lag value of the plurality of original pitch lag values, a time value of the plurality of time values is assigned to said original pitch lag value.

8. An apparatus according to claim 7,

wherein the pitch lag estimator is configured to estimate the estimated pitch lag by determining two parameters a, b, by minimizing the error function

$$err = \sum_{i=0}^{k} time_{passed}(i) \cdot ((a + b \cdot i) - P(i))^2$$

wherein a is a real number,

wherein b is a real number,

wherein k is an integer with $k \geq 2$, and

wherein $P(i)$ is the i-th original pitch lag value,

wherein $time_{passed}(i)$ is the i-th time value being assigned to the i -th pitch lag value $P(i)$.

9. An apparatus according to claim 8, wherein $k = 4$.

10. An apparatus according to claim 8 or 9, wherein the pitch lag estimator is configured to determine the estimated pitch lag p according to

$$p = a \cdot i + b,$$

wherein a is a real number,

wherein b is a real number, and

wherein i is an index.

11. An apparatus according to one of the preceding claims, wherein the pitch lag estimator (120) is configured to estimate the estimated pitch lag depending on said plurality of original pitch lag values and depending on said plurality of pitch gain values and depending on said plurality of time values, each of the plurality of time values depending on the amount of time that has passed after correctly receiving the pitch lag, wherein for each original pitch lag value of the plurality of original pitch lag values, a pitch gain value of the plurality of pitch gain values is assigned to said original pitch lag value, and wherein for each original pitch lag value of the plurality of original pitch lag values, a time value of the plurality of time values is assigned to said original pitch lag value.

12. A system for reconstructing a frame comprising a speech signal, wherein the system comprises:

an apparatus according to one of the preceding claims for determining an estimated pitch lag, and

an apparatus for reconstructing the frame, wherein the apparatus for reconstructing the frame is configured to reconstruct the frame depending on the estimated pitch lag,

wherein the estimated pitch lag is a pitch lag of the speech signal.

13. A system for reconstructing a frame according to claim 12,

wherein the reconstructed frame is associated with one or more available frames, said one or more available frames being at least one of one or more preceding frames of the reconstructed frame and one or more succeeding frames of the reconstructed frame, wherein the one or more available frames comprise one or more pitch cycles as one or more available pitch cycles, and

wherein the apparatus for reconstructing the frame comprises

a determination unit (210) for determining a sample number difference indicating a difference between a number of samples of one of the one or more available pitch cycles and a number of samples of a first pitch cycle to be reconstructed, and

a frame reconstructor (220) for reconstructing the reconstructed frame by reconstructing, depending on the sample number difference and depending on the samples of said one of the one or more available pitch cycles, the first pitch cycle to be reconstructed as a first reconstructed pitch cycle,

wherein the frame reconstructor (220) is configured to reconstruct the reconstructed frame, such that the reconstructed frame completely or partially comprises the first reconstructed pitch cycle, such that the reconstructed frame completely or partially comprises a second reconstructed pitch cycle, and such that the number of samples of the first reconstructed pitch cycle differs from a number of samples of the second reconstructed pitch cycle,

wherein the determination unit (210) is configured to determine the sample number difference depending on the estimated pitch lag.

14. A method for determining an estimated pitch lag, comprising:

receiving a plurality of original pitch lag values, and

estimating the estimated pitch lag,

wherein estimating the estimated pitch lag is conducted by minimizing an error function.

wherein estimating the estimated pitch lag is conducted depending on a plurality of original pitch lag values and depending on a plurality of pitch gain values, wherein for each original pitch lag value of the plurality of original pitch lag values, a pitch gain value of the plurality of pitch gain values is assigned to said original pitch lag value, or

wherein estimating the estimated pitch lag is conducted depending on a plurality of original pitch lag values and depending on a plurality of time values, each of the plurality of time values depending on an amount of time that has passed after correctly receiving the pitch lag, wherein for each original pitch lag value of the

plurality of original pitch lag values, a time value of the plurality of time values is assigned to said original pitch lag value, or

wherein estimating the estimated pitch lag is conducted depending on said plurality of original pitch lag values and depending on said plurality of pitch gain values and depending on said plurality of time values, each of the plurality of time values depending on the amount of time that has passed after correctly receiving the pitch lag, wherein for each original pitch lag value of the plurality of original pitch lag values, a pitch gain value of the plurality of pitch gain values is assigned to said original pitch lag value, and wherein for each original pitch lag value of the plurality of original pitch lag values, a time value of the plurality of time values is assigned to said original pitch lag value.

15. A computer program for implementing the method of claim 14 when being executed on a computer or signal processor.

Claims

1. An apparatus for determining an estimated pitch lag, comprising:

an input interface for receiving a plurality of original pitch lag values, and

a pitch lag estimator for estimating the estimated pitch lag,

wherein the pitch lag estimator is configured to estimate the estimated pitch lag depending on a plurality of original pitch lag values and depending on a plurality of information values, wherein for each original pitch lag value of the plurality of original pitch lag values, an information value of the plurality of information values is assigned to said original pitch lag value.

2. An apparatus according to claim 1, wherein the pitch lag estimator is configured to estimate the estimated pitch lag depending on the plurality of original pitch lag values and depending on a plurality of pitch gain values as the plurality of information values, wherein for each original pitch lag value of the plurality of original pitch lag values, a pitch gain value of the plurality of pitch gain values is assigned to said original pitch lag value.

3. An apparatus according to claim 2, wherein each of the plurality of pitch gain values is an adaptive codebook gain.

4. An apparatus according to claim 2 or 3, wherein the pitch lag estimator is configured to estimate the estimated pitch lag by minimizing an error function.

5. An apparatus according to claim 4, wherein the pitch lag estimator is configured to estimate the estimated pitch lag by determining two parameters a, b, by minimizing the error function

$$err = \sum_{i=0}^{k} g_p(i) \cdot ((a + b \cdot i) - P(i))^2 ,$$

wherein a is a real number,

wherein b is a real number,

wherein k is an integer with $k \geq 2$, and

wherein $P(i)$ is the i-th original pitch lag value,

wherein $g_p(i)$ is the i-th pitch gain value being assigned to the i -th pitch lag value $P(i)$.

6. An apparatus according to claim 4, wherein the pitch lag estimator is configured to estimate the estimated pitch lag by determining two parameters a, b, by minimizing the error function

$$err = \sum_{i=0}^{4} g_p(i) \cdot ((a + b \cdot i) - P(i))^2 ,$$

wherein a is a real number,

wherein b is a real number,

wherein P(i) is the i-th original pitch lag value,

wherein $g_p(i)$ is the i-th pitch gain value being assigned to the i -th pitch lag value P(i).

7. An apparatus according to claim 4 or 5, wherein the pitch lag estimator is configured to determine the estimated pitch lag p according to

$p = a \cdot i + b.$

8. An apparatus according to claim 1, wherein the pitch lag estimator is configured to estimate the estimated pitch lag depending on the plurality of original pitch lag values and depending on a plurality of time values as the plurality of information values, wherein for each original pitch lag value of the plurality of original pitch lag values, a time value of the plurality of time values is assigned to said original pitch lag value.

9. An apparatus according to claim 8, wherein the pitch lag estimator is configured to estimate the estimated pitch lag by minimizing an error function.

10. An apparatus according to claim 9, wherein the pitch lag estimator is configured to estimate the estimated pitch lag by determining two parameters a, b, by minimizing the error function

$$err = \sum_{i=0}^{k} time_{passed}(i) \cdot ((a + b \cdot i) - P(i))^2 ,$$

wherein a is a real number,

wherein b is a real number,

wherein k is an integer with $k \geq 2$, and

wherein $P(i)$ is the i-th original pitch lag value,

wherein $time_{passed}(i)$ is the i-th time value being assigned to the i -th pitch lag value $P(i)$.

11. An apparatus according to claim 9, wherein the pitch lag estimator is configured to estimate the estimated pitch lag by determining two parameters a, b, by minimizing the error function

$$err = \sum_{i=0}^{4} time_{passed}(i) \cdot ((a + b \cdot i) - P(i))^2 ,$$

wherein a is a real number,

wherein b is a real number,

wherein P(i) is the i-th original pitch lag value,

wherein $time_{passed}(i)$ is the i-th time value being assigned to the i -th pitch lag value P(i).

12. An apparatus according to claim 10 or 11, wherein the pitch lag estimator is configured to determine the estimated pitch lag p according to

$$p = a \cdot i + b.$$

13. A system for reconstructing a frame comprising a speech signal, wherein the system comprises:

an apparatus according to claim 1 for determining an estimated pitch lag, and

an apparatus for reconstructing the frame, wherein the apparatus for reconstructing the frame is configured to reconstruct the frame depending on the estimated pitch lag,

wherein the estimated pitch lag is a pitch lag of the speech signal.

14. An system for reconstructing a frame according to claim 13,

wherein the reconstructed frame is associated with one or more available frames, said one or more available frames being at least one of one or more preceding frames of the reconstructed frame and one or more succeeding frames of the reconstructed frame, wherein the one or more available frames comprise one or more pitch cycles as one or more available pitch cycles, and

wherein the apparatus for reconstructing the frame comprises

a determination unit for determining a sample number difference indicating a difference between a number of samples of one of the one or more available pitch cycles and a number of samples of a first pitch cycle to be reconstructed, and

a frame reconstructor for reconstructing the reconstructed frame by reconstructing, depending on the sample number difference and depending on the samples of said one of the one or more available pitch cycles, the first pitch cycle to be reconstructed as a first reconstructed pitch cycle,

wherein the frame reconstructor is configured to reconstruct the reconstructed frame, such that the reconstructed frame completely or partially comprises the first reconstructed pitch cycle, such that the reconstructed frame completely or partially

comprises a second reconstructed pitch cycle, and such that the number of samples of the first reconstructed pitch cycle differs from a number of samples of the second reconstructed pitch cycle,

wherein the determination unit is configured to determine the sample number difference depending on the estimated pitch lag.

15. A method for determining an estimated pitch lag, comprising:

receiving a plurality of original pitch lag values, and

estimating the estimated pitch lag,

wherein estimating the estimated pitch lag is conducted depending on a plurality of original pitch lag values and depending on a plurality of information values, wherein for each original pitch lag value of the plurality of original pitch lag values, an information value of the plurality of information values is assigned to said original pitch lag value.

16. A computer program for implementing the method of claim 15 when being executed on a computer or signal processor.