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[Continued on next page]

(54) **Title:** INSTRUCTIONS FOR PERFORMING AN OPERATION ON A OPERAND IN MEMORY AND SUBSEQUENTLY LOADING AN ORIGINAL VALUE OF SAID OPERAND IN A REGISTER

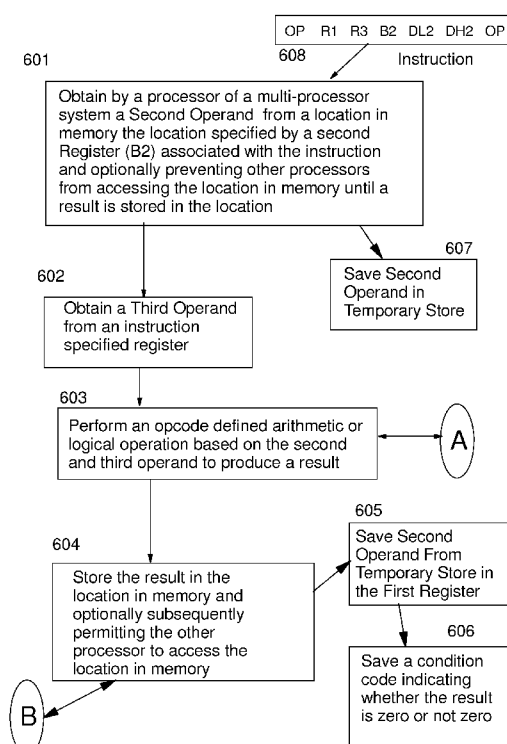


FIG. 6A

(57) **Abstract:** An arithmetic/logical instruction is executed having inter-locked memory operands, when executed obtains a second operand from a location in memory, and saves a temporary copy of the second operand, the execution performs an arithmetic or logical operation based on the second operand and a third operand and stores the result in the memory location of the second operand, and subsequently stores the temporary copy in a first register.



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