

FIGURE 1
PRIOR ART

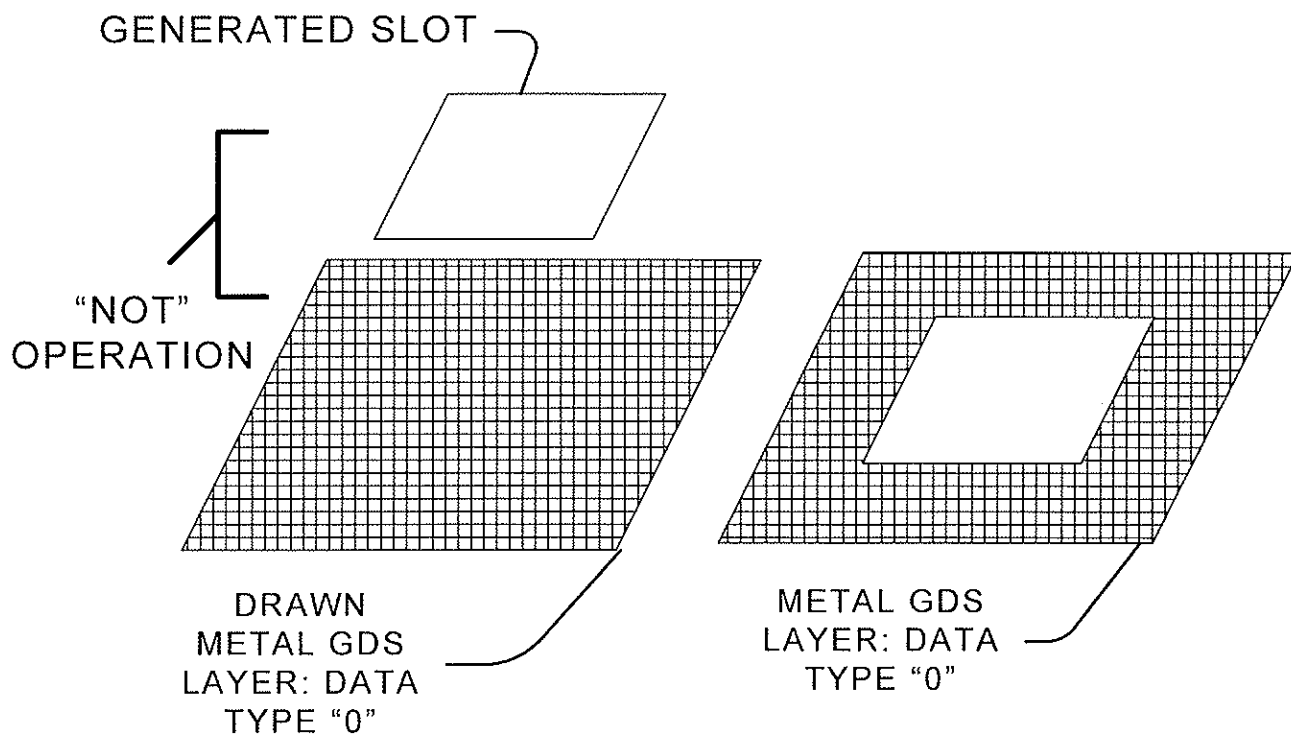


FIGURE 3A

FIGURE 3B

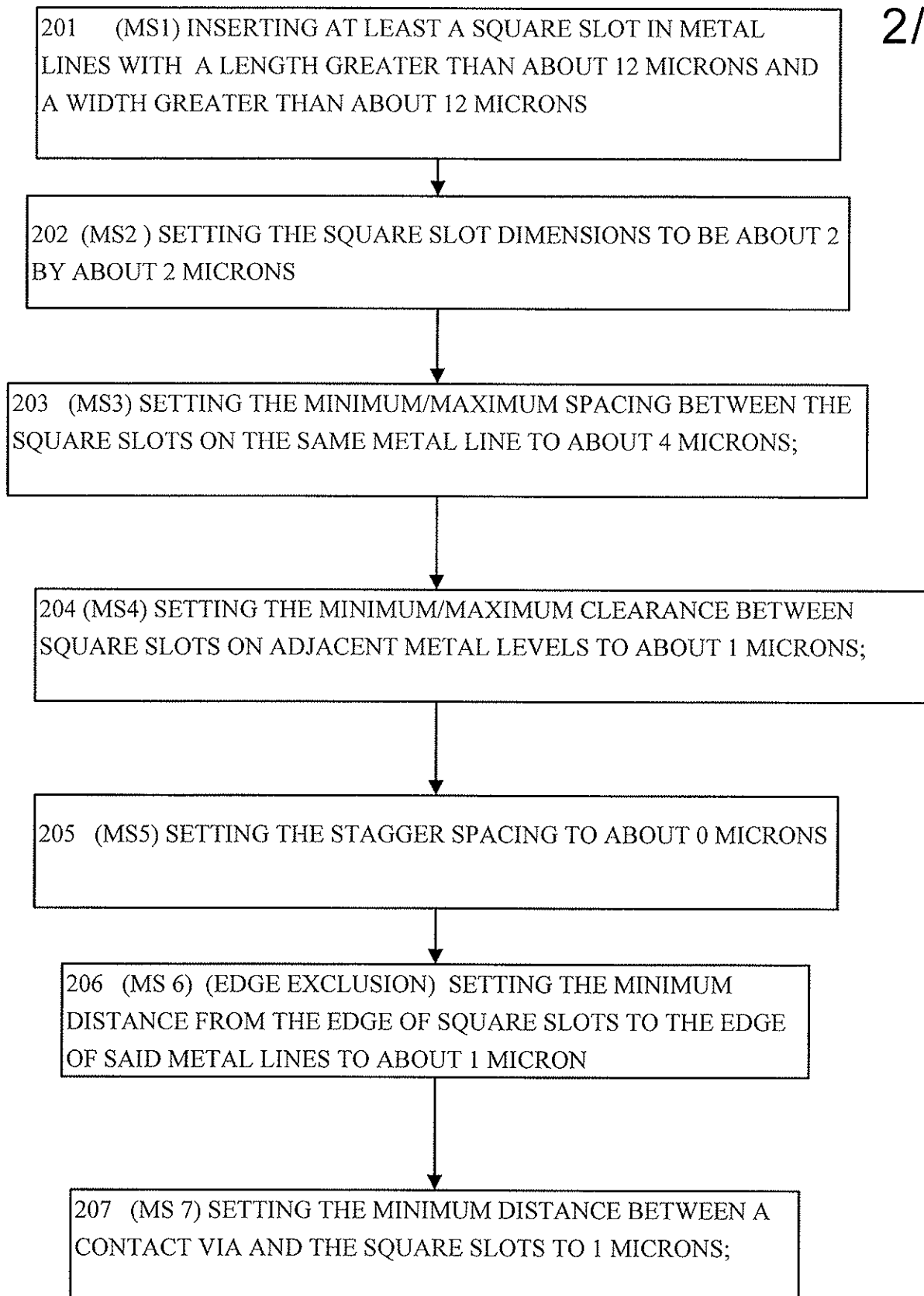
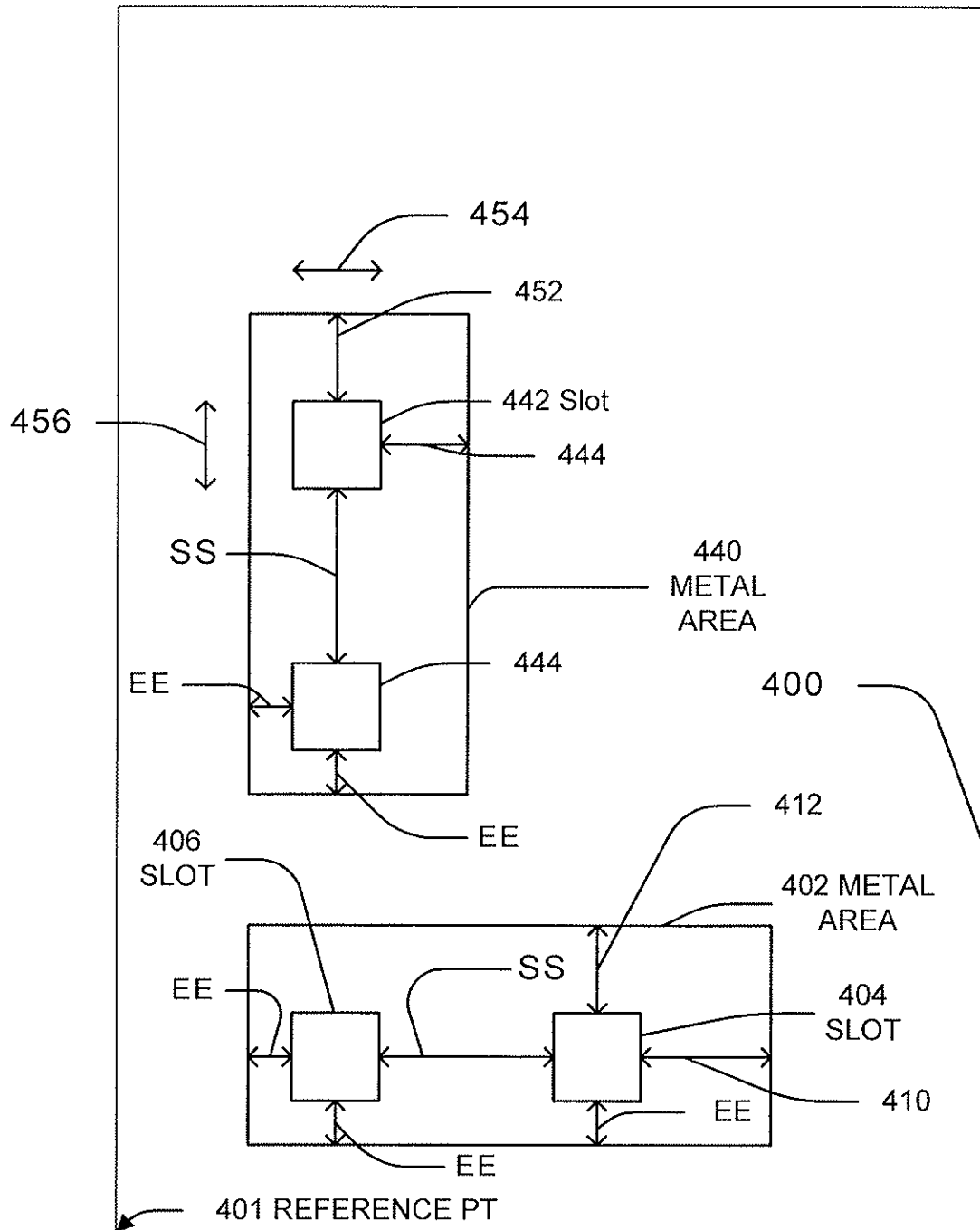


FIGURE 2

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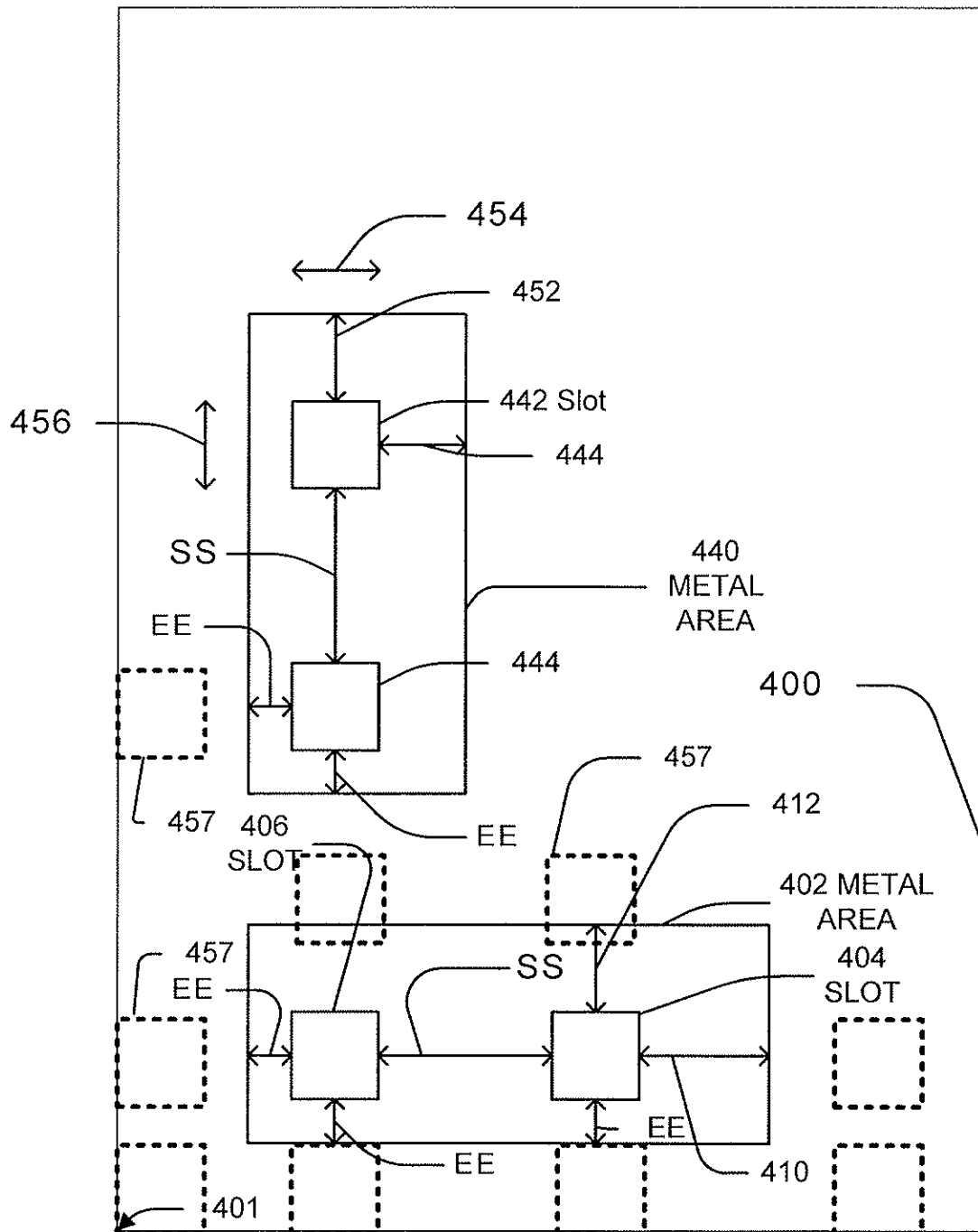


EE = MINIMUM EDGE EXCLUSION = 1 MICRON

SS = SLOT SPACING = 4 MICRONS

FIGURE 4A

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EE = MINIMUM EDGE EXCLUSION = 1 MICRON

SS = SLOT SPACING = 4 MICRONS

FIGURE 4B

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Slot size	spacing between odd and even layer (y)	slot spacing (2y+x)
0.5	1	2.5
1	1	3
1.5	1	3.5
2	1	4
2.5	1	4.5
3	1	5
3.5	1	5.5
4	1	6
0.5	0.5	1.5
1	0.5	2
1.5	0.5	2.5
2	0.5	3
2.5	0.5	3.5
3	0.5	4
3.5	0.5	4.5
4	0.5	5
0.5	2	4.5
1	2	5
1.5	2	5.5
2	2	6
2.5	2	6.5
3	2	7
3.5	2	7.5
4	2	8

FIGURE 4C

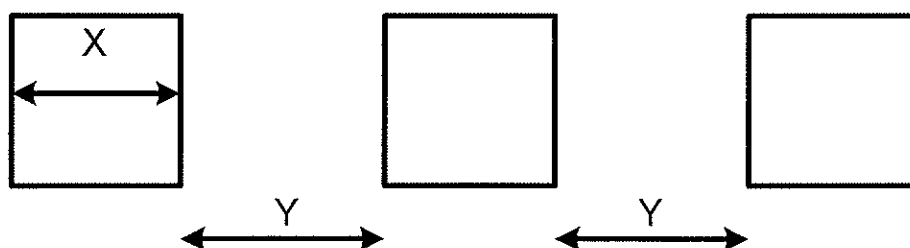
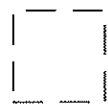
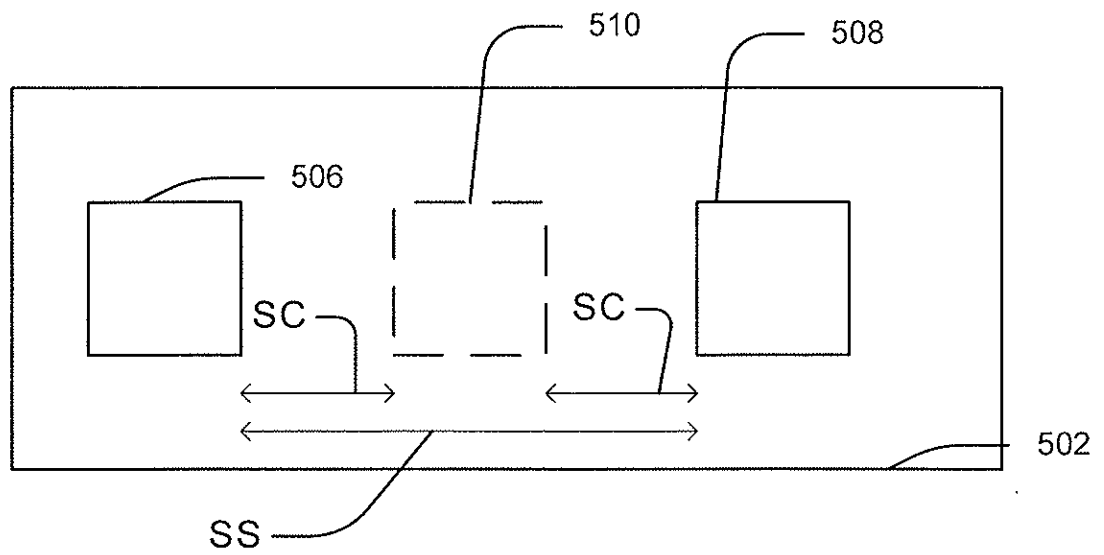


FIGURE 4D

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ODD LAYER



EVEN LAYER

FIGURE 5A

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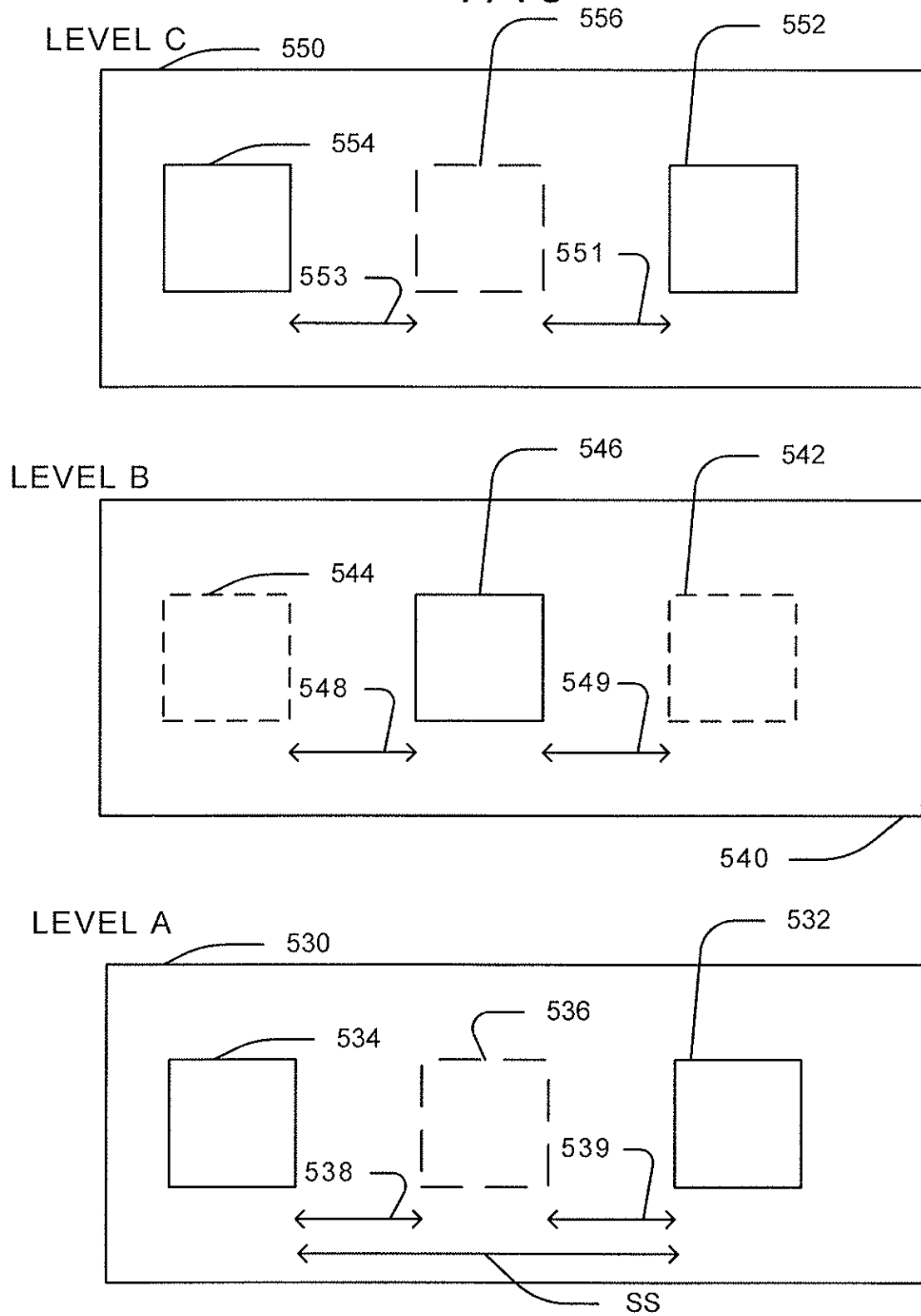


FIGURE 5B

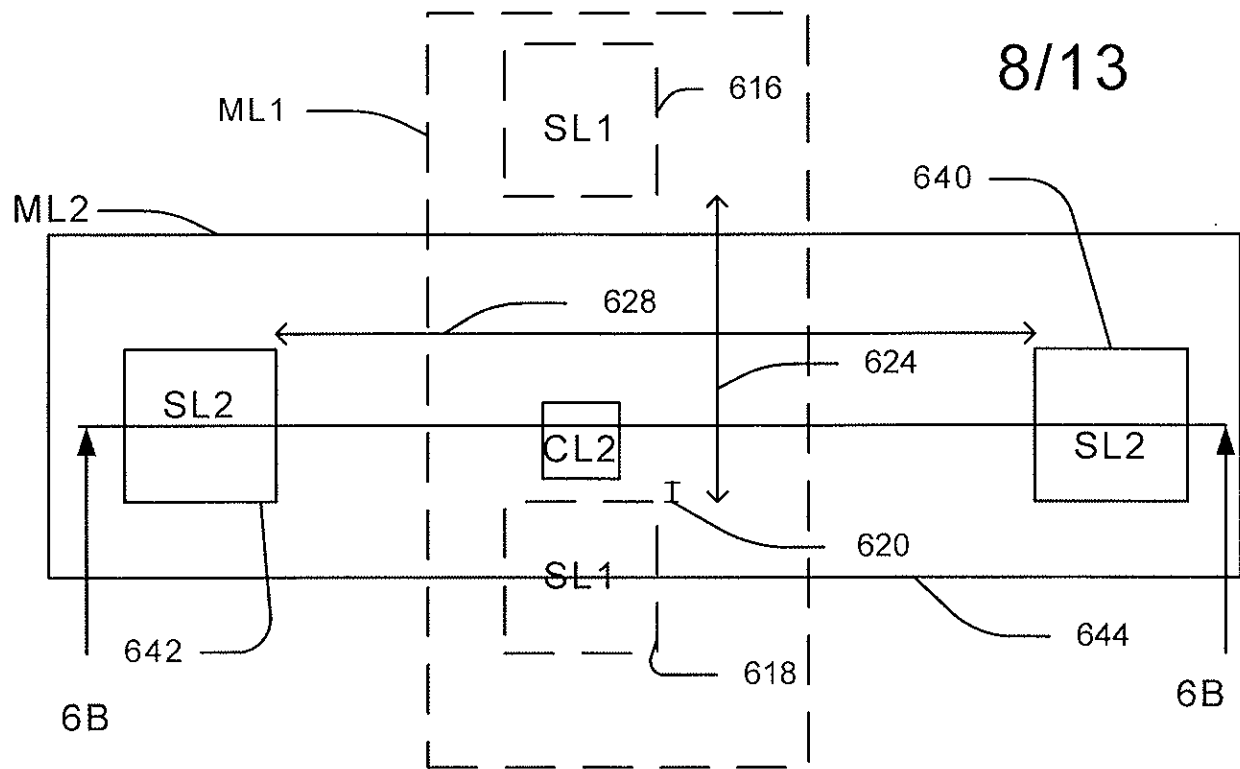


FIGURE 6A

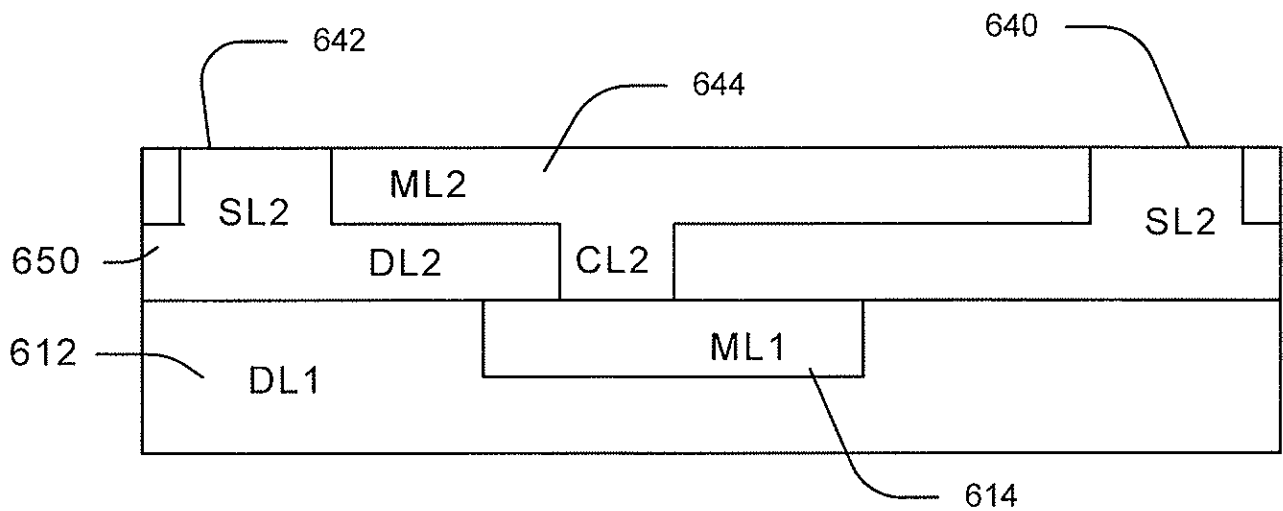


FIGURE 6B

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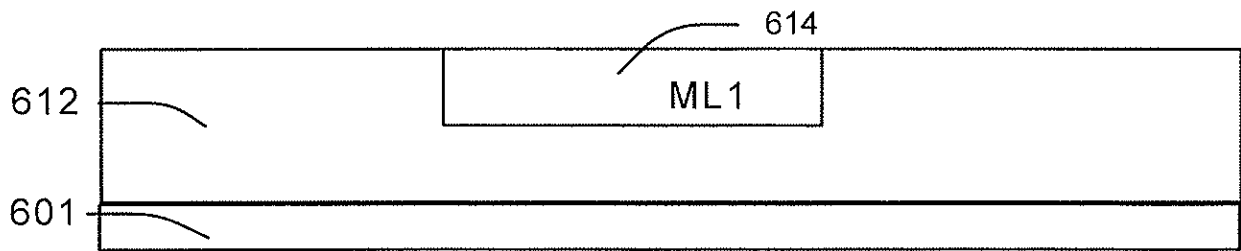


FIGURE 6C

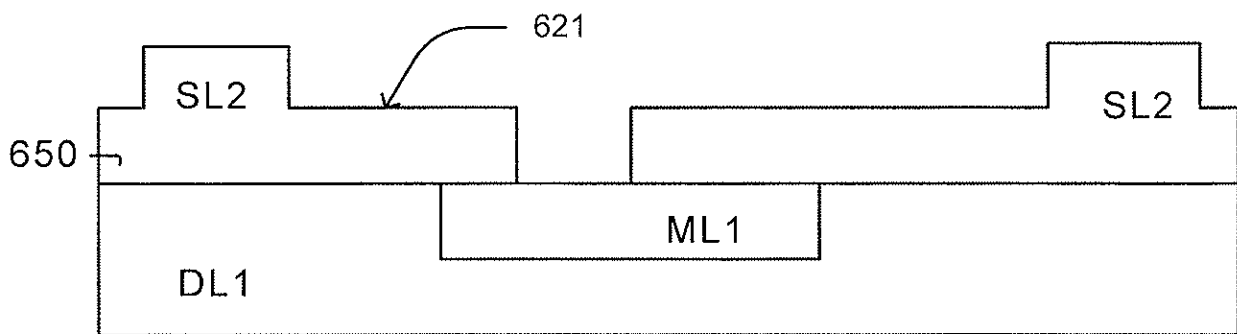


FIGURE 6D

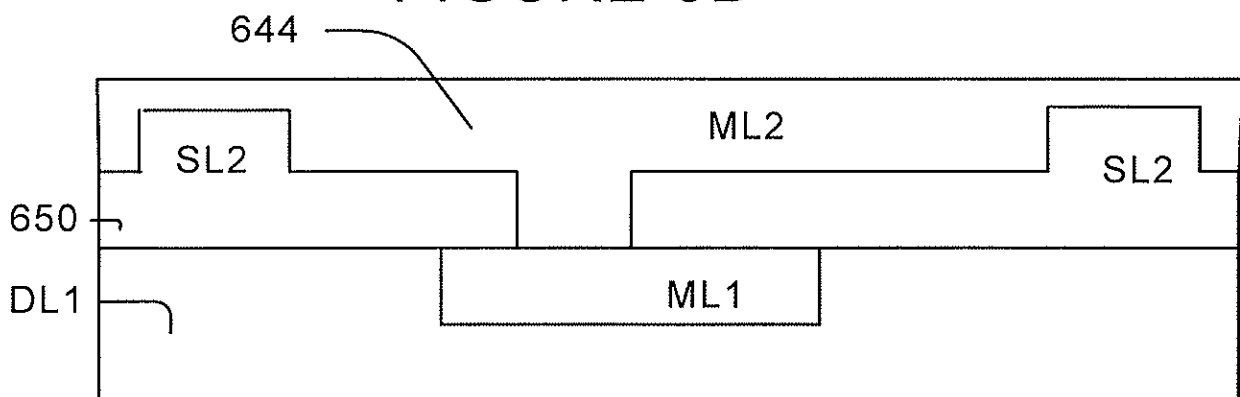


FIGURE 6E

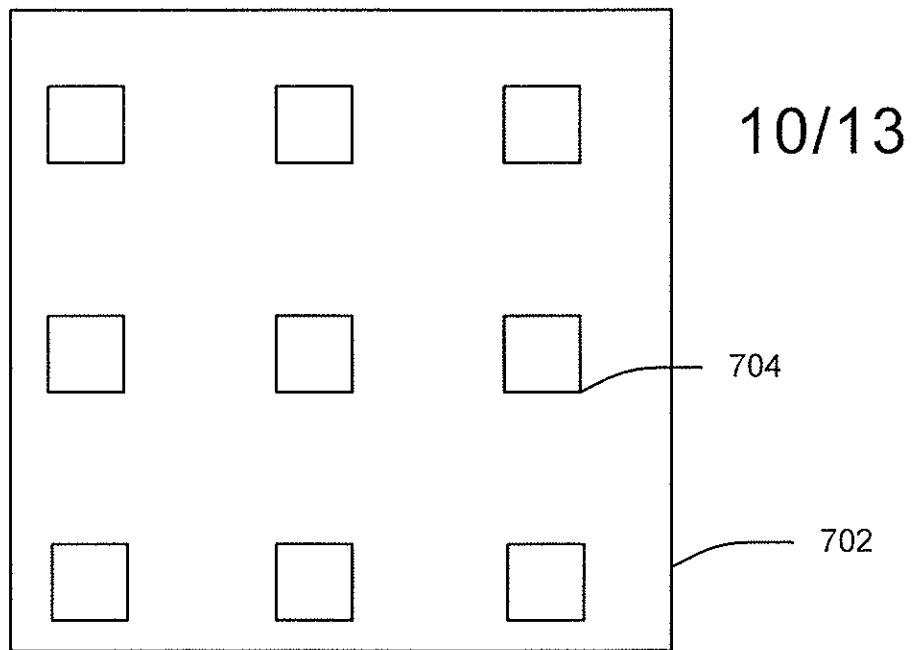


FIGURE 7A

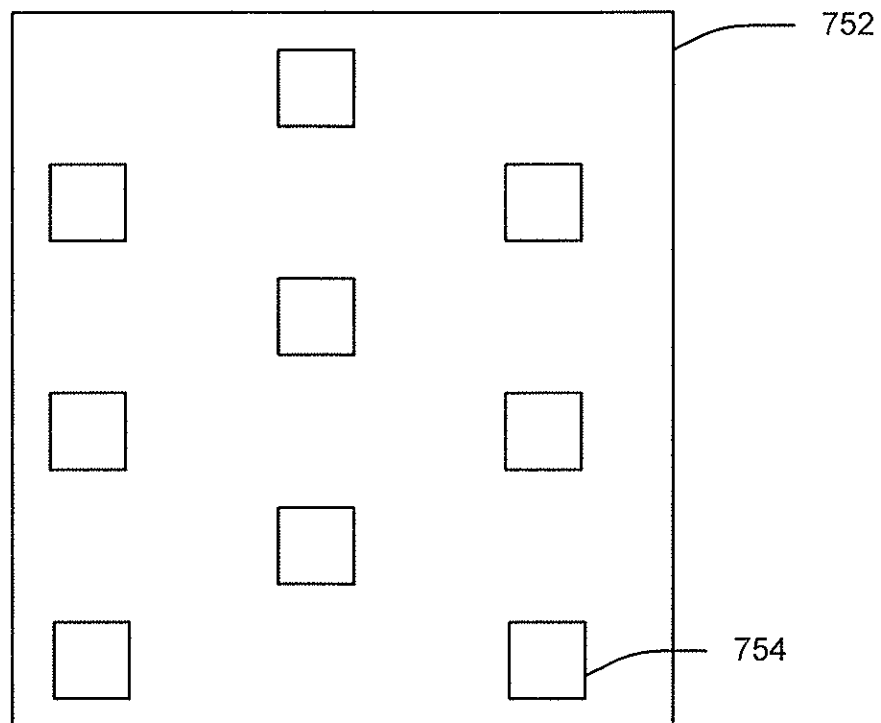


FIGURE 7B

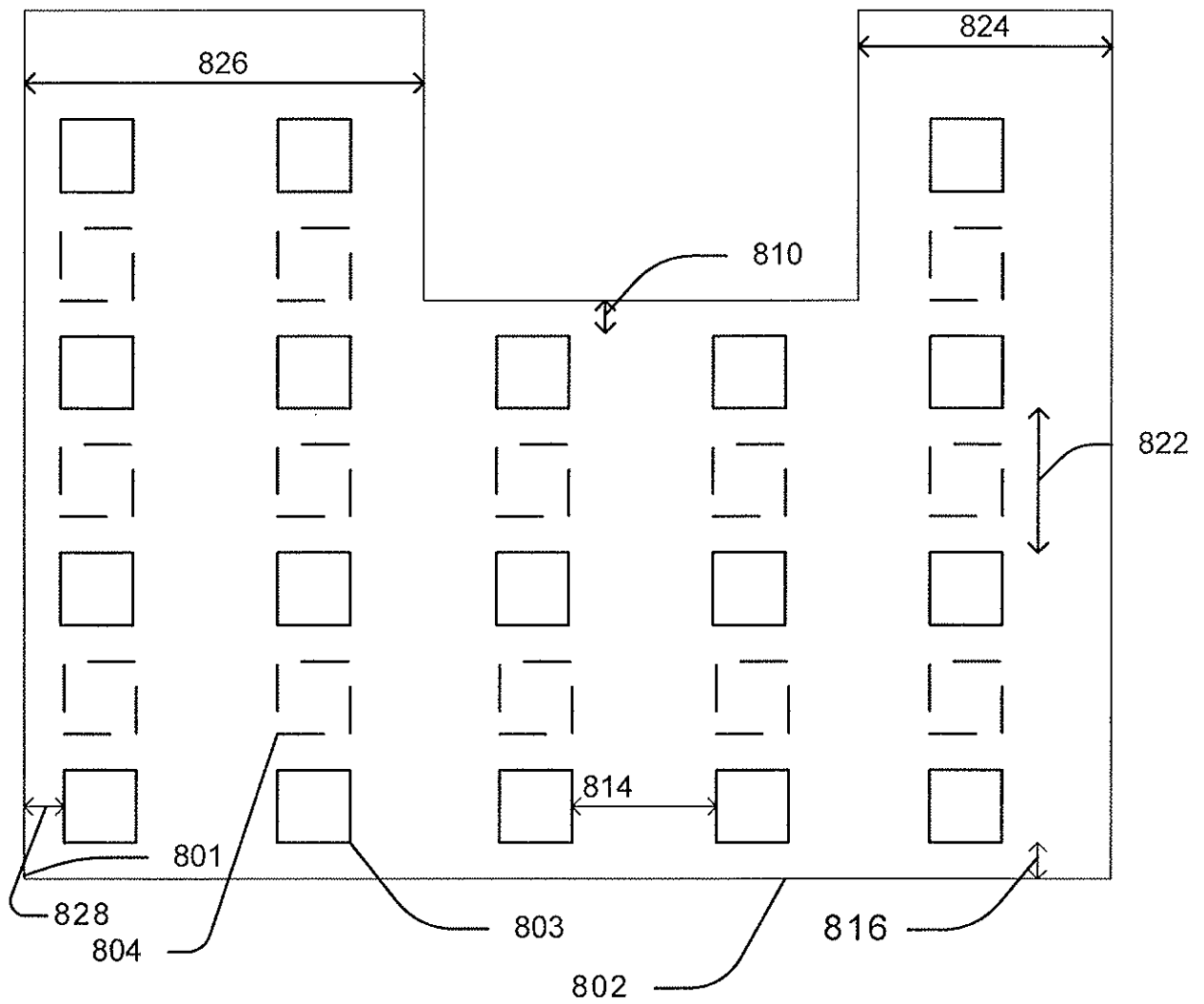


FIGURE 8

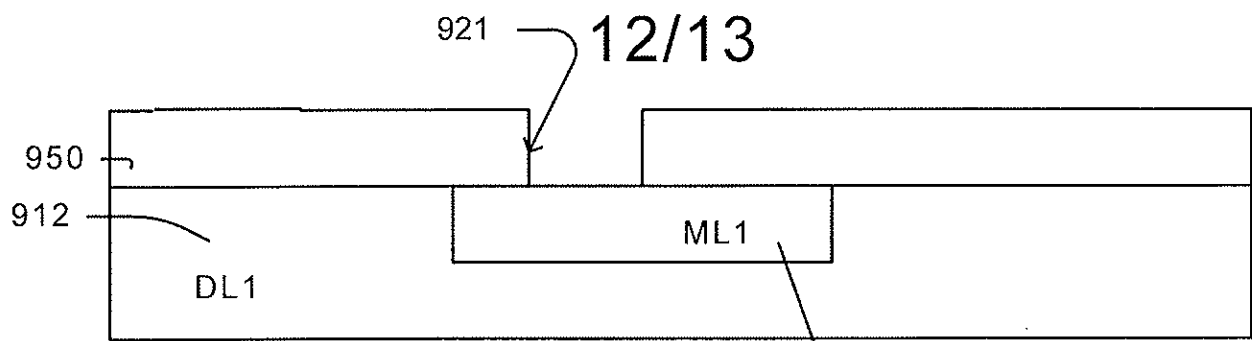


FIGURE 9A

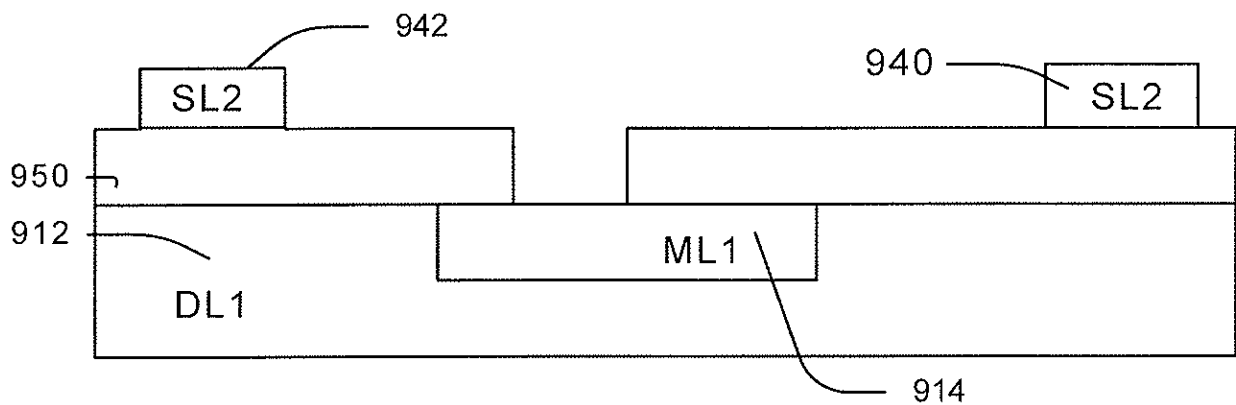


FIGURE 9B

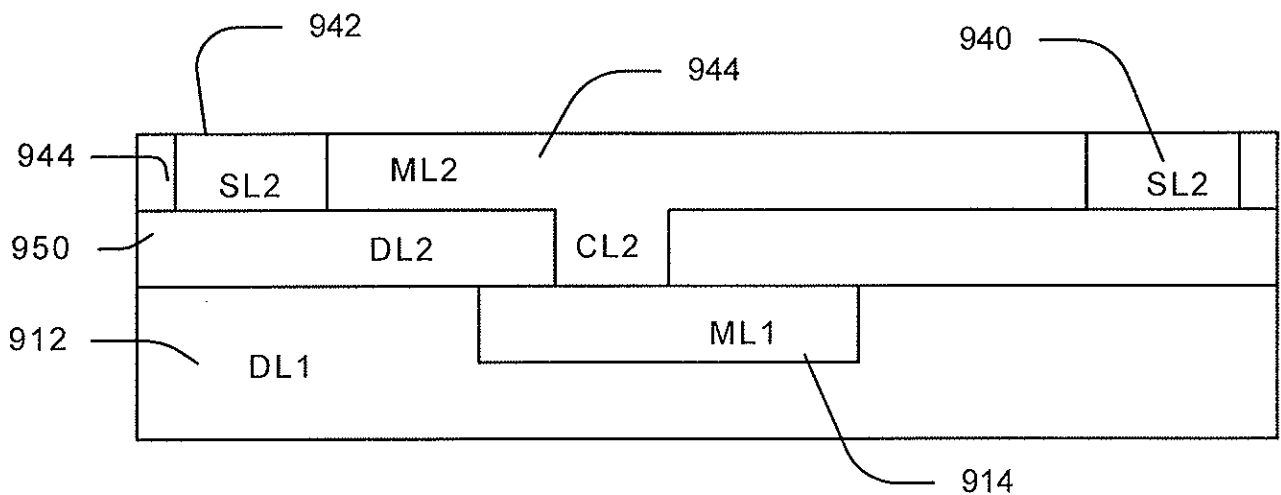


FIGURE 9C

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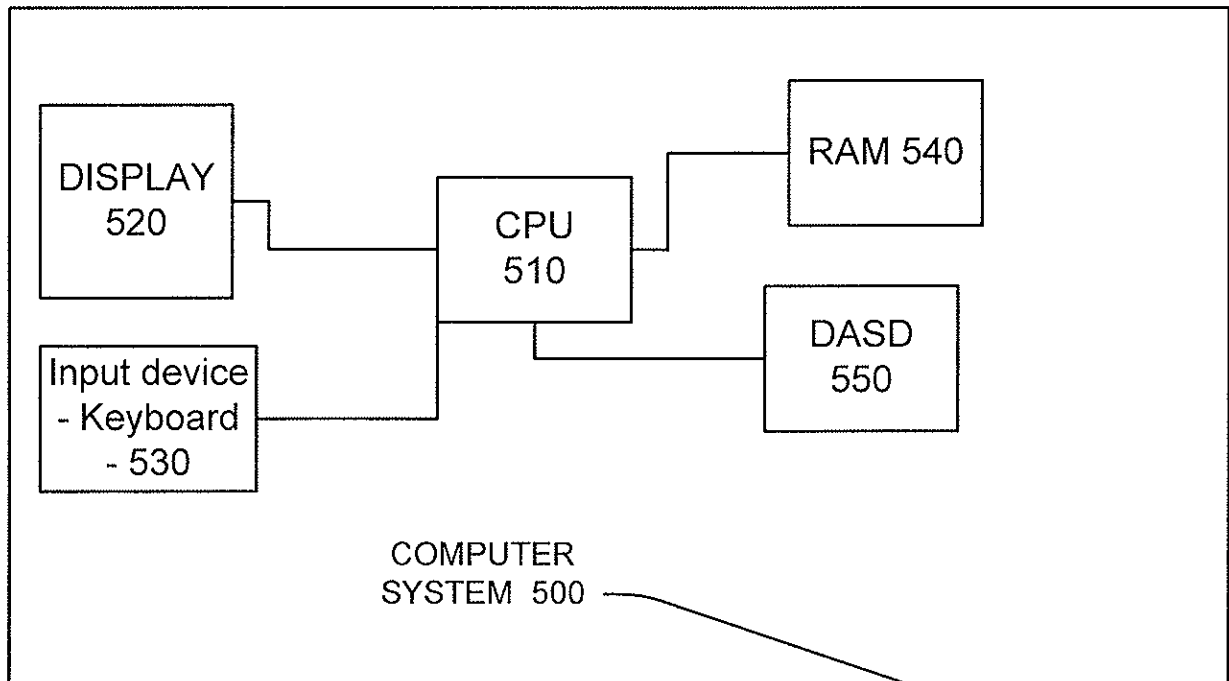


FIGURE 10