SYSTEM FOR COMPENSATING CROSSTALK IN LCDS

ABSTRACT

A method and apparatus for compensating crosstalk in liquid crystal displays (1 and 2) is disclosed which involves applying boost voltages (V5- and V0+) to the rows (40) and columns (38) of the display (1 and 2) in proportion to the number of ON pixels in a row (40) or column (38), the number of transitions between "ON-and-OFF" or "OFF-and-ON" in each column, and the position of the pixel (Px, y) in a row (40). "Boost" voltages (V5- and V0+) are applied to each row (40) as it is being actively scanned to provide horizontal crosstalk compensation, while "boost" voltages (V5- and V0+) are applied to each column (38) during the vertical retrace interval of the display sequence to provide vertical crosstalk compensation. In a preferred embodiment, the vertical crosstalk compensation is determined during the vertical retrace interval over several frames.