

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
11 March 2004 (11.03.2004)

PCT

(10) International Publication Number
WO 2004/021194 A2

(51) International Patent Classification⁷: **G06F 12/02**,
12/08, 12/06

(21) International Application Number:
PCT/US2003/026243

(22) International Filing Date: 22 August 2003 (22.08.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
10/229,655 27 August 2002 (27.08.2002) US

(71) Applicant: INTEL CORPORATION [US/US]; 2200
Mission College Boulevard, Santa Clara, CA 95052 (US).

(72) Inventor: DODD, James; 4561 Barnett Ranch Road,
Shingle Springs, CA 95682 (US).

(74) Agent: MALLIE, Michael, J.; Blakely, Sokoloff, Taylor
& Zafman, 7th Floor, 12400 Wilshire Boulevard, Los An-
geles, CA 90025 (US).

(81) Designated States (*national*): AE, AG, AL, AM, AT, AU,
AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU,
CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH,
GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC,
LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW,
MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC,
SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA,
UG, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (*regional*): ARIPO patent (GH, GM,
KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW),
Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE,
ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO,
SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM,
GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— without international search report and to be republished
upon receipt of that report

For two-letter codes and other abbreviations, refer to the "Guid-
ance Notes on Codes and Abbreviations" appearing at the begin-
ning of each regular issue of the PCT Gazette.

(54) Title: PRECHARGE SUGGESTION

(57) Abstract: Machine-readable media, methods, and apparatus are described which process memory transactions. In some em-
bodiments, a processor requests an external memory controller to close a storage location of a memory associated with a first memory
transaction based upon a relationship between the first memory transaction and a second memory transaction.

WO 2004/021194 A2