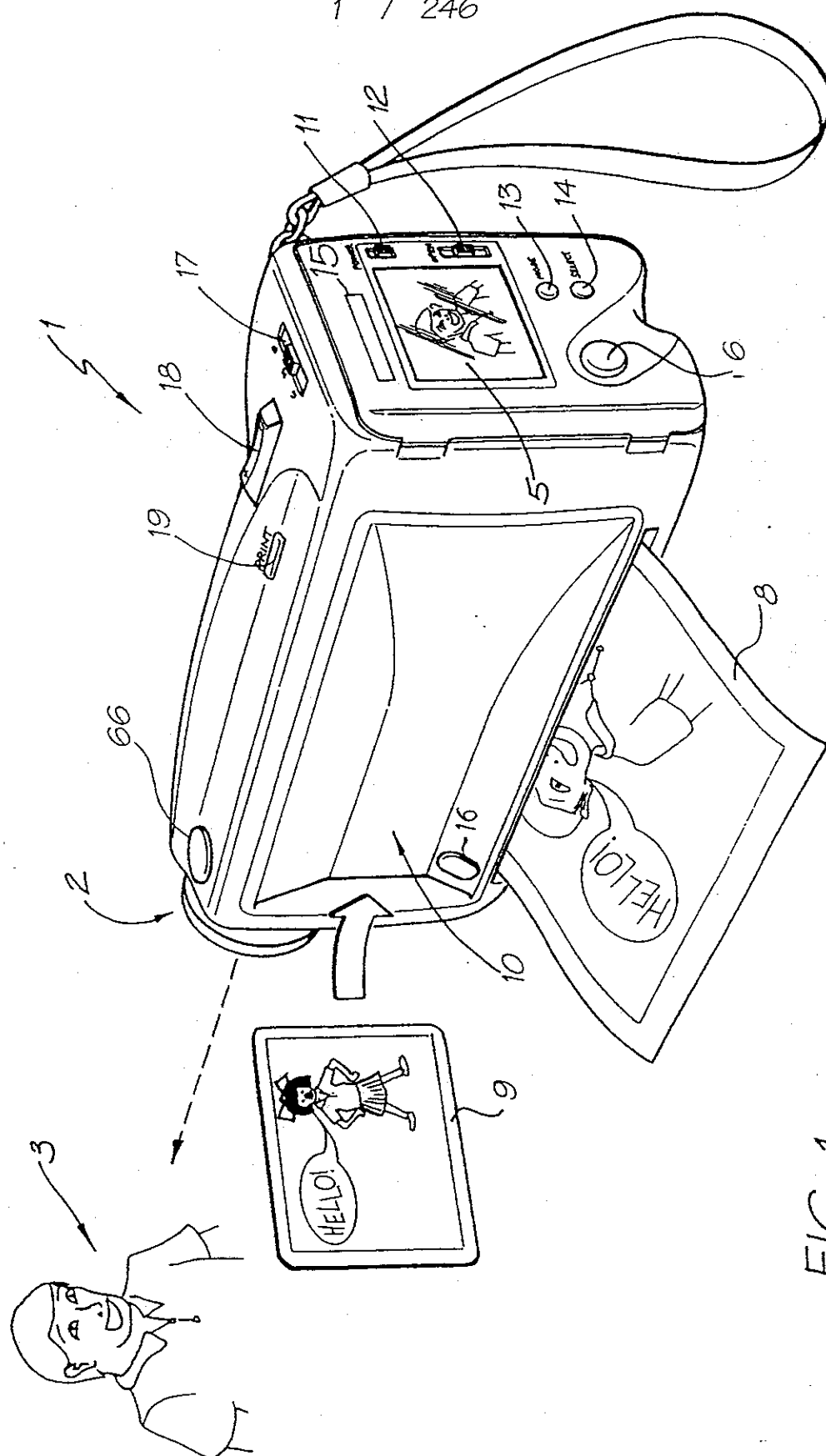


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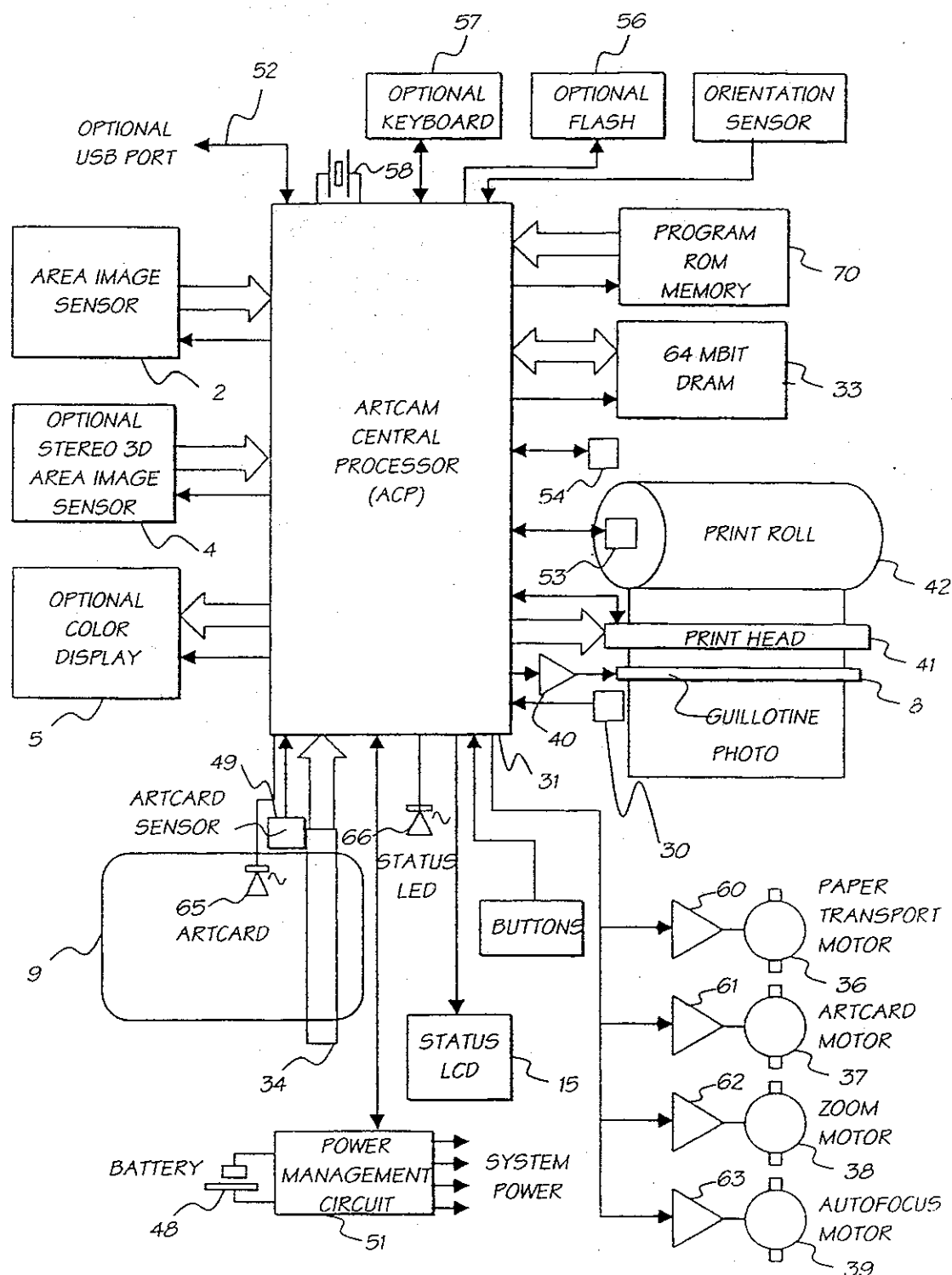
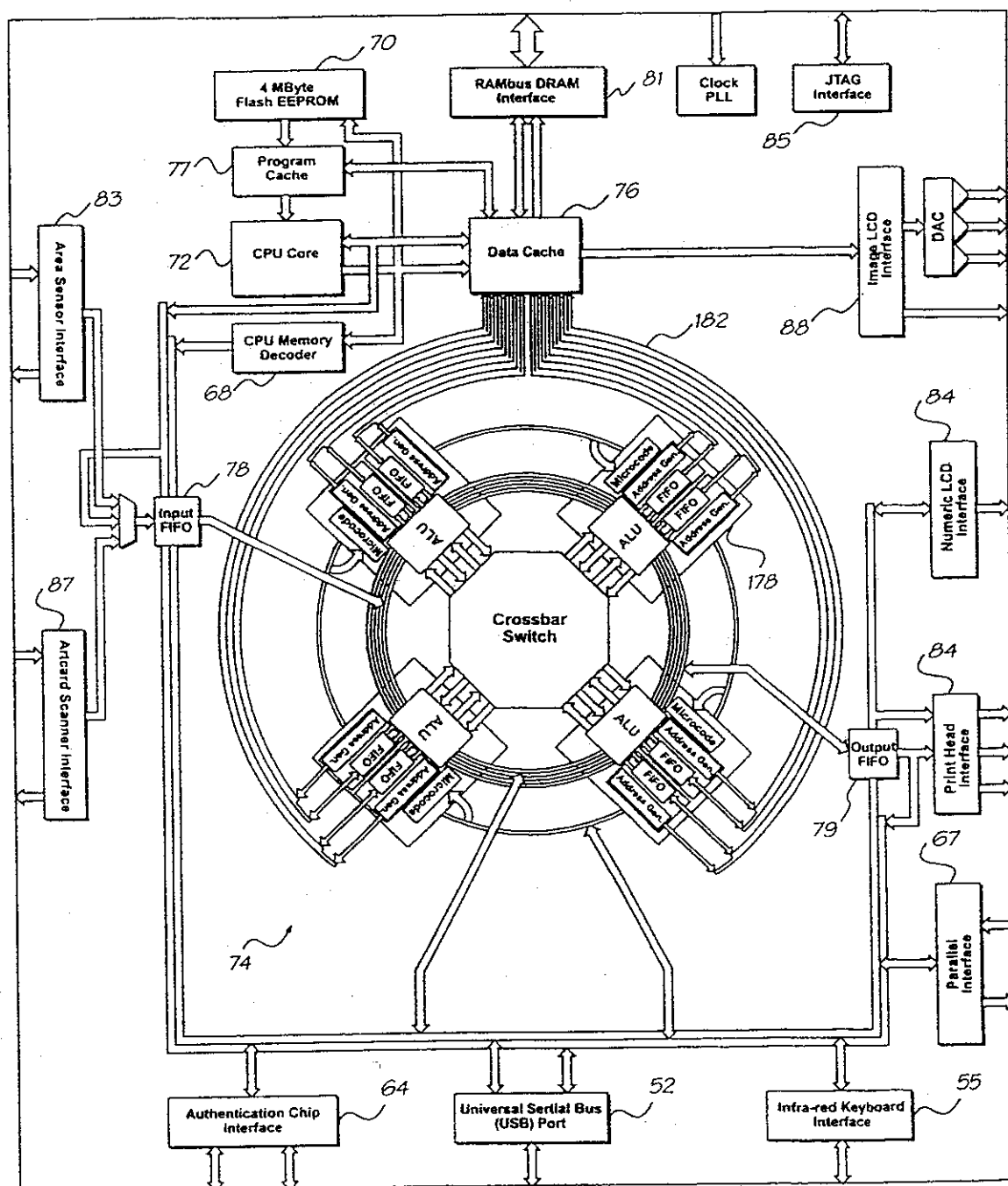


FIG. 2

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31

FIG. 3

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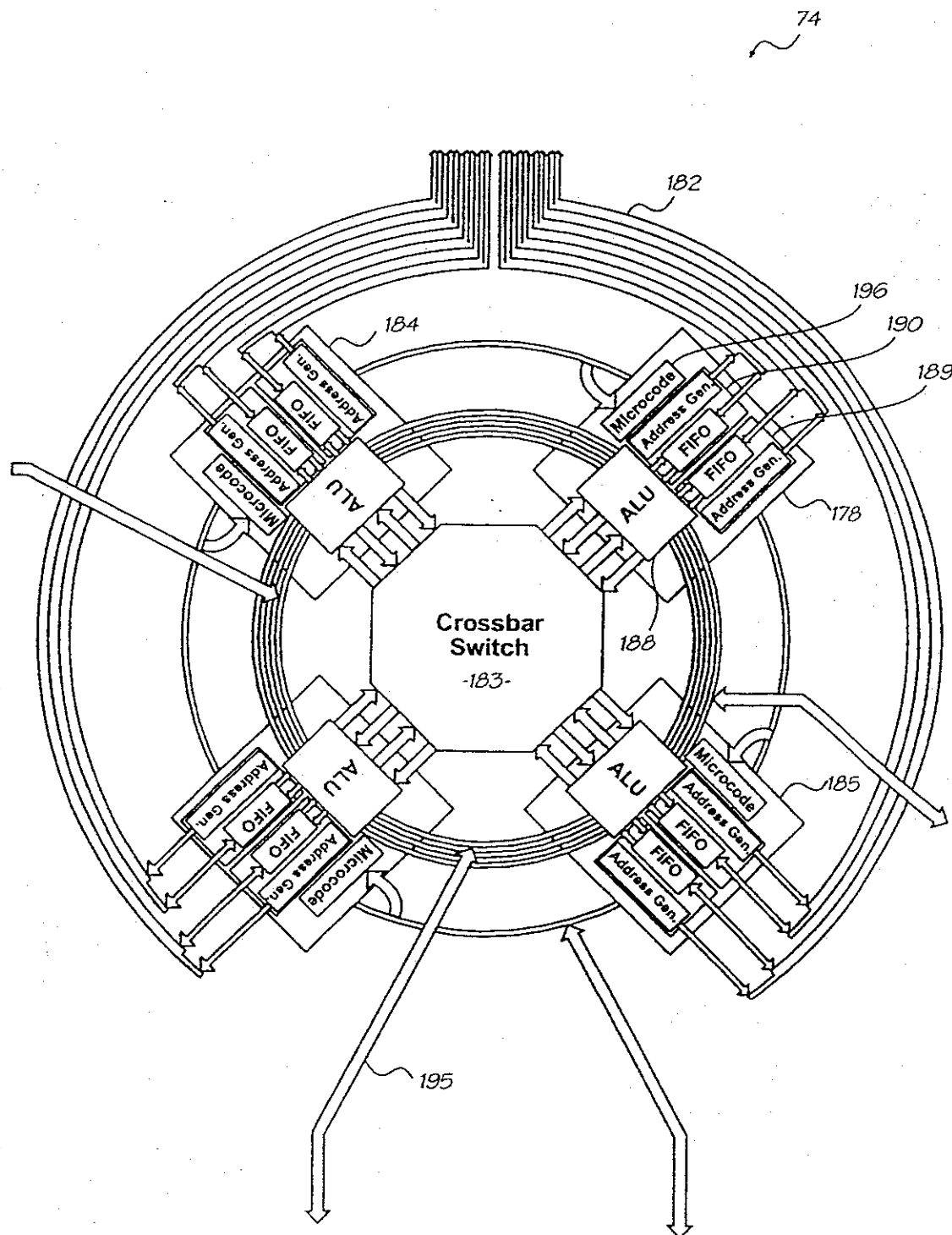


FIG. 3(a)

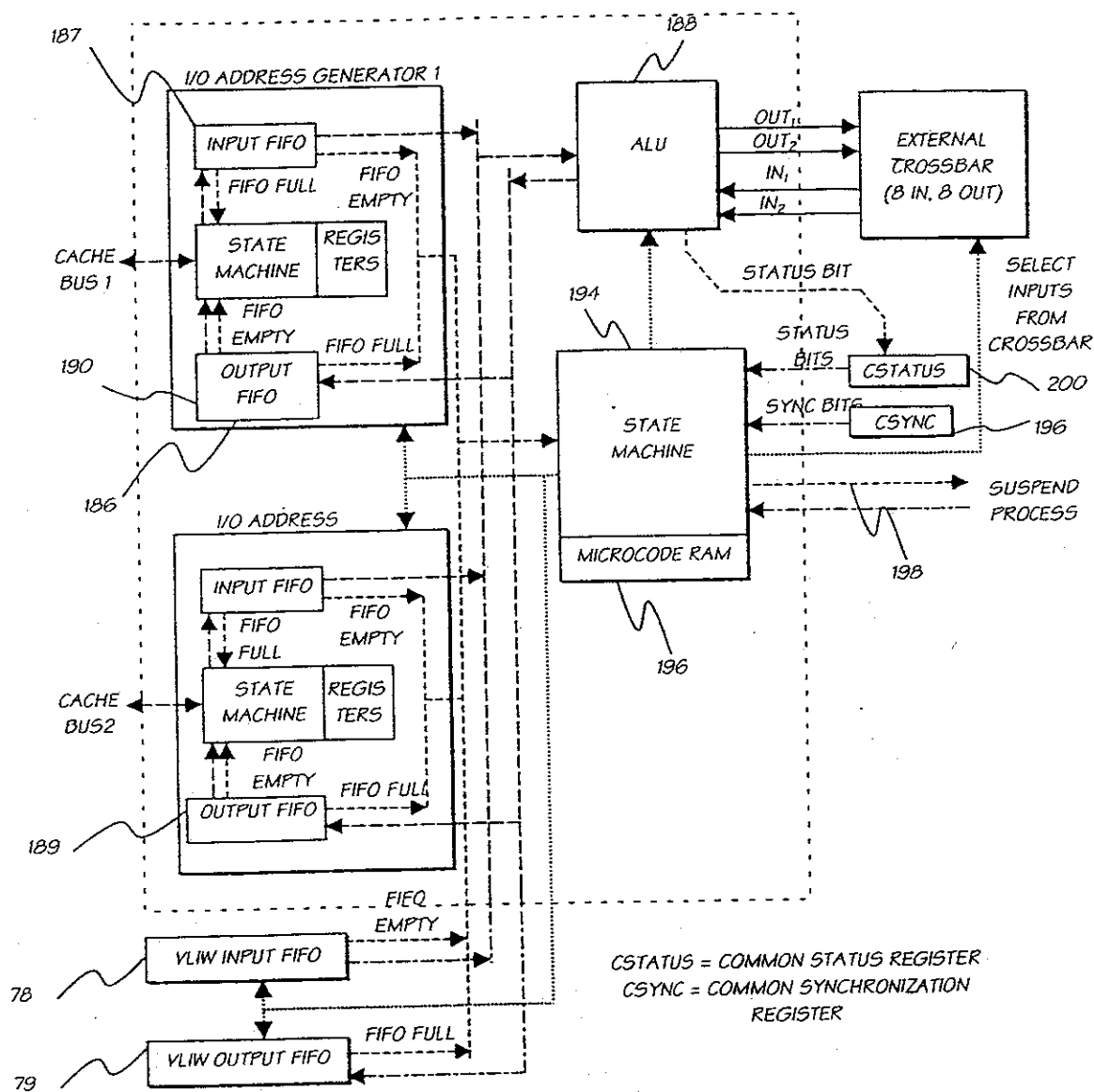


FIG. 4

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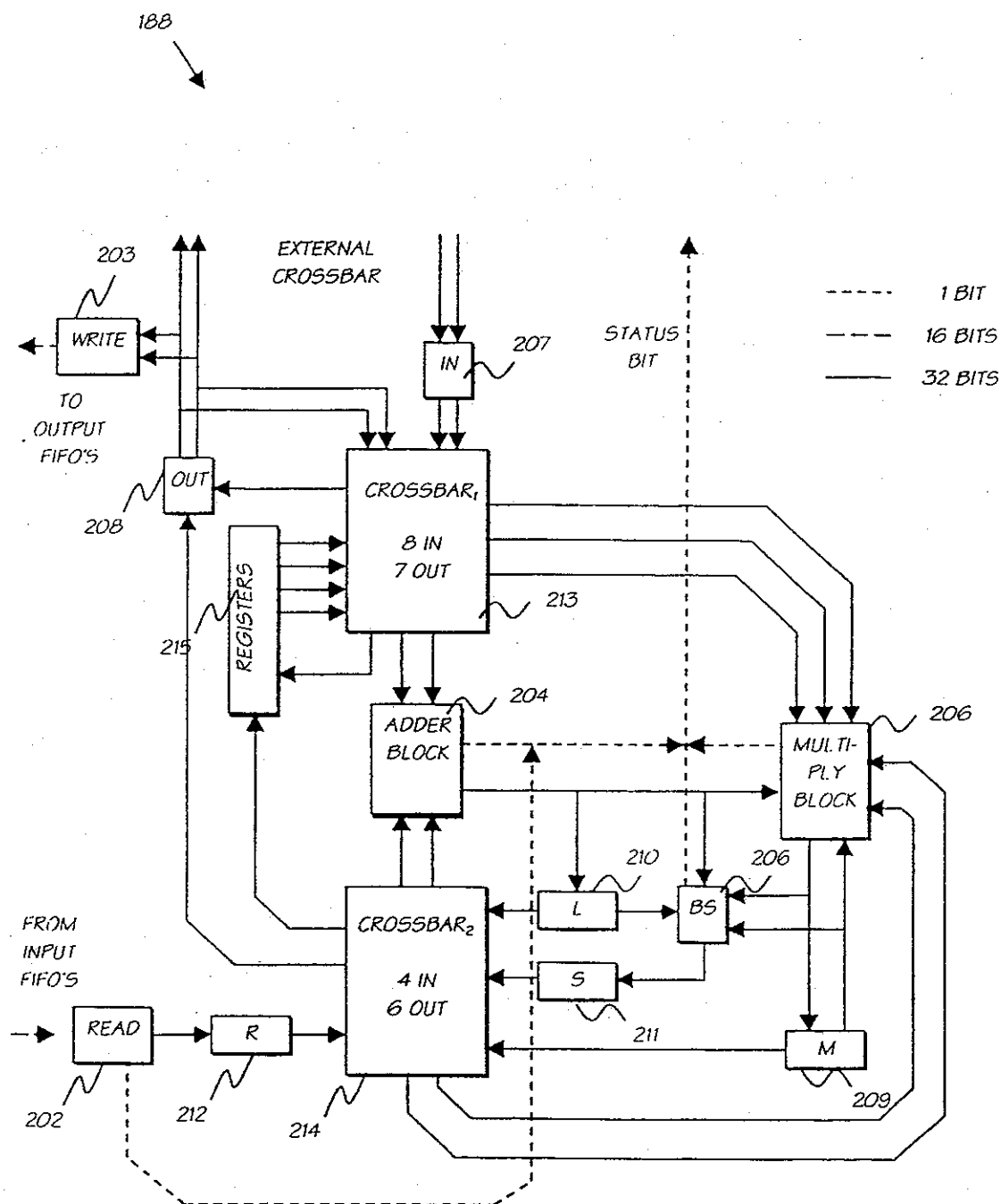


FIG. 5

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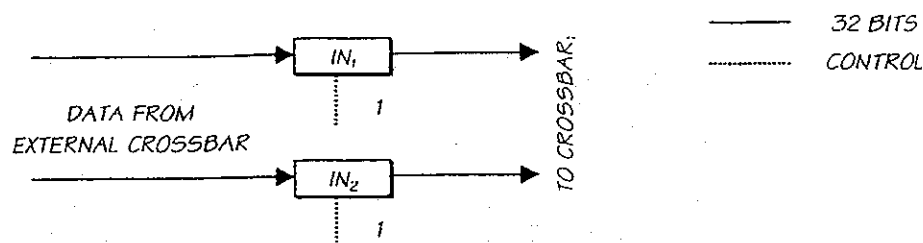


FIG. 6

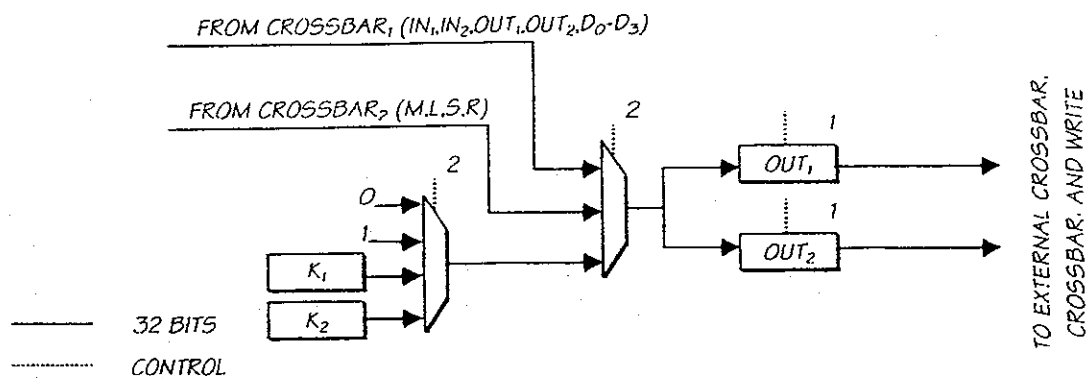


FIG. 7

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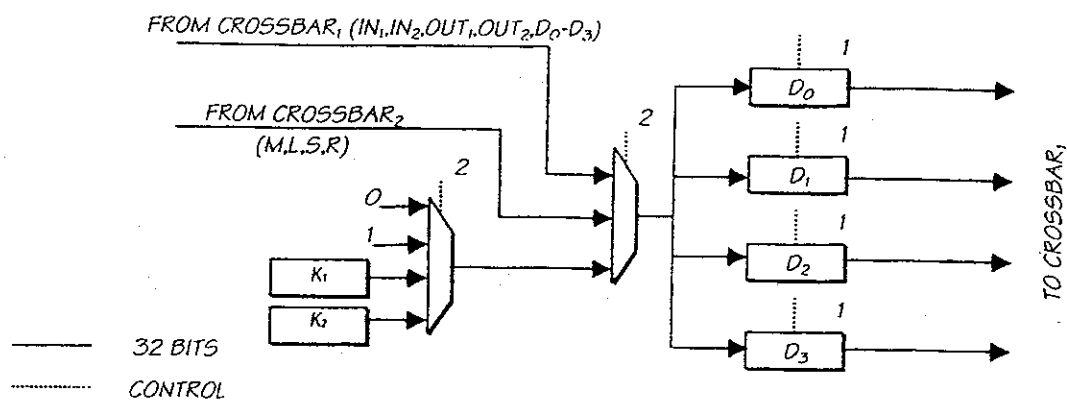


FIG. 8

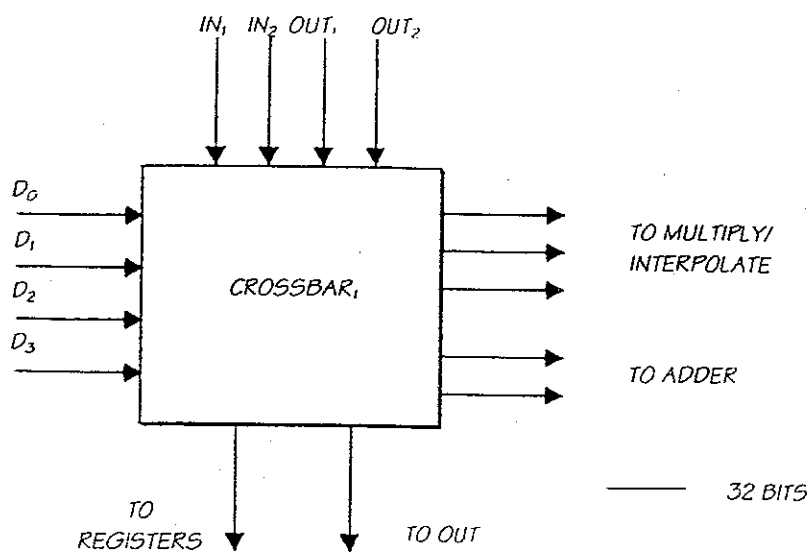


FIG. 9

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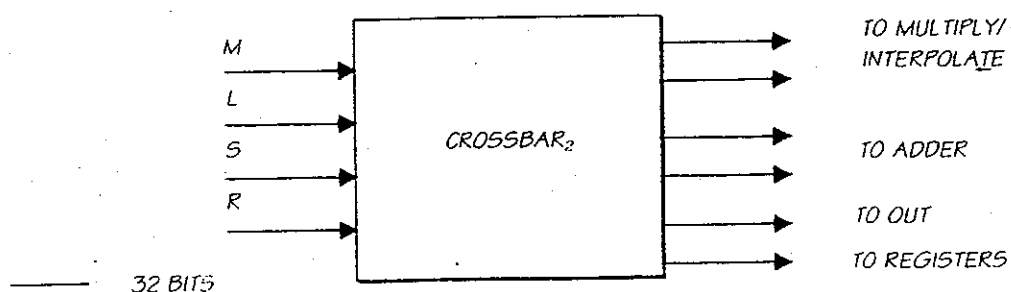


FIG. 10

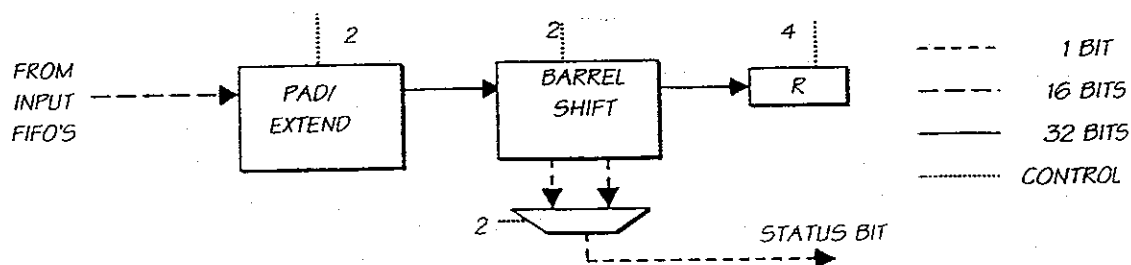


FIG. 11

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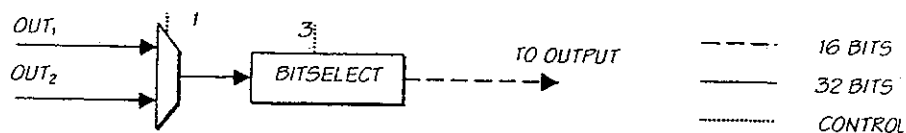


FIG. 12

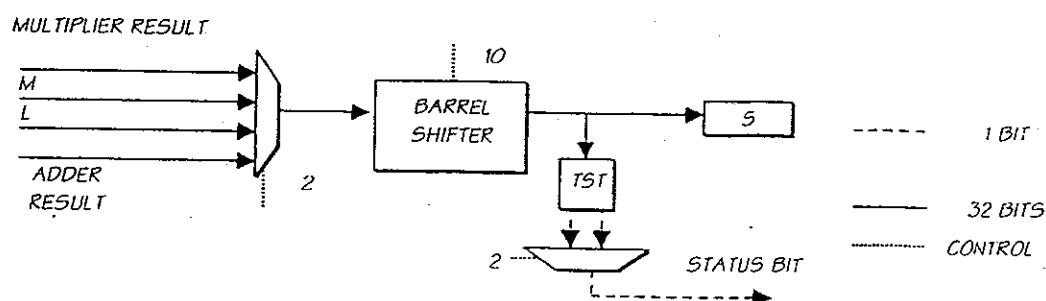


FIG. 13

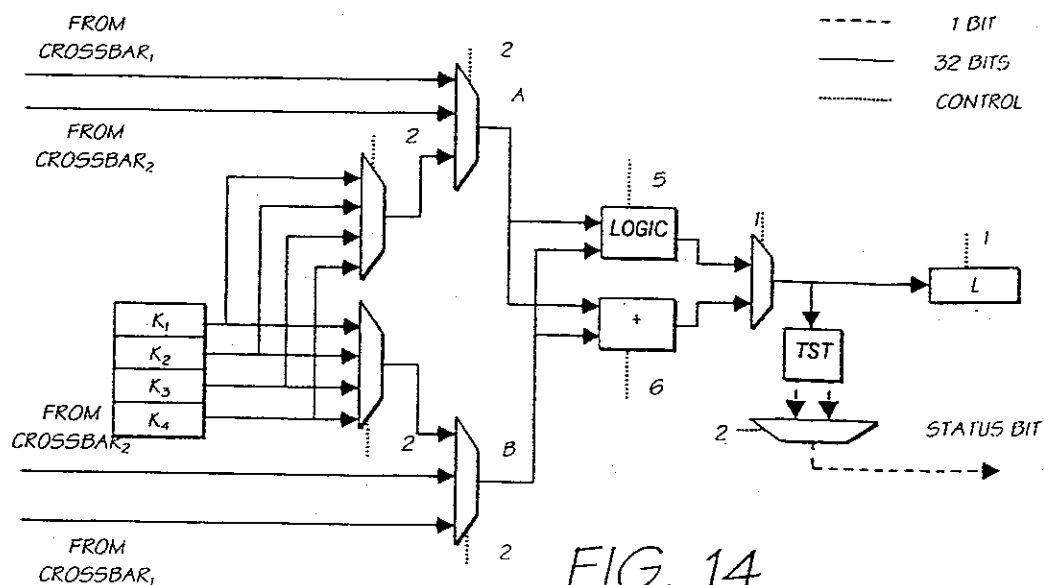


FIG. 14

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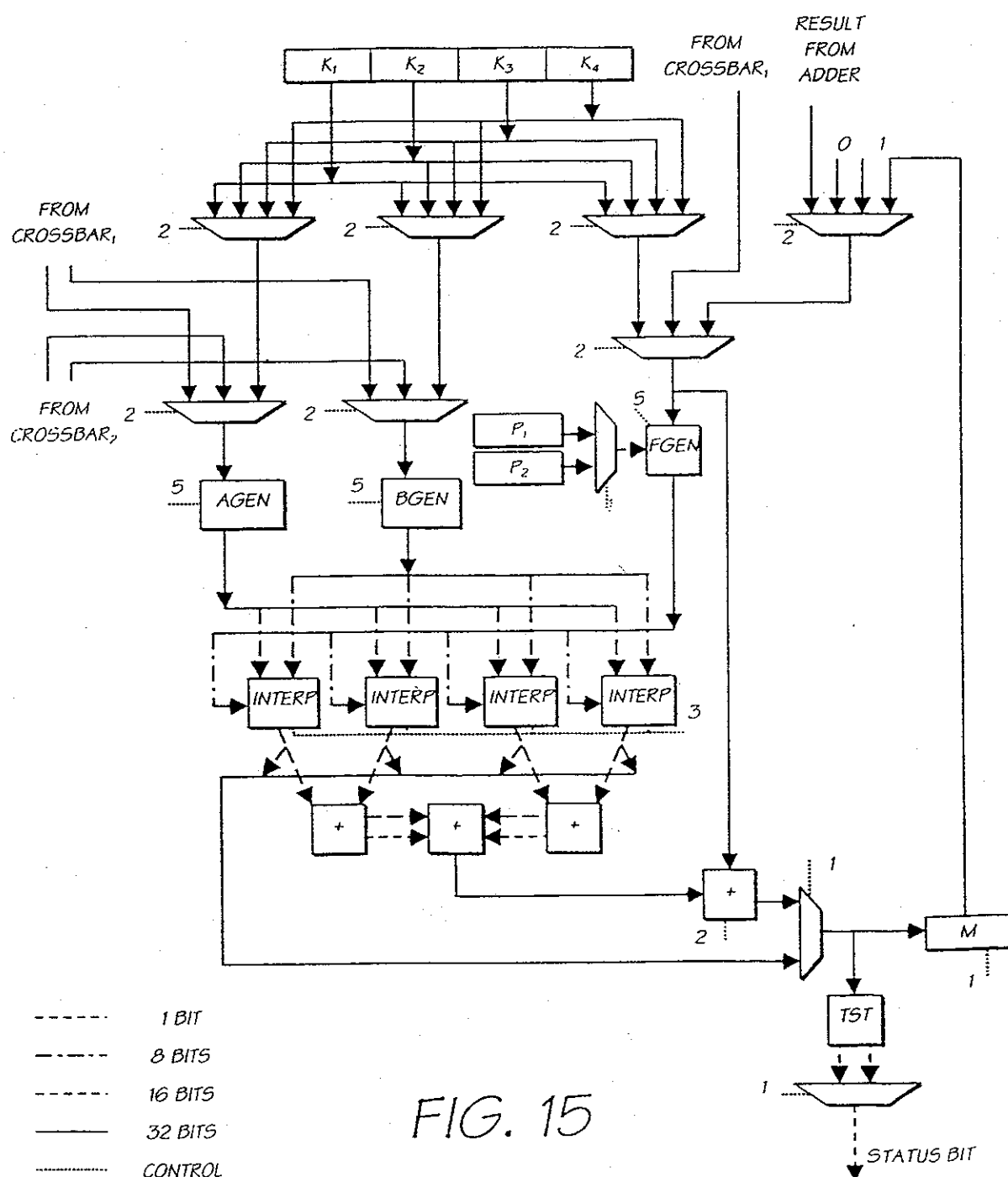


FIG. 15

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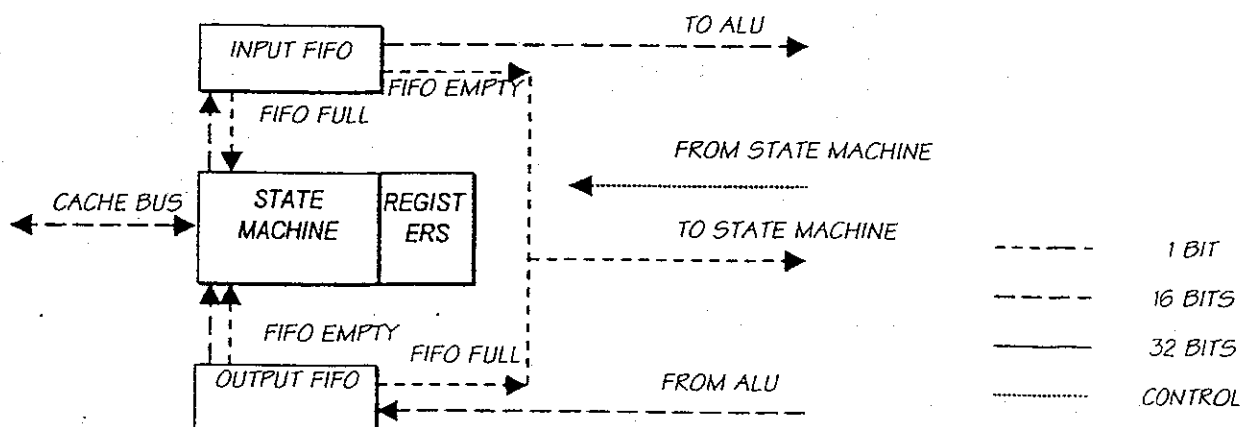


FIG. 16

ORDER OF PIXELS PRESENTED BY A SEQUENTIAL READ ITERATOR
ON A 4 X 2 IMAGE WITH PADDING.

0	1	2	3	
4	5	6	7	

FIG. 17

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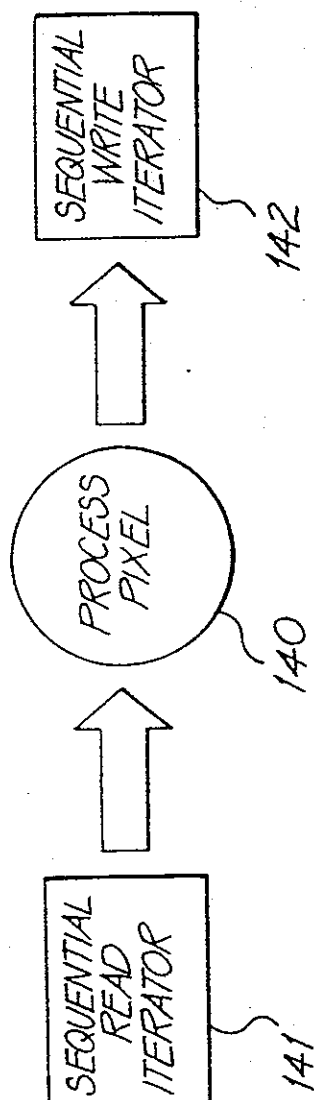
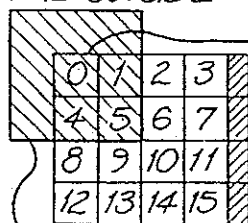


FIG. 18

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A 3x3 BOX VIEW TRAVERSES THE PIXELS IN ORDER 0,1,2,3,4,5,6,7,8 ETC,
PLACING A 3x3 BOX CENTERED OVER EACH PIXEL...

3x3 BOX VIEW OF FIRST PIXEL IN
IMAGE = 9 PIXELS, 5 OF WHICH
ARE OUTSIDE THE IMAGE



150

151

152

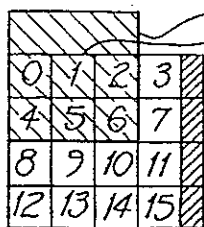
FIRST 9 PIXELS FROM THE
BOX READ ITERATOR:

IF DUPLICATION OF EDGE PIXELS IS ON:
0,0,0,0,0,1,4,4,5

IF DUPLICATION OF EDGE PIXELS IS OFF:
V,V,V,V,0,1,V,4,5
WHERE V IS CONSTANT
"OUTSIDE IMAGE" PIXEL VALUE

153

3x3 BOX VIEW OF SECOND PIXEL IN
IMAGE = 9 PIXELS, 3 OF WHICH
ARE OUTSIDE THE IMAGE



155

156

SECOND 9 PIXELS FROM THE
BOX READ ITERATOR:

IF DUPLICATION OF EDGE PIXELS IS ON:
0,1,2,0,1,2,4,5,6

IF DUPLICATION OF EDGE PIXELS IS OFF:
V,V,V,0,1,2,4,5,6
WHERE V IS CONSTANT
"OUTSIDE IMAGE" PIXEL VALUE

FIG. 19

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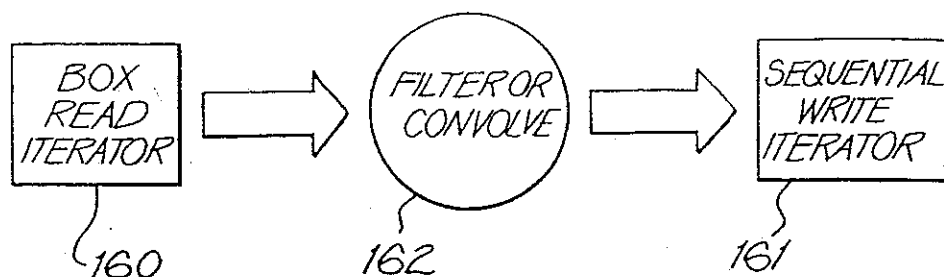
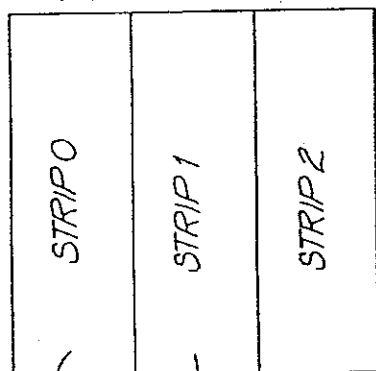


FIG. 20

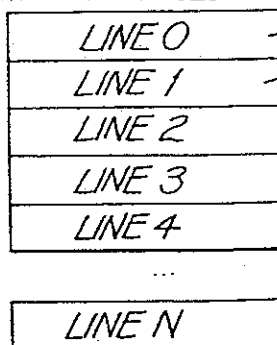
IMAGE BROKEN INTO
VERTICAL STRIPS, EACH STRIP
IS 32 PIXELS ACROSS.



169

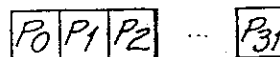
170

LINES ARE ACCESSED
LINE 0 TO LINE N
WITHIN A SINGLE STRIP.



166

PIXELS ARE ACCESSED
PIXEL 0-PIXEL 31 WITHIN
A SINGLE LINE.



167

165

FIG. 21

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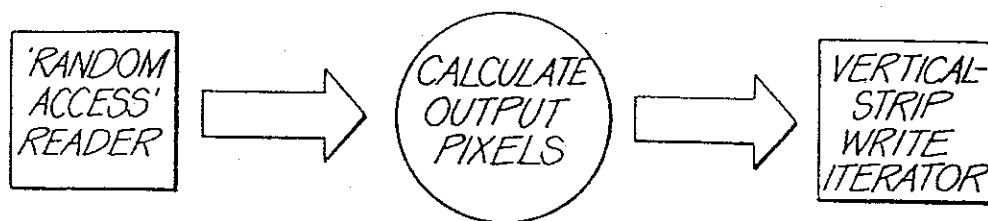


FIG. 22

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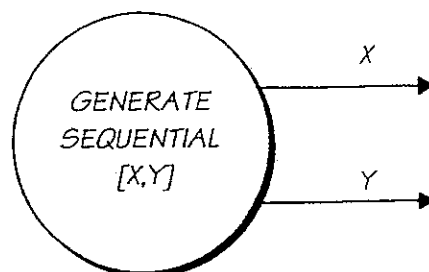


FIG. 23

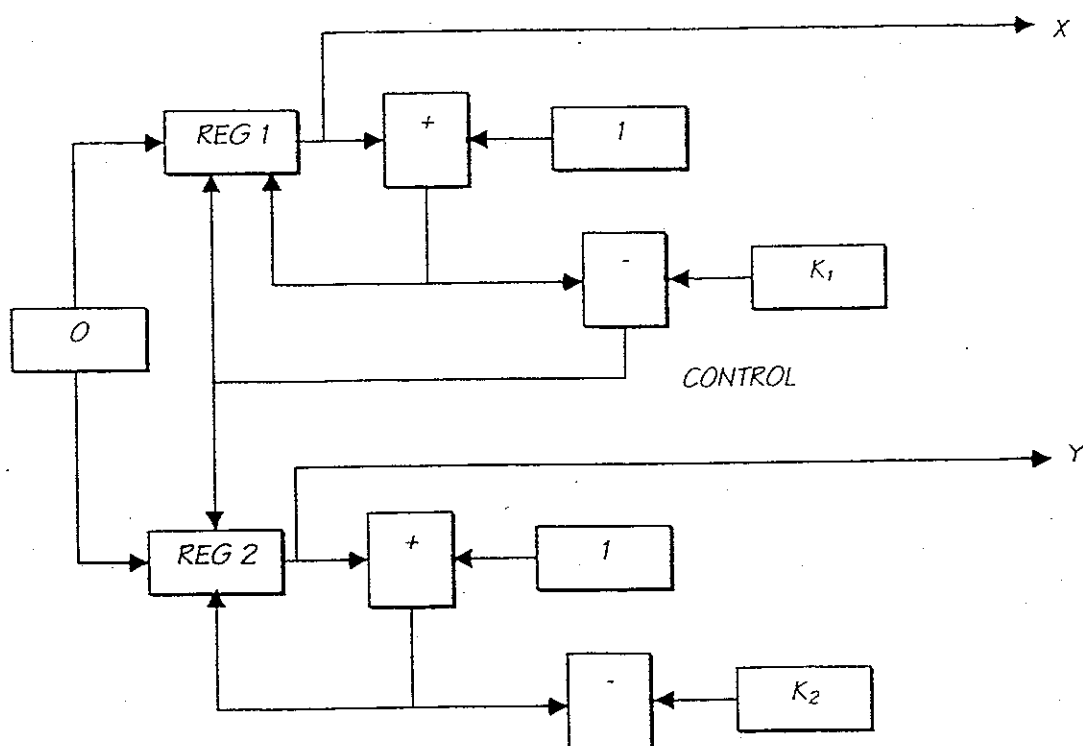


FIG. 24

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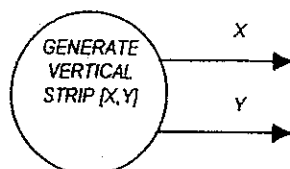


FIG. 25

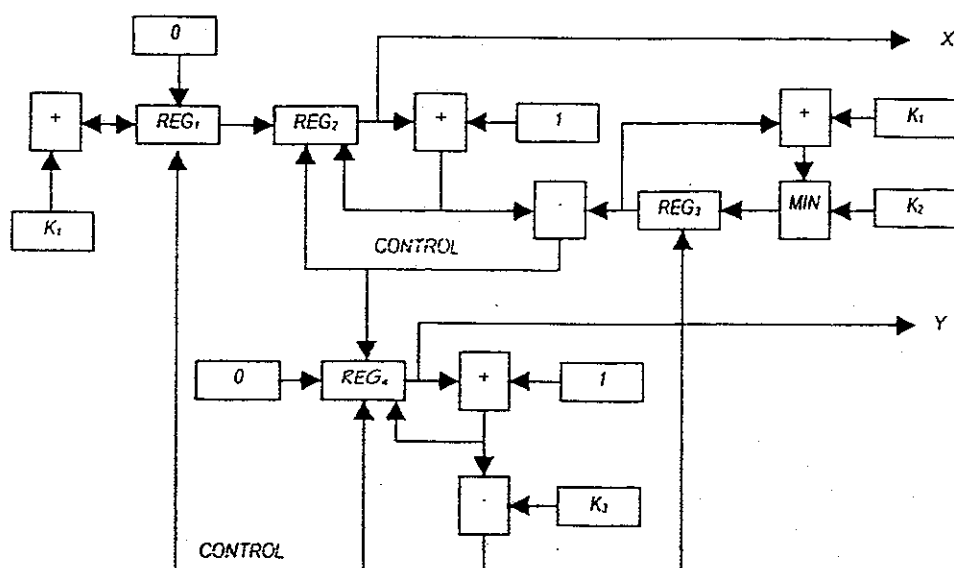
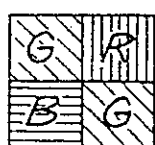
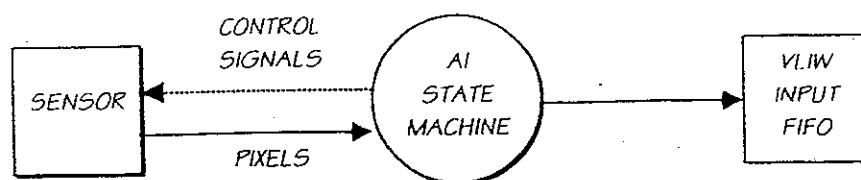


FIG. 26

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*2x2 PIXEL BLOCK FROM CCD**FIG. 27**FIG. 28*

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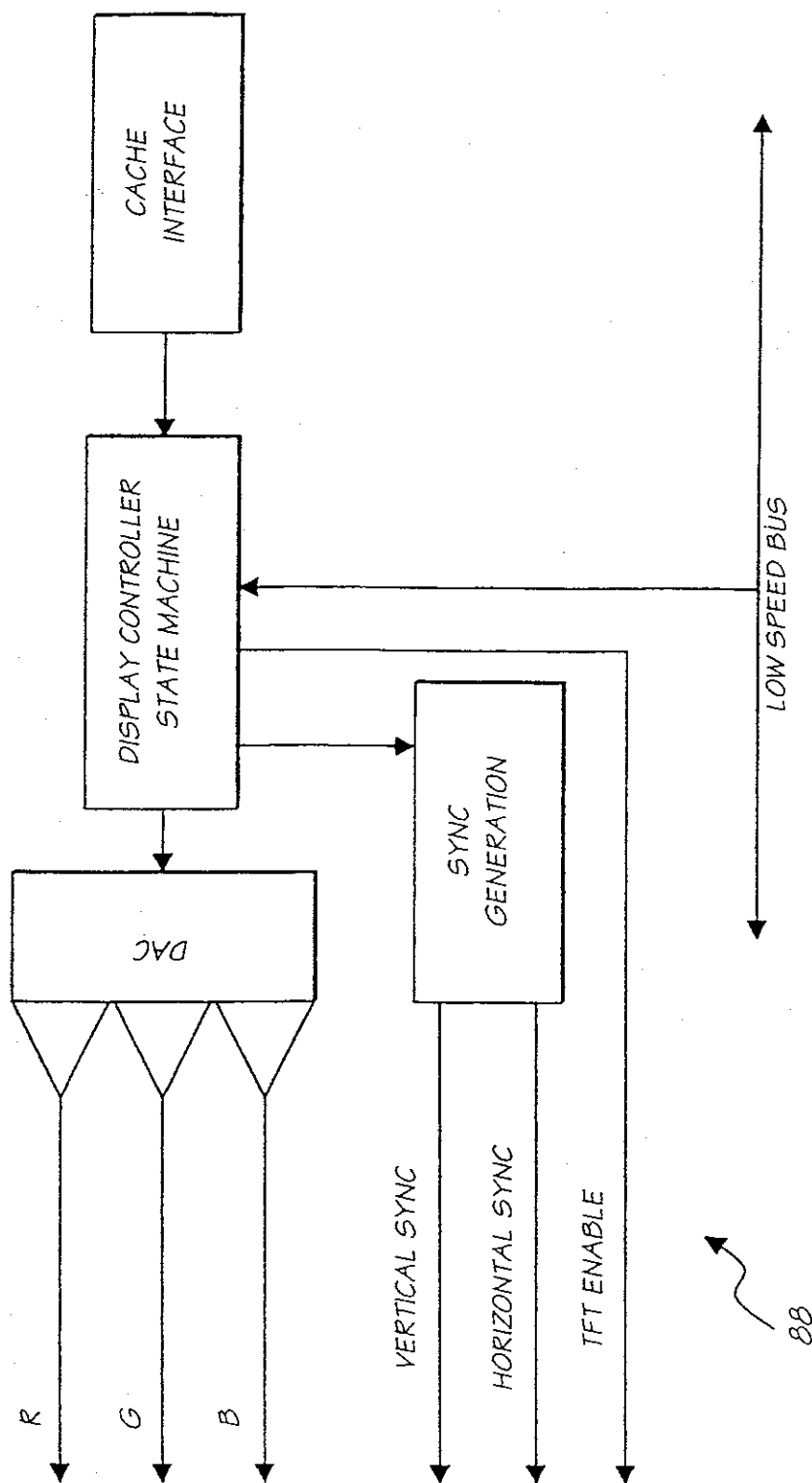


FIG. 29

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2x2 PIXEL BLOCK FROM CCD

FIG. 30

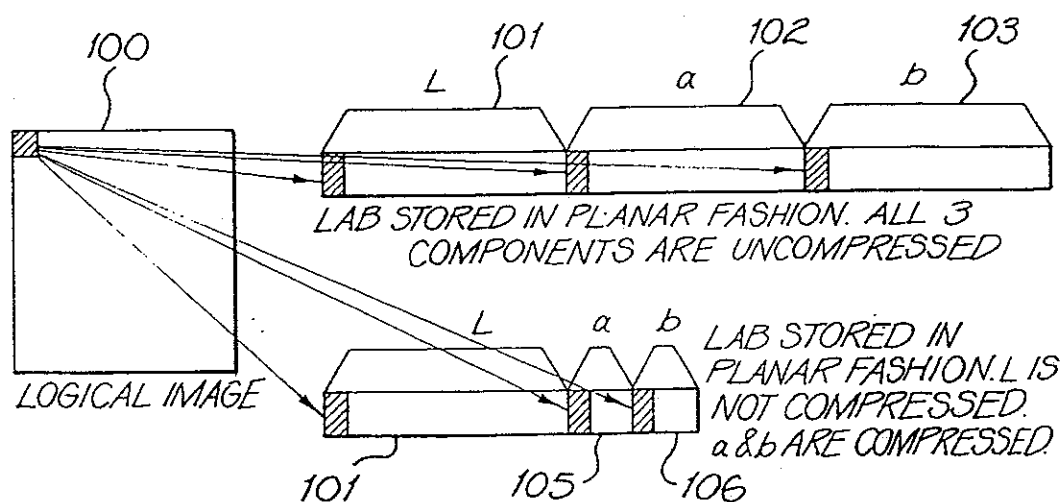


FIG. 31

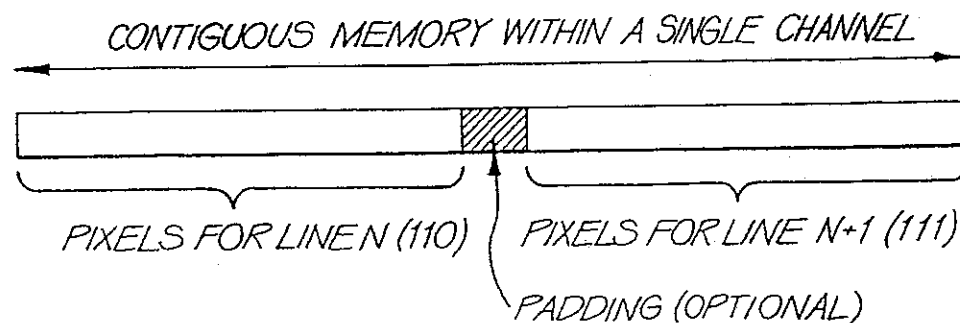


FIG. 32

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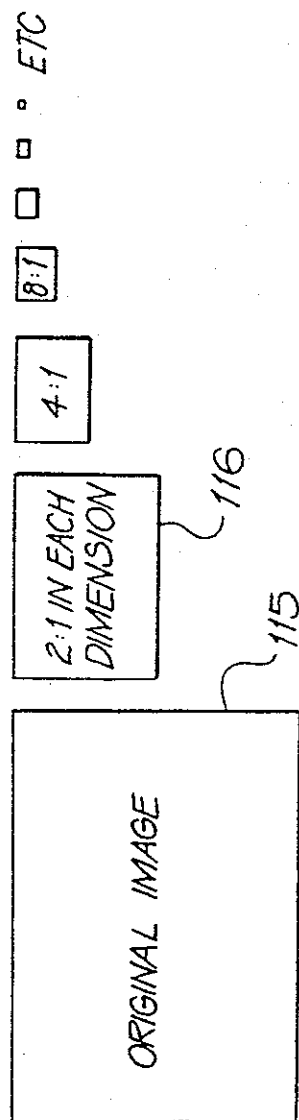


FIG. 33

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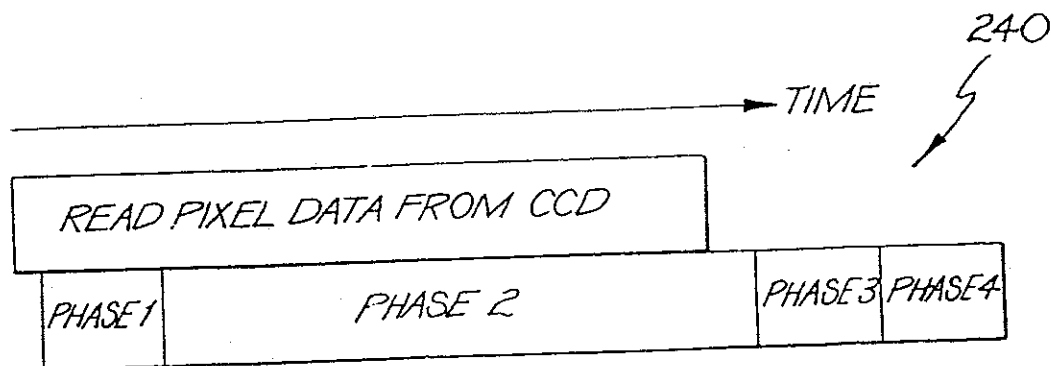


FIG. 34

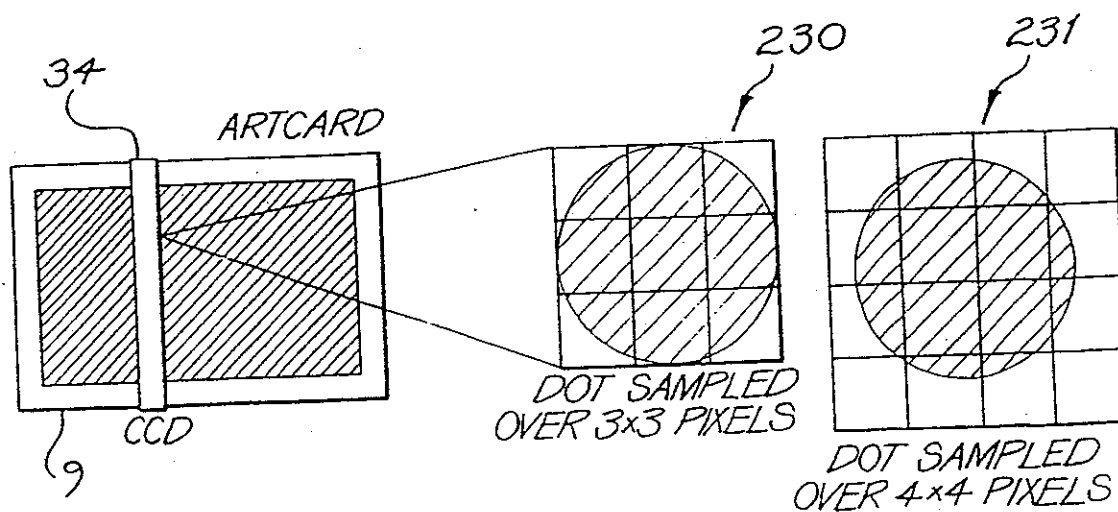


FIG. 35

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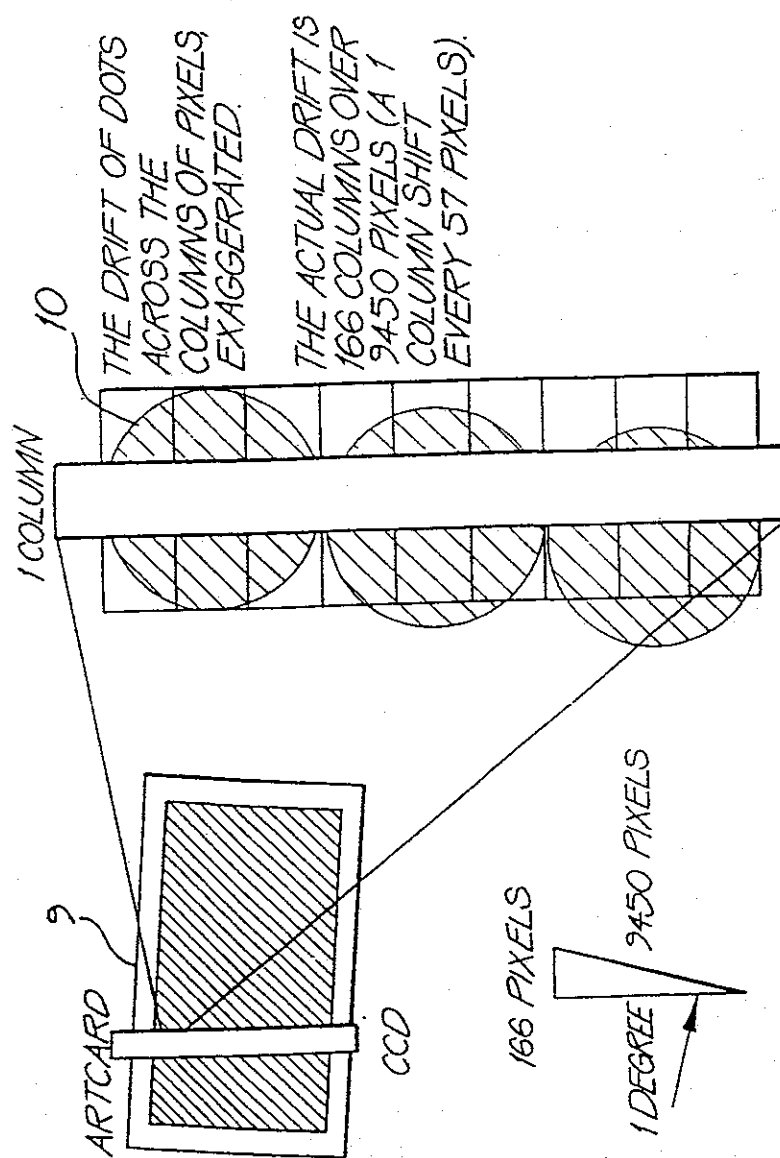


FIG. 36

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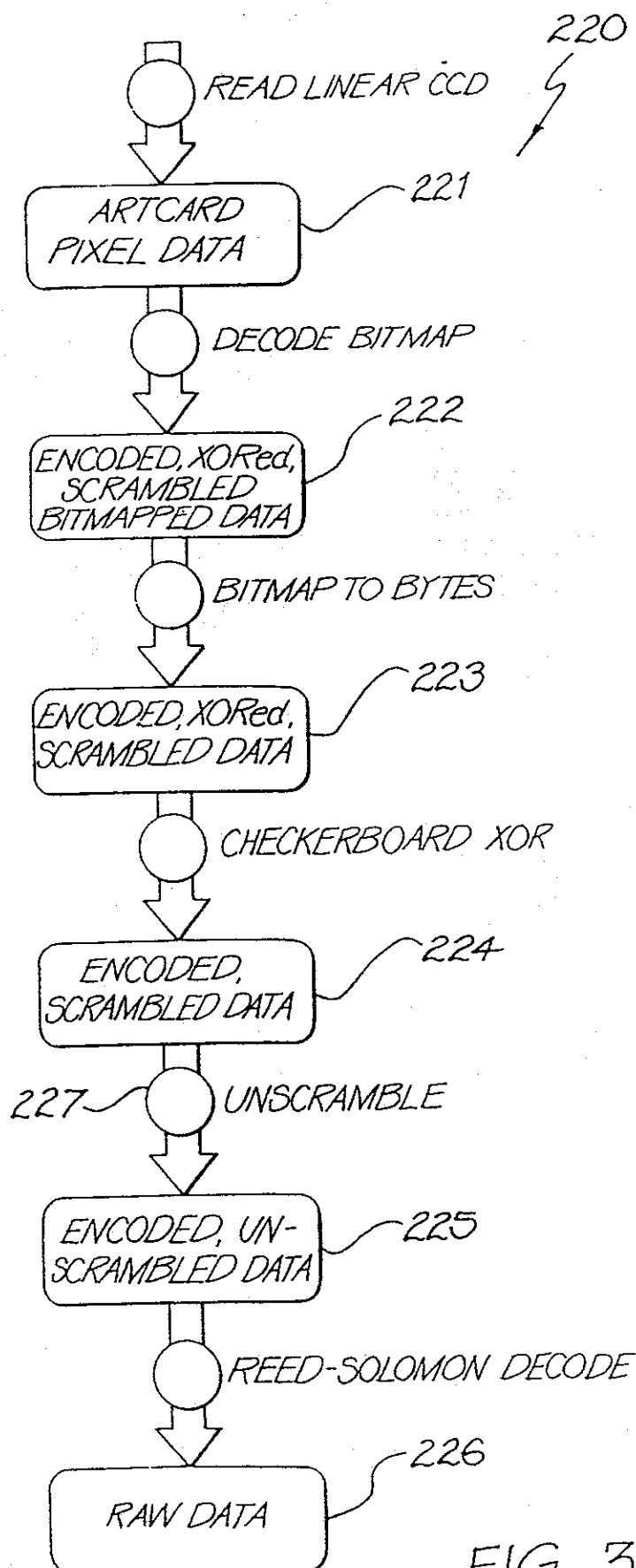


FIG. 37

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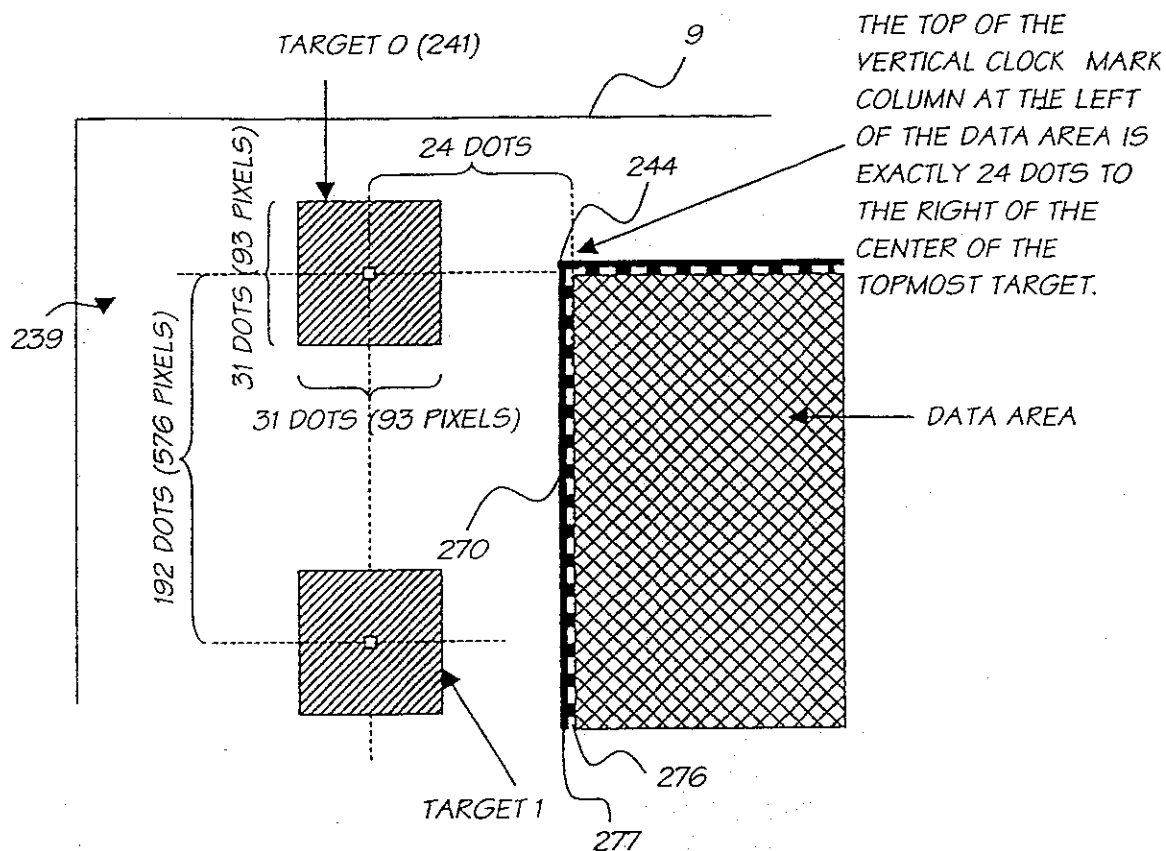


FIG. 38

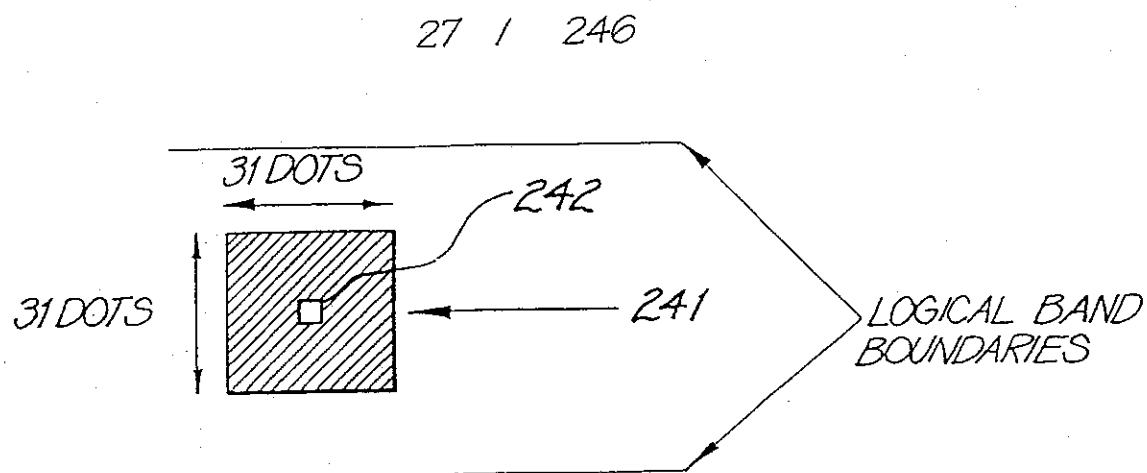


FIG. 39

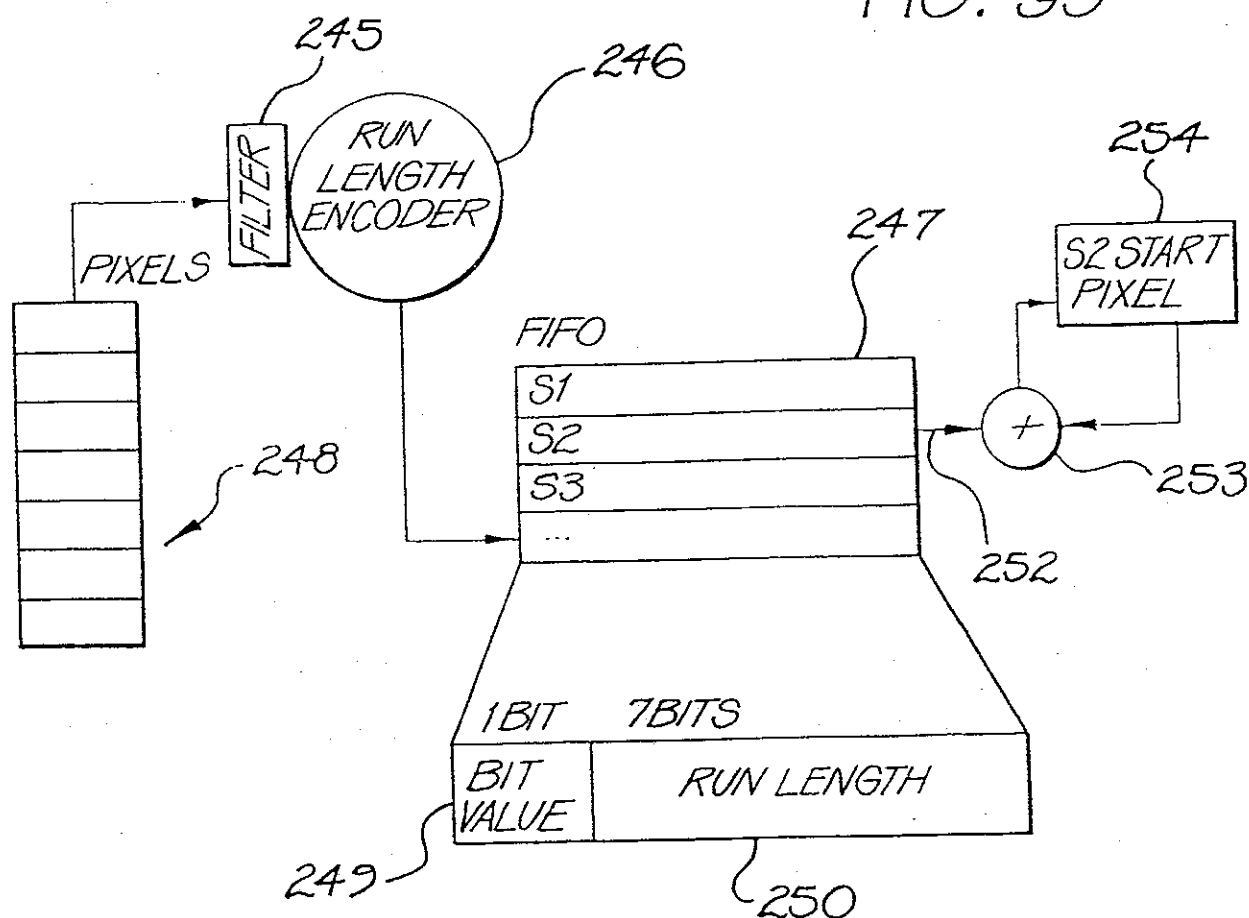


FIG. 40

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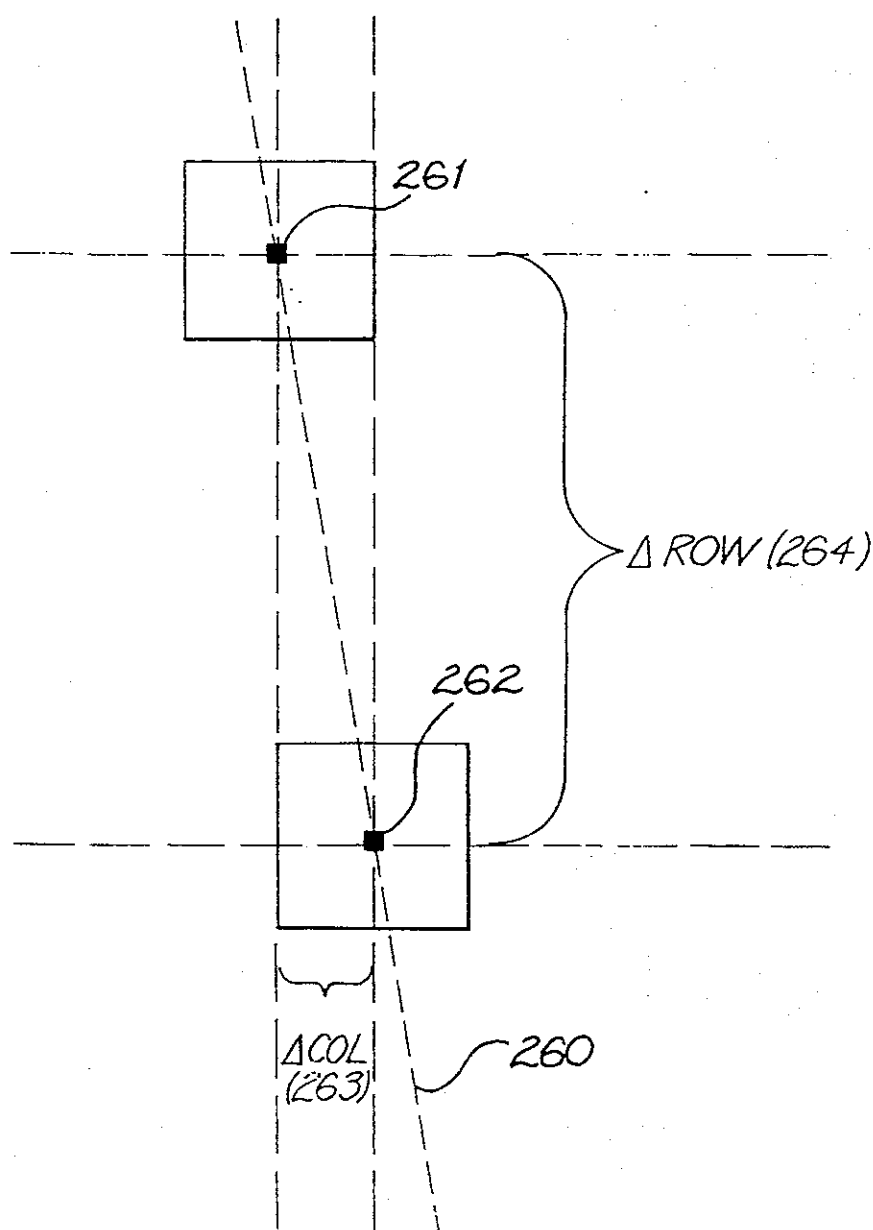
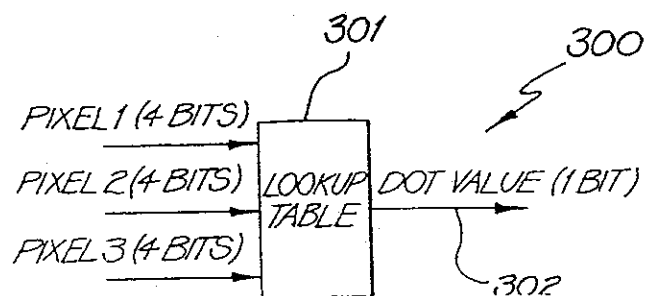
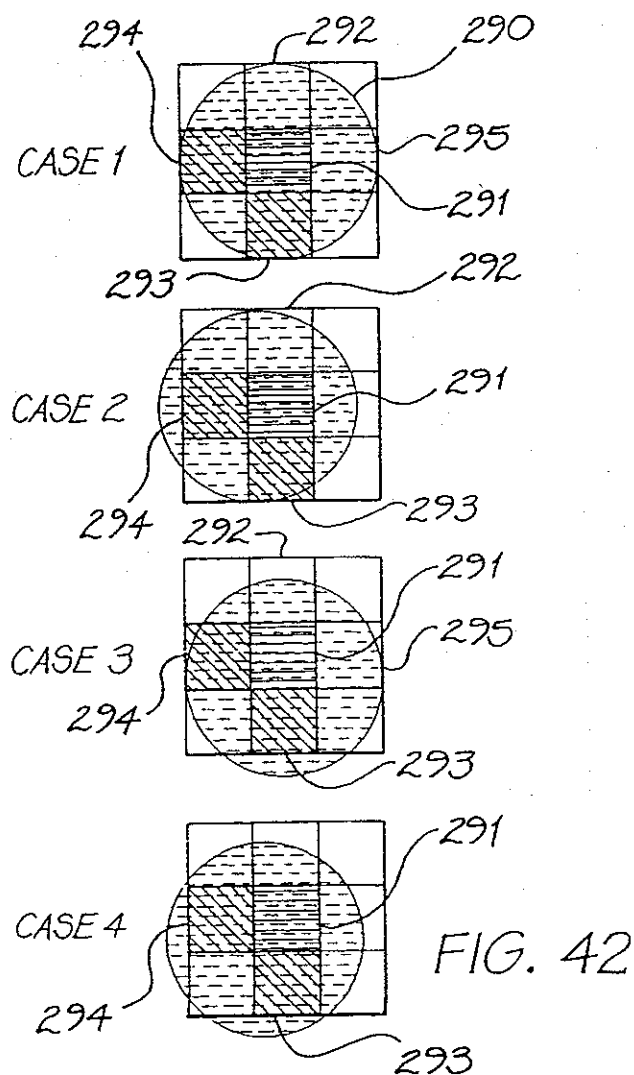


FIG. 41

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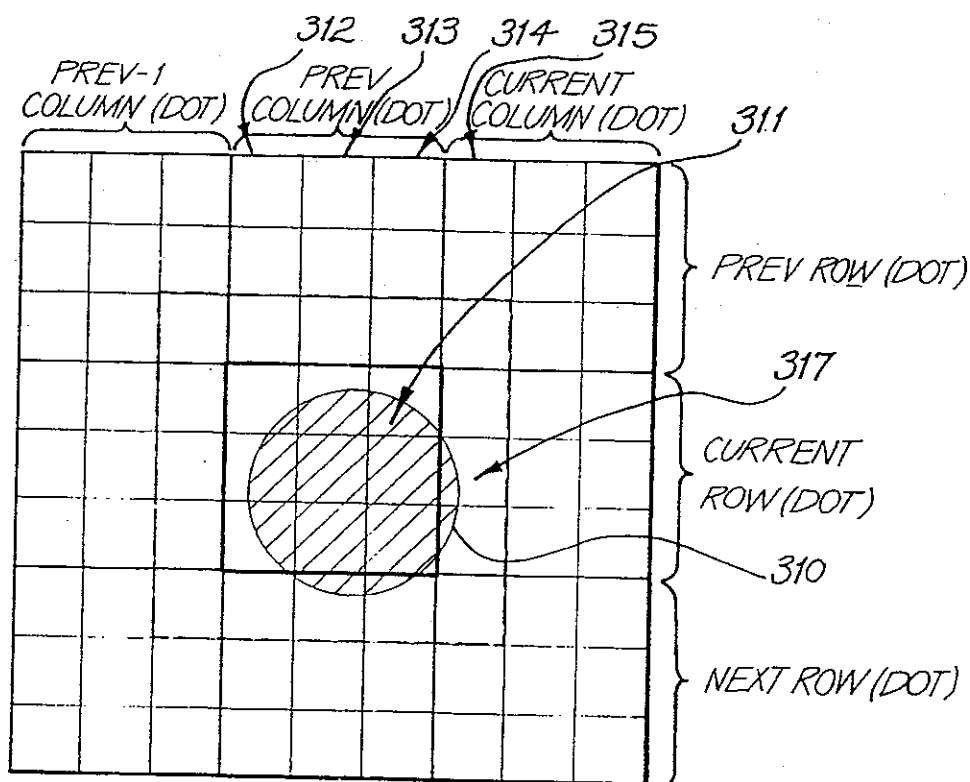
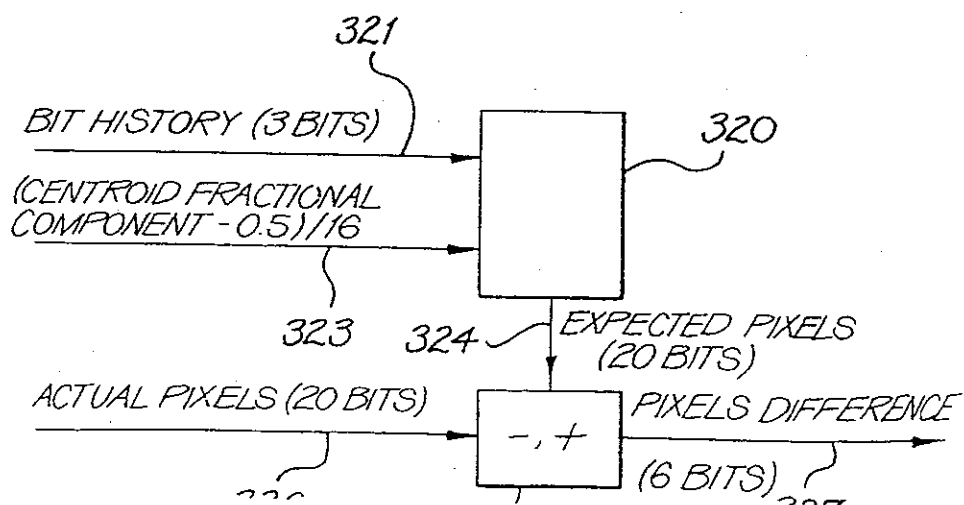


FIG. 44



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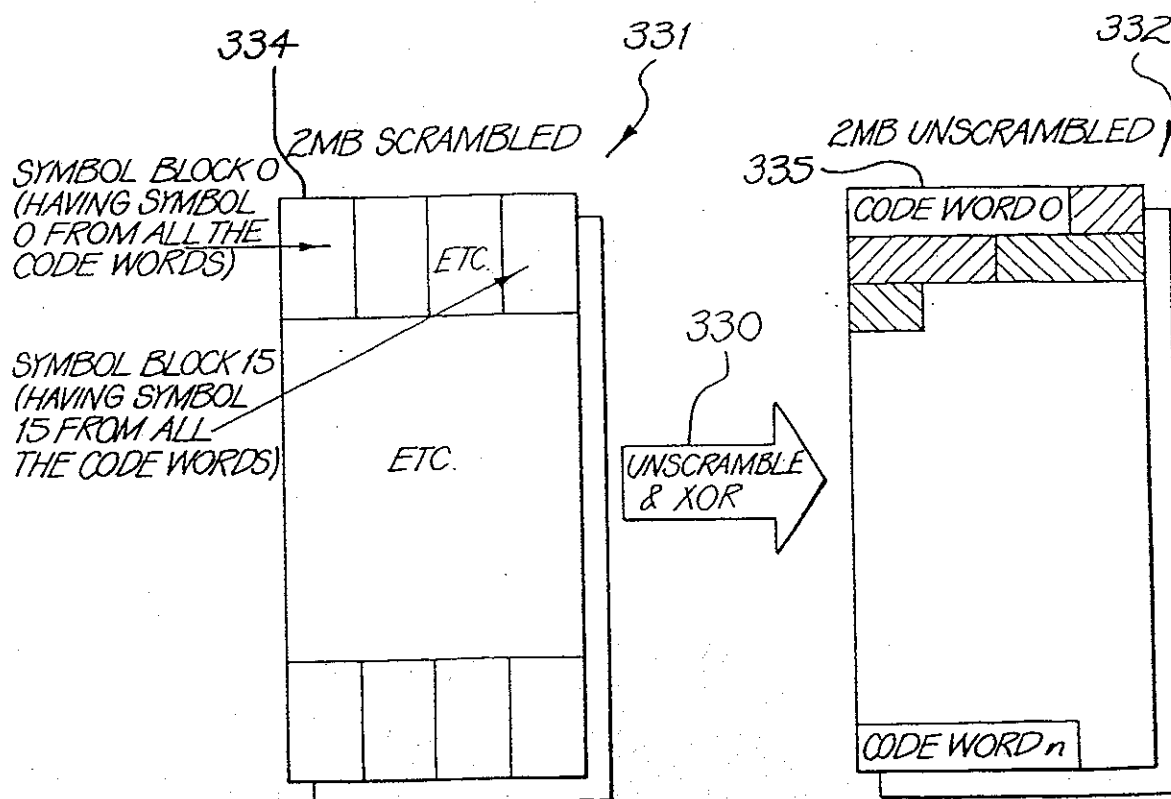


FIG. 46

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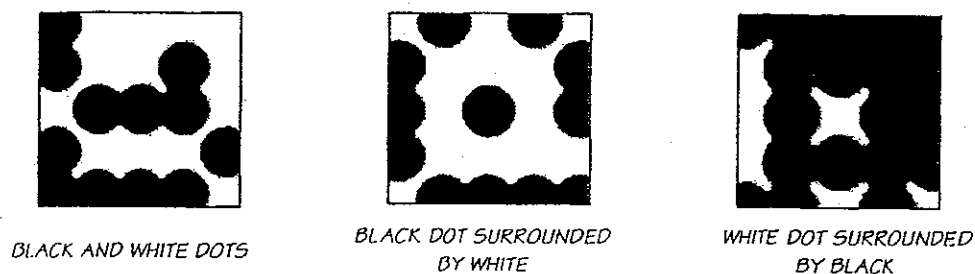


FIG. 47

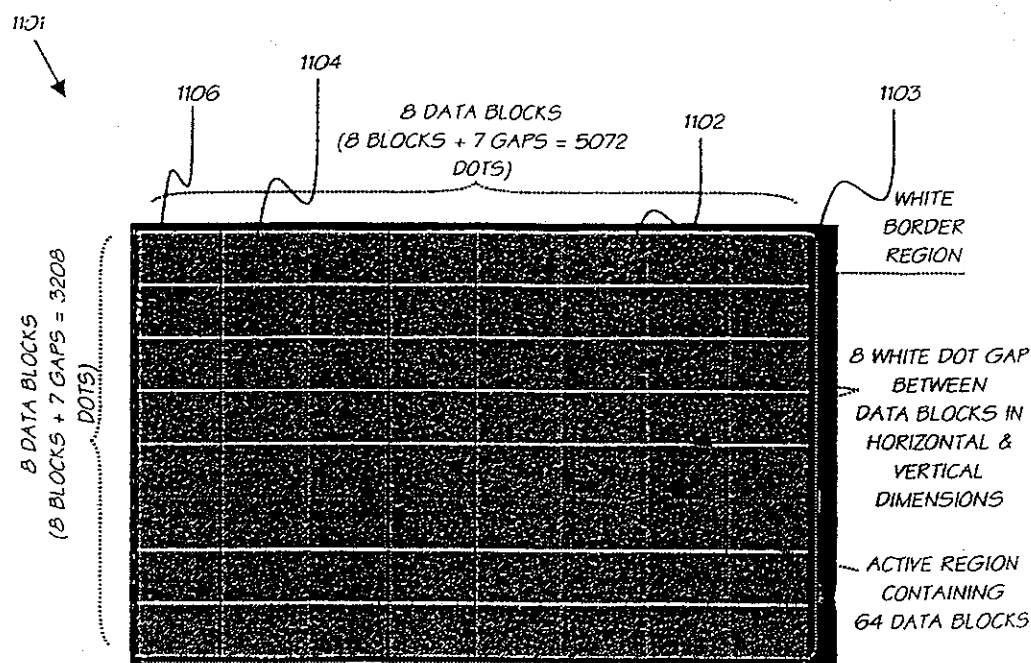


FIG. 48

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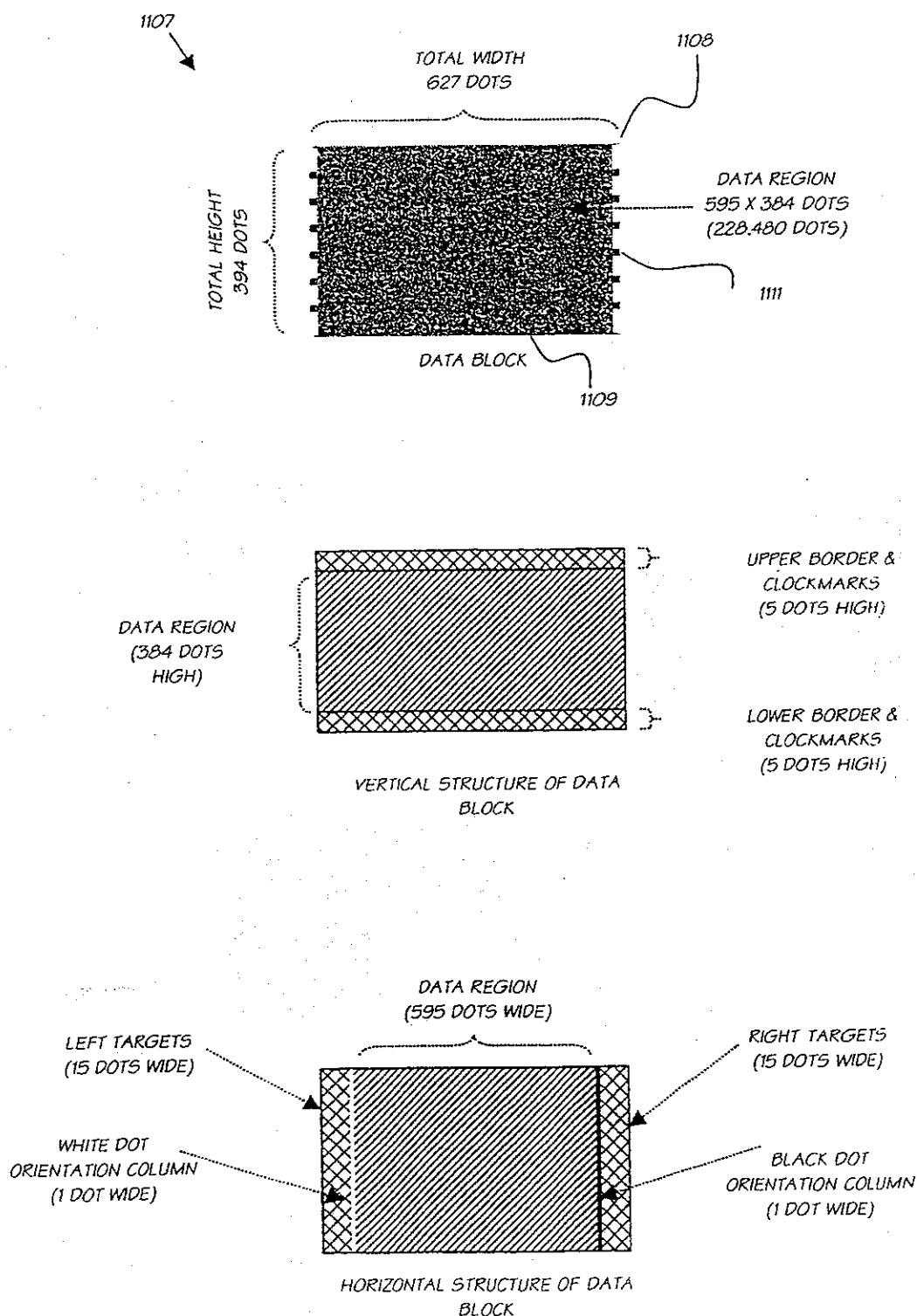


FIG. 49

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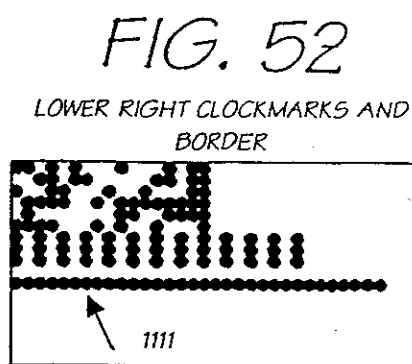
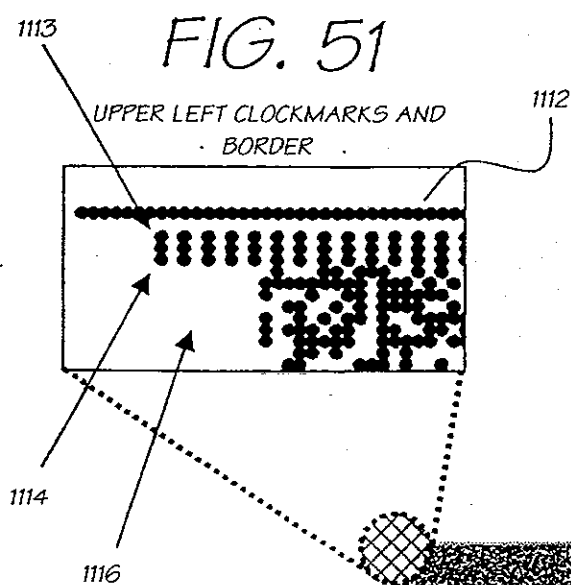


FIG. 50

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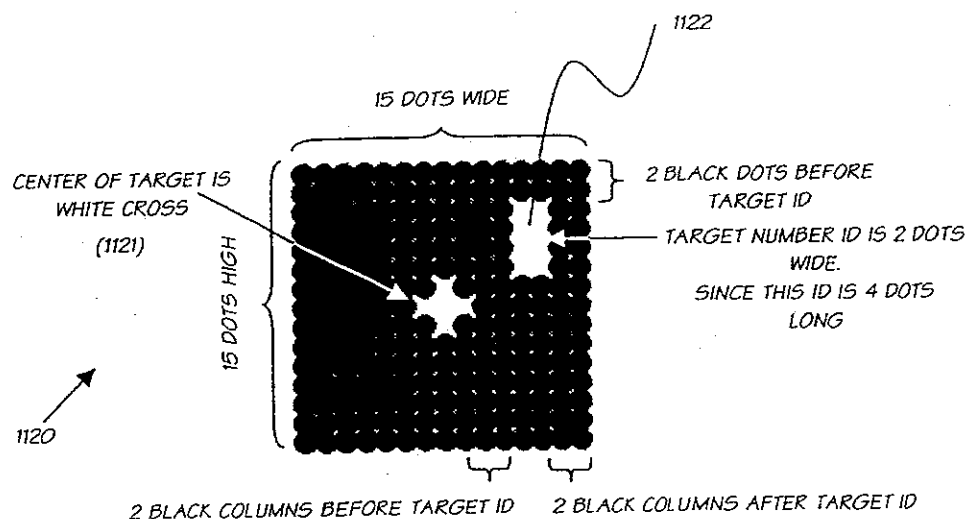


FIG. 53

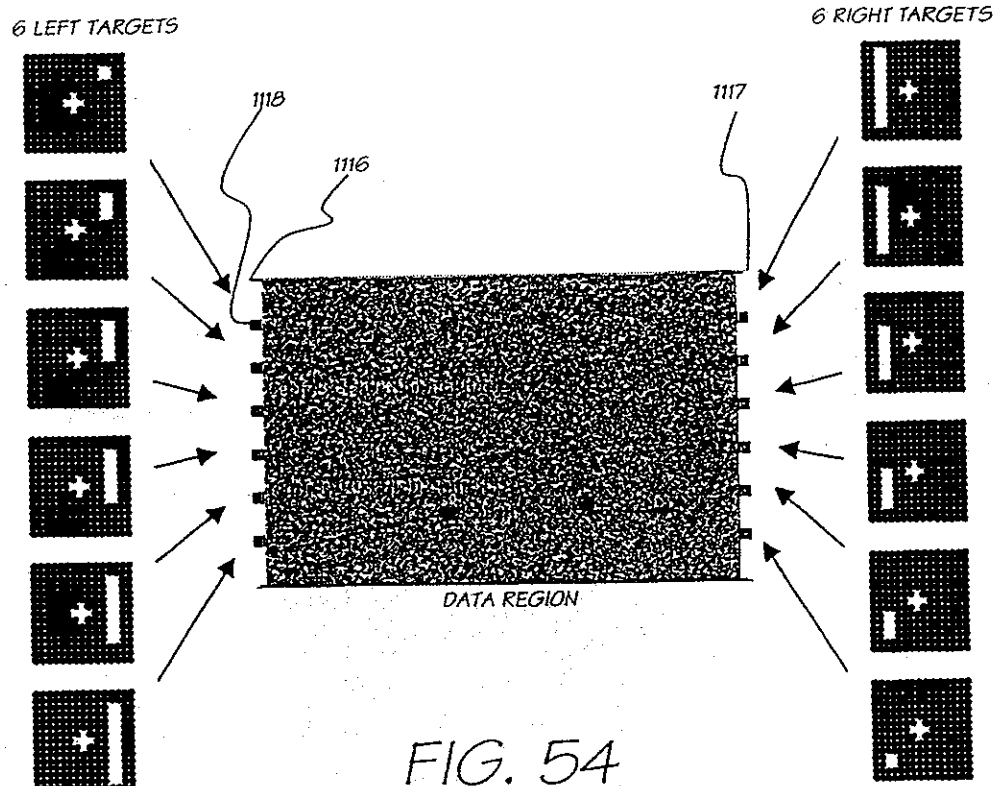


FIG. 54

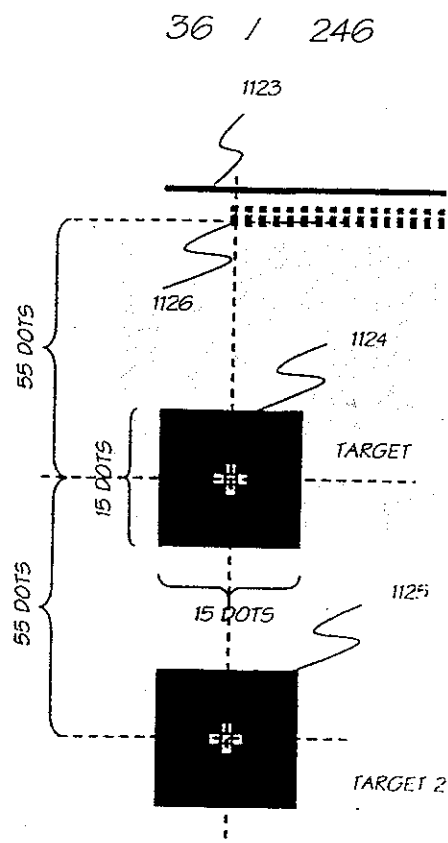
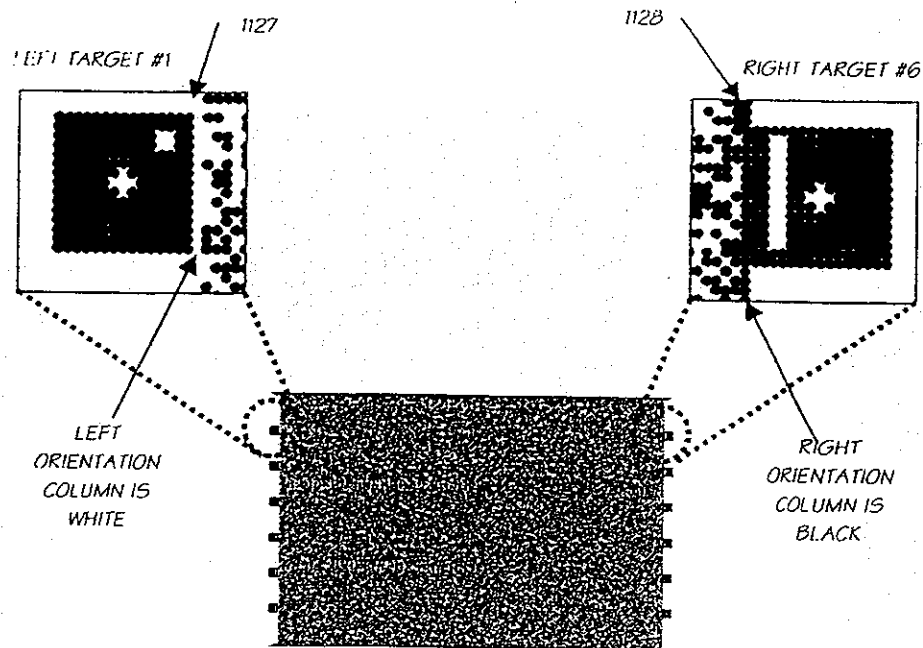


FIG. 55



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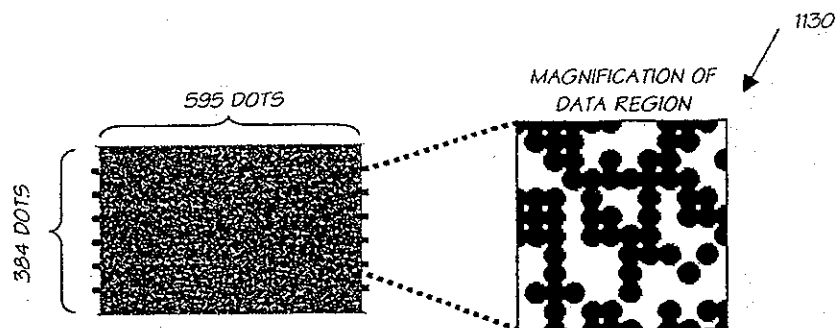


FIG. 57

00:	4F 00 3D 4F 00 3D 4F 00 3D 4F 00 3D	32 COPIES OF THE 3 BYTE CONTROL INFORMATION
0C:	4F 00 3D 4F 00 3D 4F 00 3D 4F 00 3D	
18:	4F 00 3D 4F 00 3D 4F 00 3D 4F 00 3D	
24:	4F 00 3D 4F 00 3D 4F 00 3D 4F 00 3D	
30:	4F 00 3D 4F 00 3D 4F 00 3D 4F 00 3D	
3C:	4F 00 3D 4F 00 3D 4F 00 3D 4F 00 3D	
48:	4F 00 3D 4F 00 3D 4F 00 3D 4F 00 3D	
54:	4F 00 3D 4F 00 3D 4F 00 3D 4F 00 3D	
60:	00 00 00 00 00 00 00 00 00 00 00 00	RESERVED BYTES ARE 0
6C:	00 00 00 00 00 00 00 00 00 00 00 00	
78:	00 00 00 00 00 00 00 00 00 00 00 00	

FIG. 59

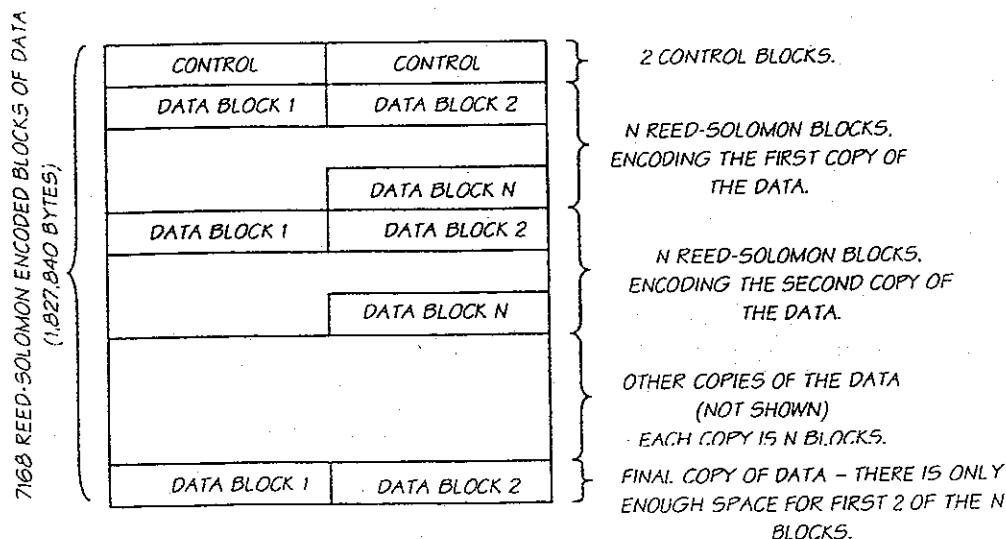


FIG. 58

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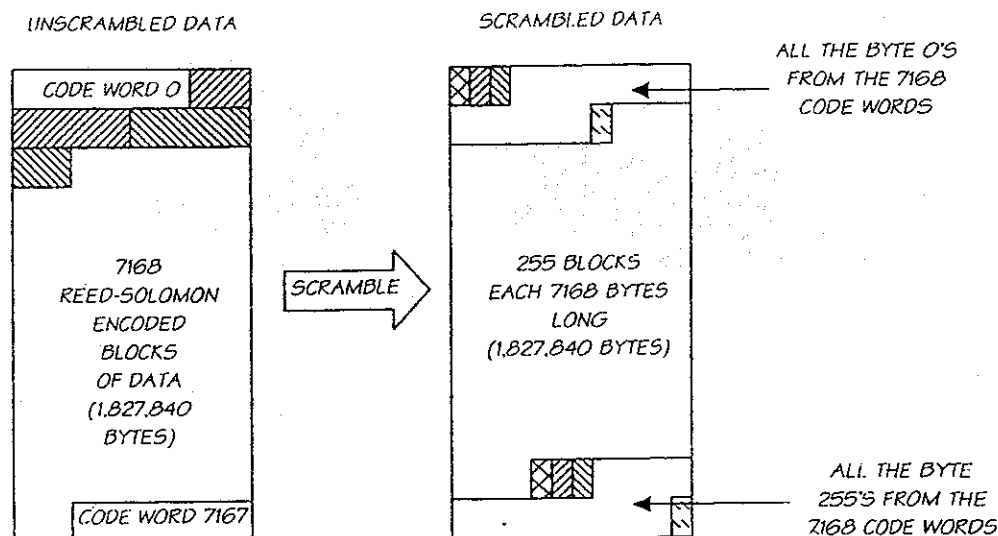


FIG. 60

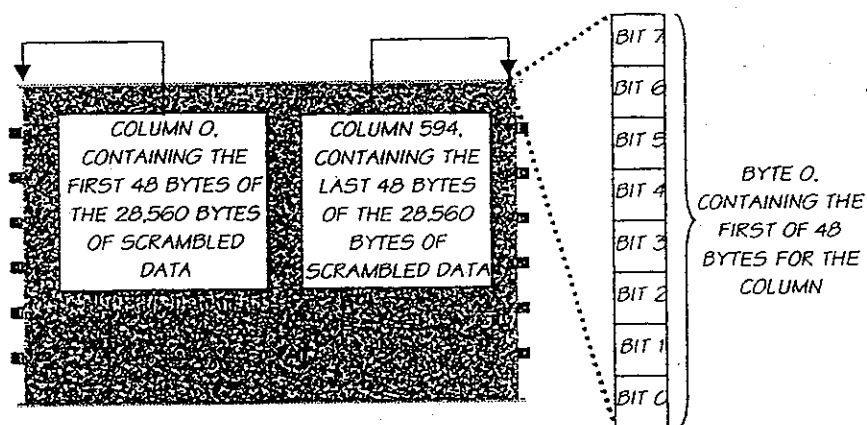


FIG. 61

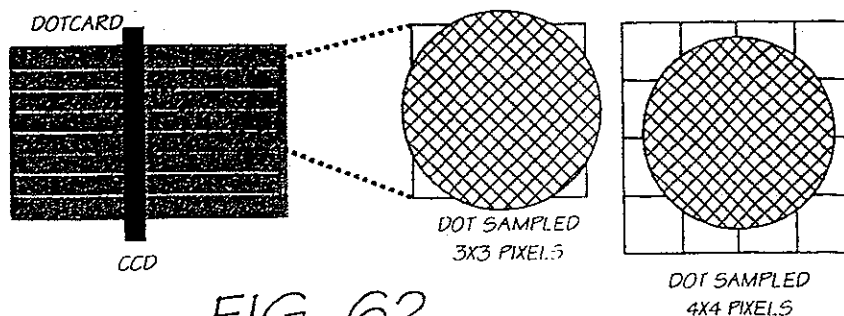


FIG. 62

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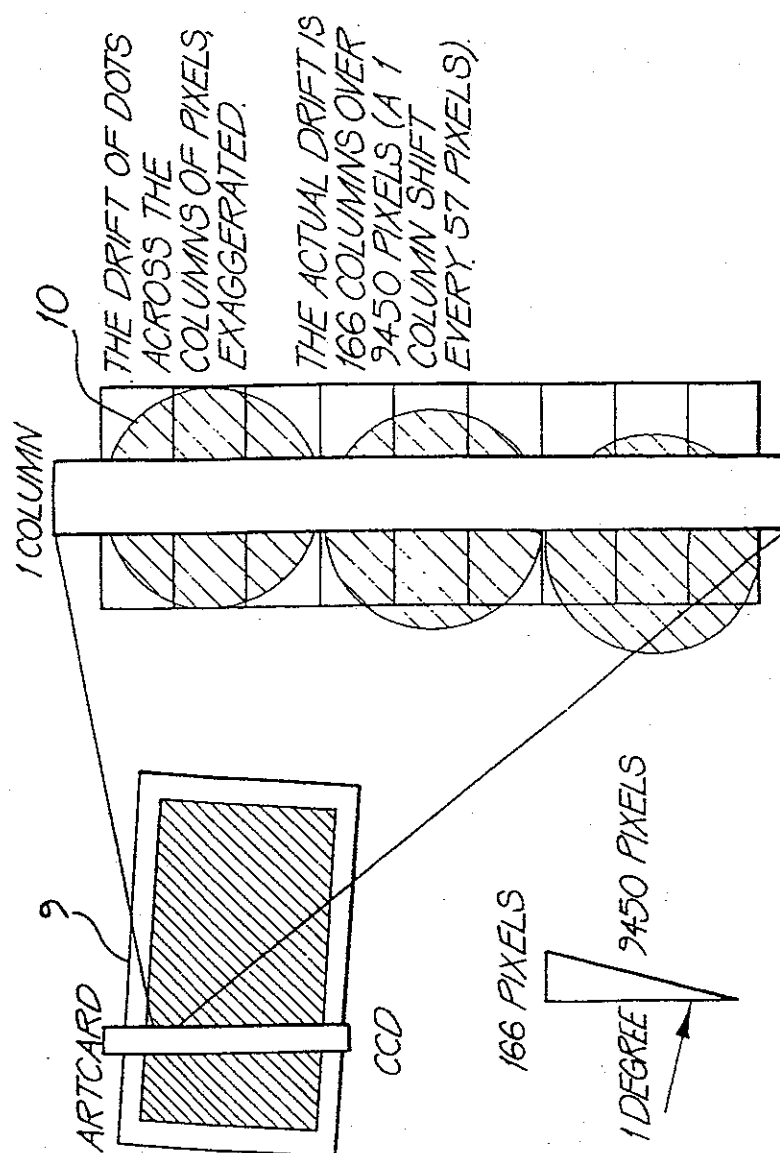


FIG. 63

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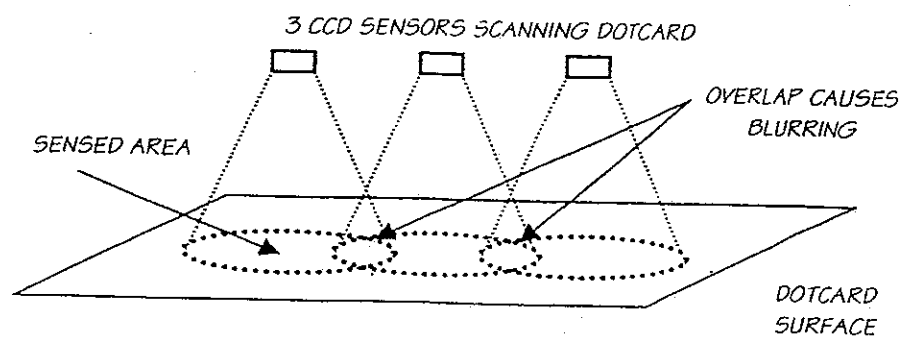


FIG. 64

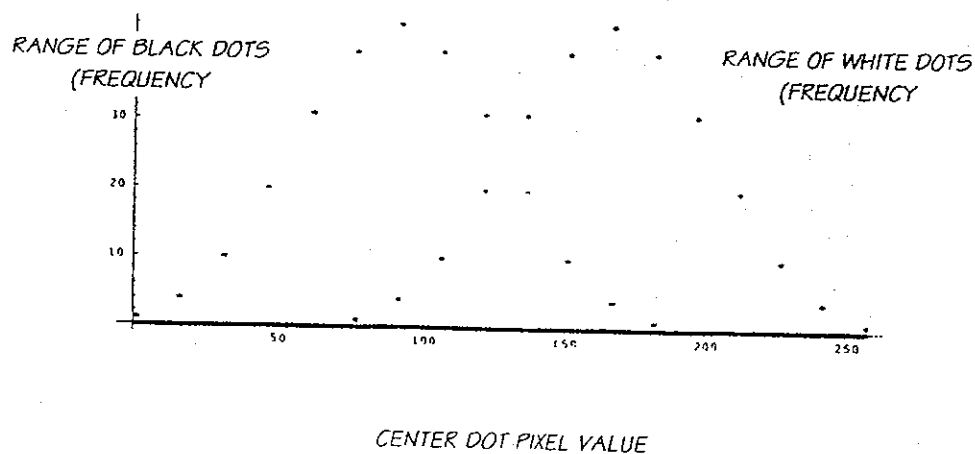


FIG. 65

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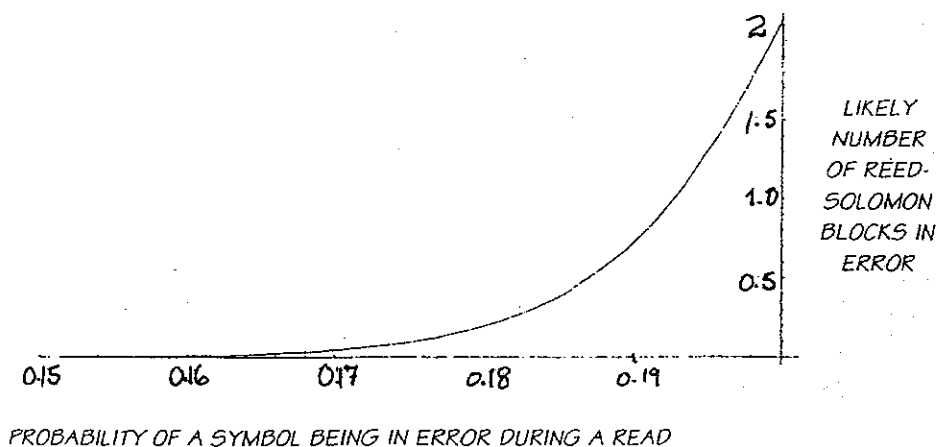
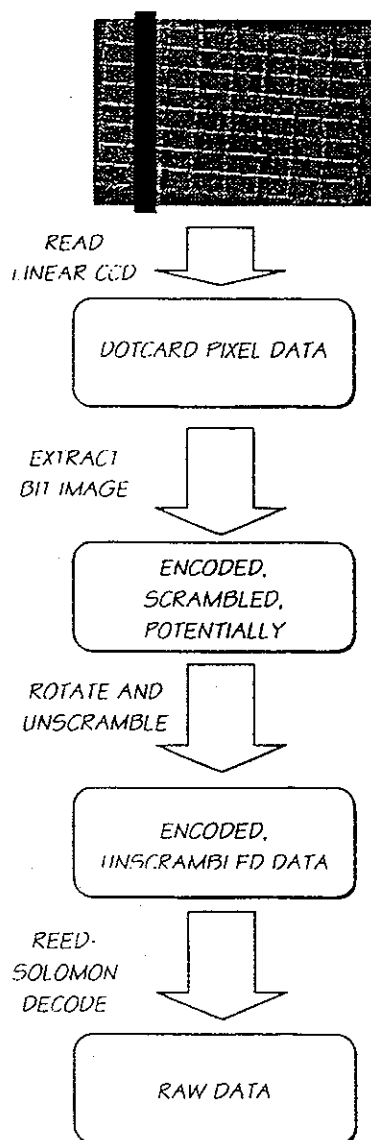


FIG. 66



APPROXIMATE DATA SIZES FOR 1600 DPI DOTCARD

86MM + 1MM IN HORIZONTAL DIMENSION FOR P ROTATION =
87MM

87MM = 16,252 SCANLINES

16,440 SCANLINES @ 11,000 PIXELS PER SCANLINE =
180,840,000 PIXELS

180,840,000 PIXELS @ 1 BYTE PER PIXEL = 180,840,000 BYTES
= 172.5 MB

64 DATA BLOCKS, EACH CONTAINING 597 COLUMNS (595 DATA REGION COLUMNS AND 2 ORIENTATION COLUMNS), @ 48 BYTES PER COLUMN = 28,656 BYTES PER DATA BLOCK FOR A TOTAL OF 1,833,984 BYTES.

64 DATA BLOCKS, EACH CONTAINING 112 ENCODED REED SOLOMON BLOCKS, @ 255 BYTES PER REED SOLOMON BLOCK FOR A TOTAL OF 1,827,840 BYTES.

DECODED DATA, WITH A MAXIMUM SIZE OF 910,082 BYTES.
(64 X 112 X 127 - (2 CONTROL BLOCKS @ 127 BYTES))

FIG. 67

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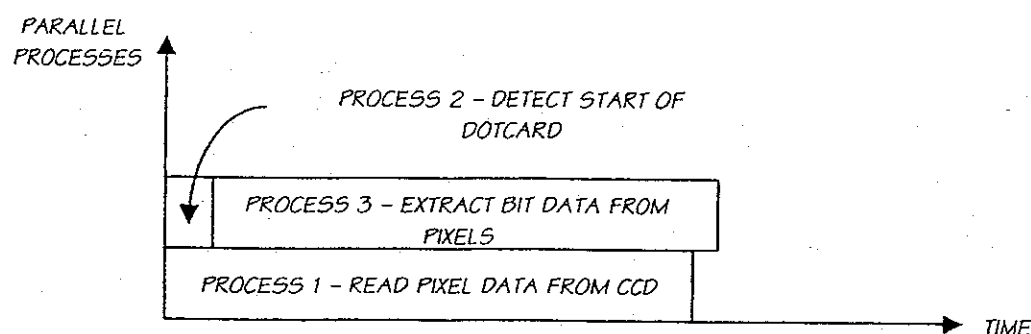


FIG. 68

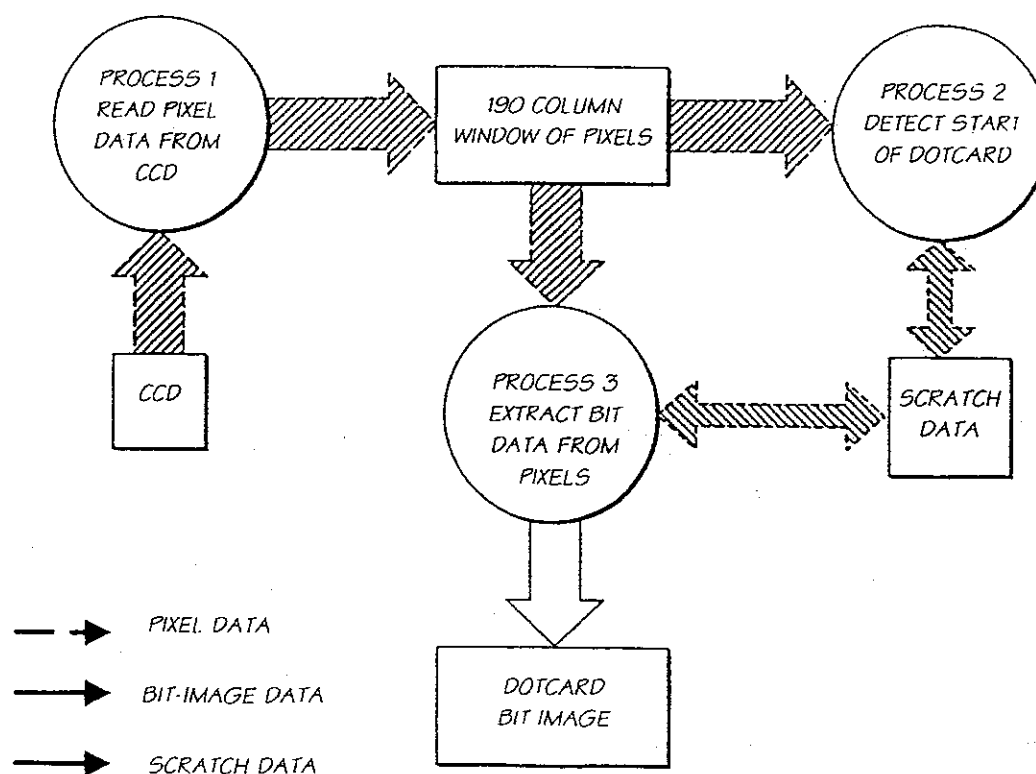


FIG. 69

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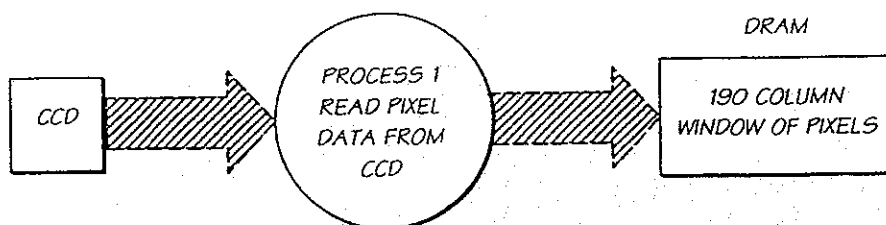


FIG. 70

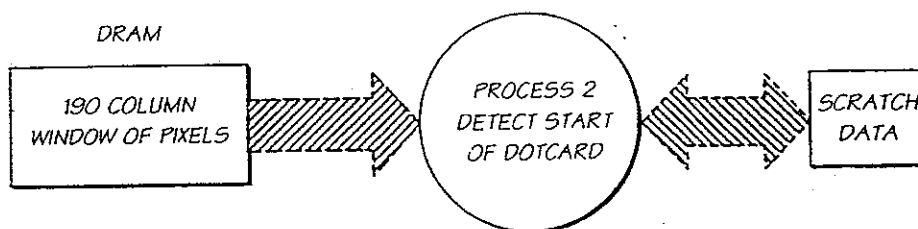


FIG. 71

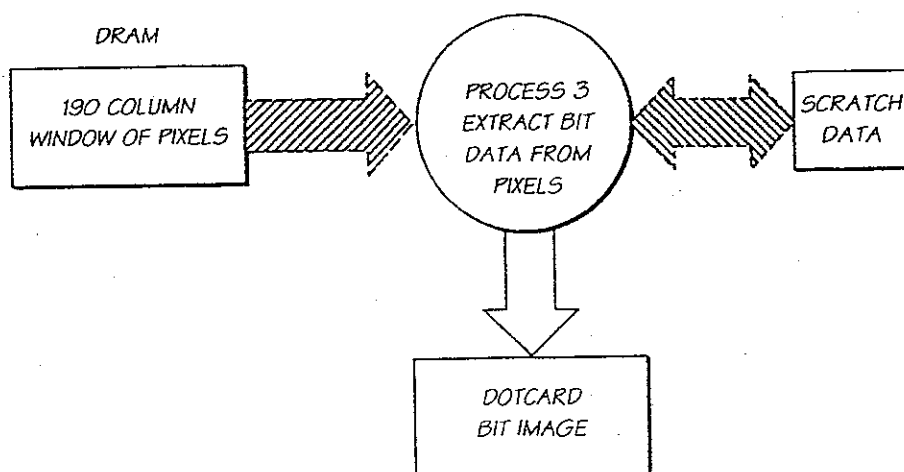


FIG. 72

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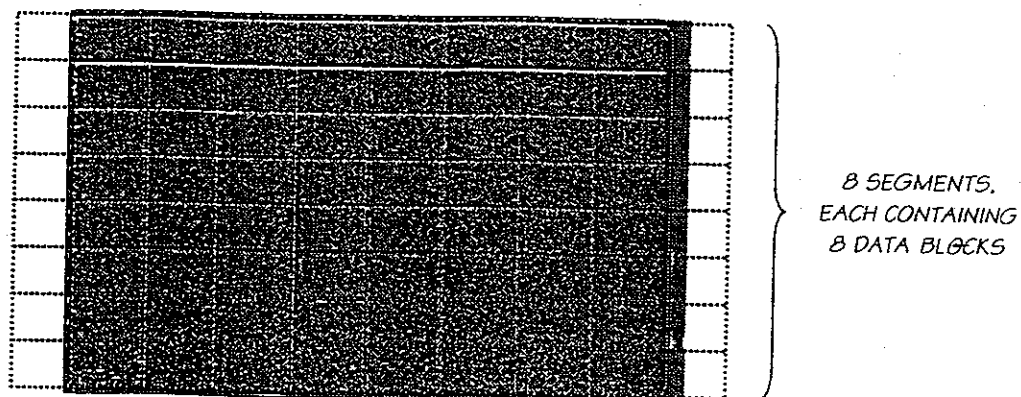


FIG. 73

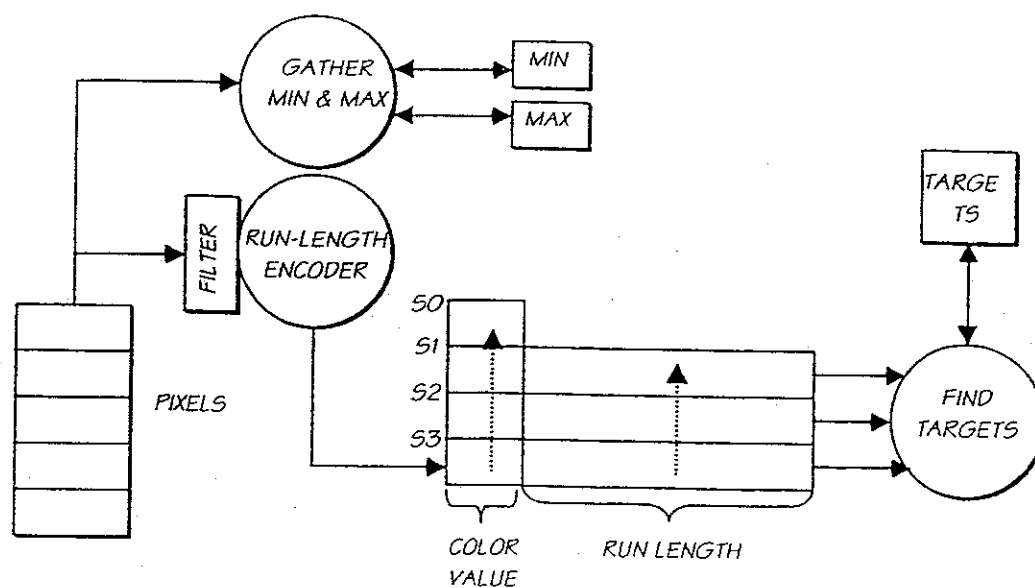


FIG. 74

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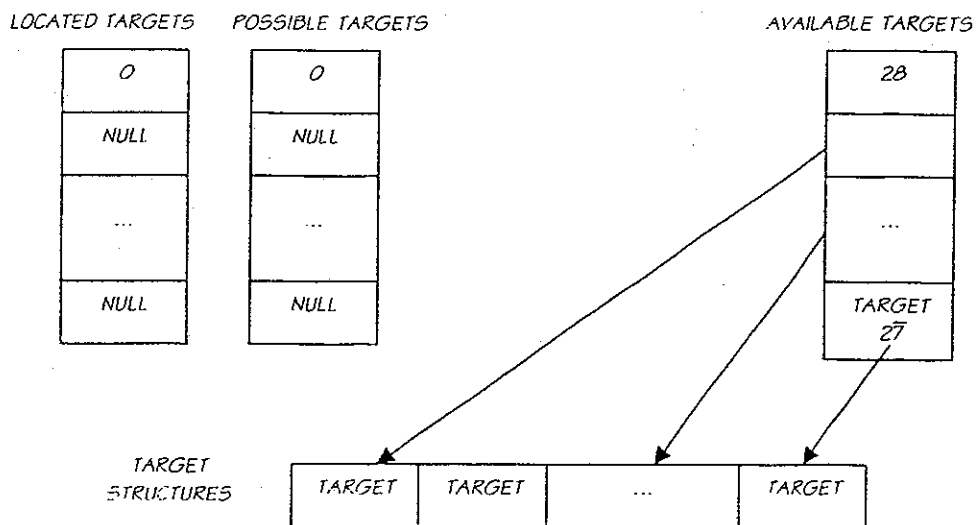


FIG. 75

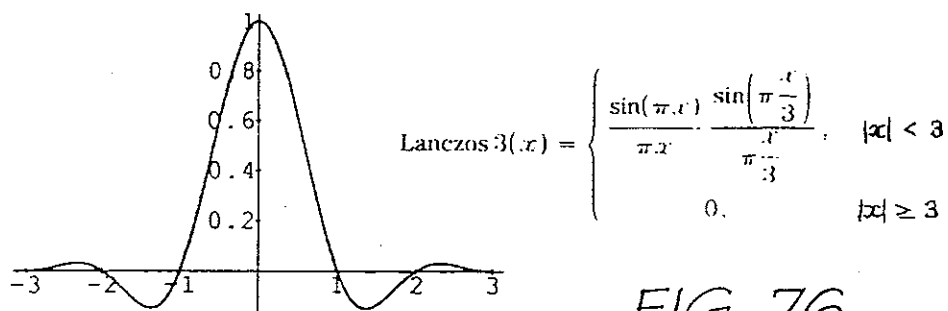


FIG. 76

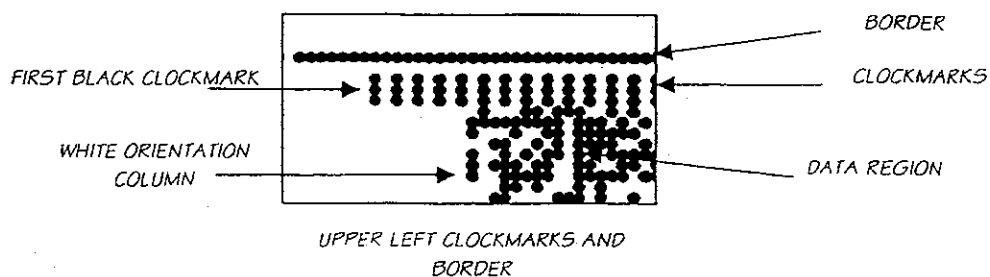


FIG. 77

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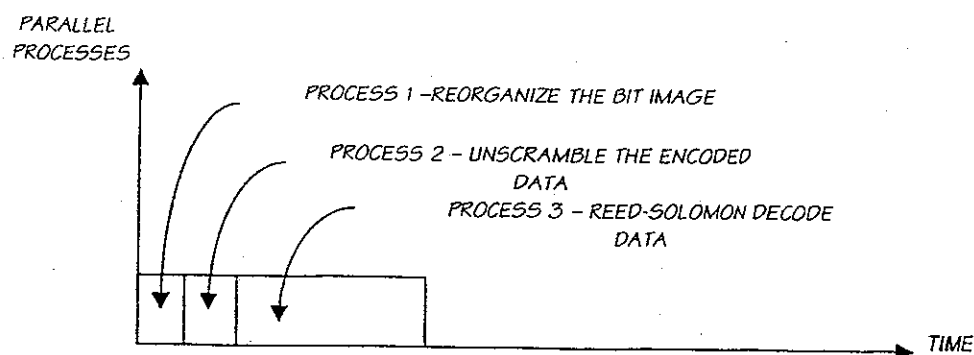


FIG. 78

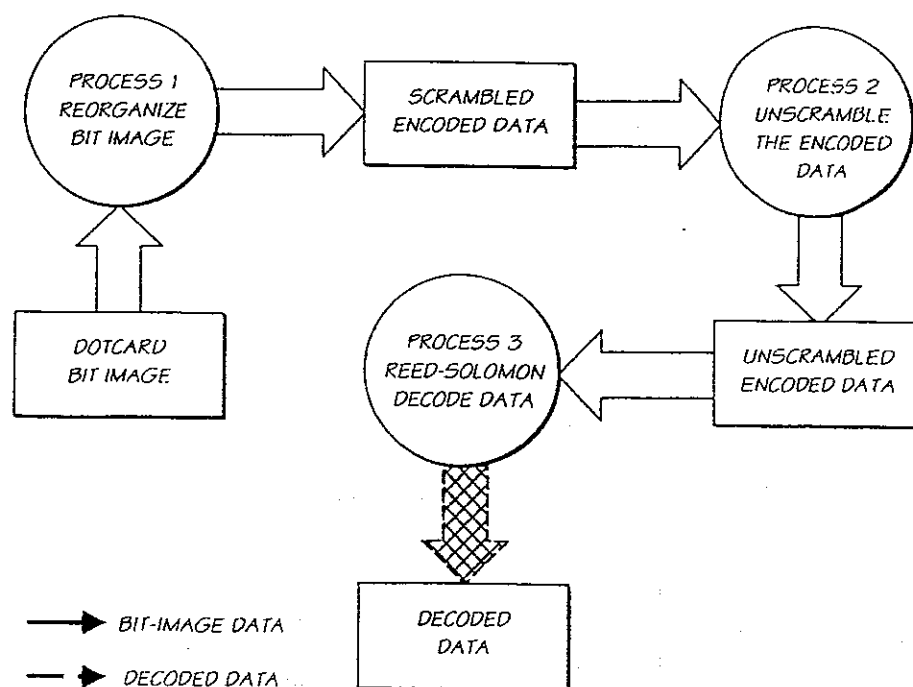


FIG. 79

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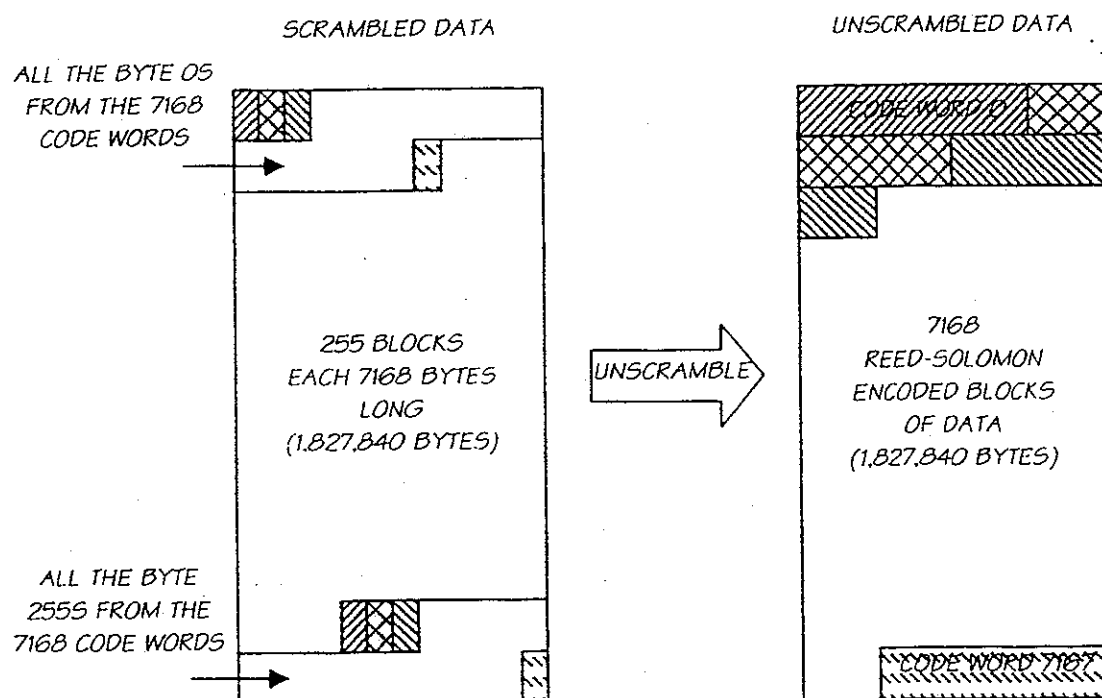


FIG. 80

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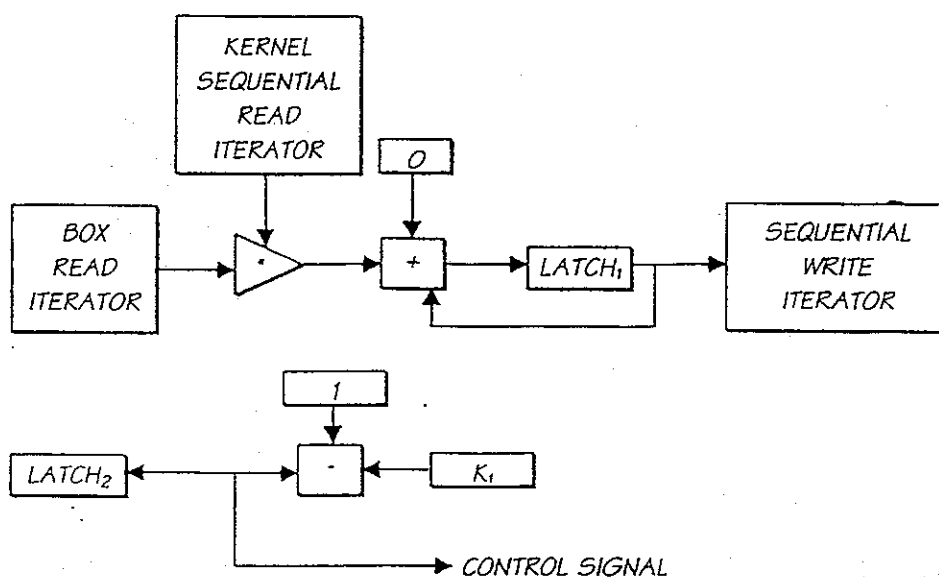


FIG. 81

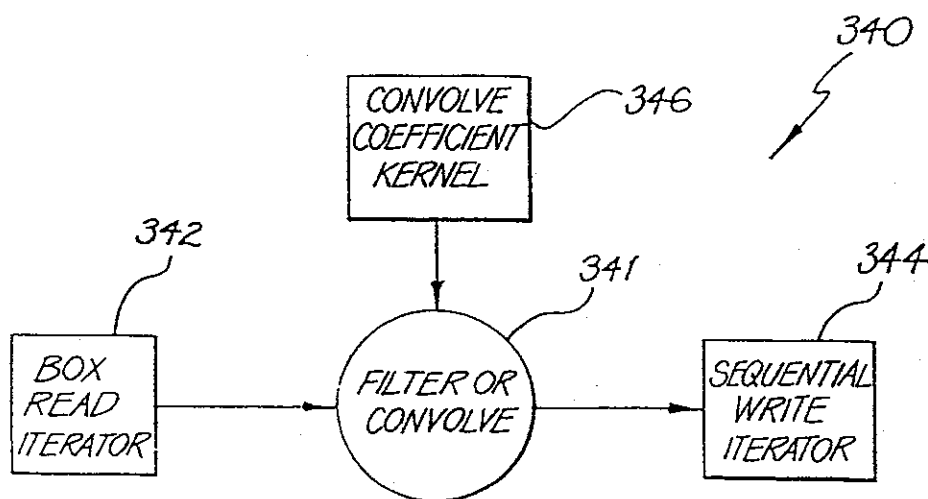


FIG. 82

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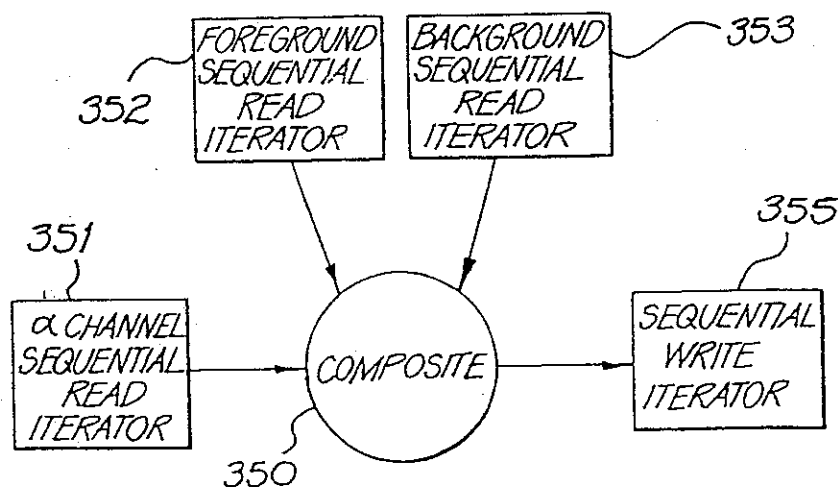
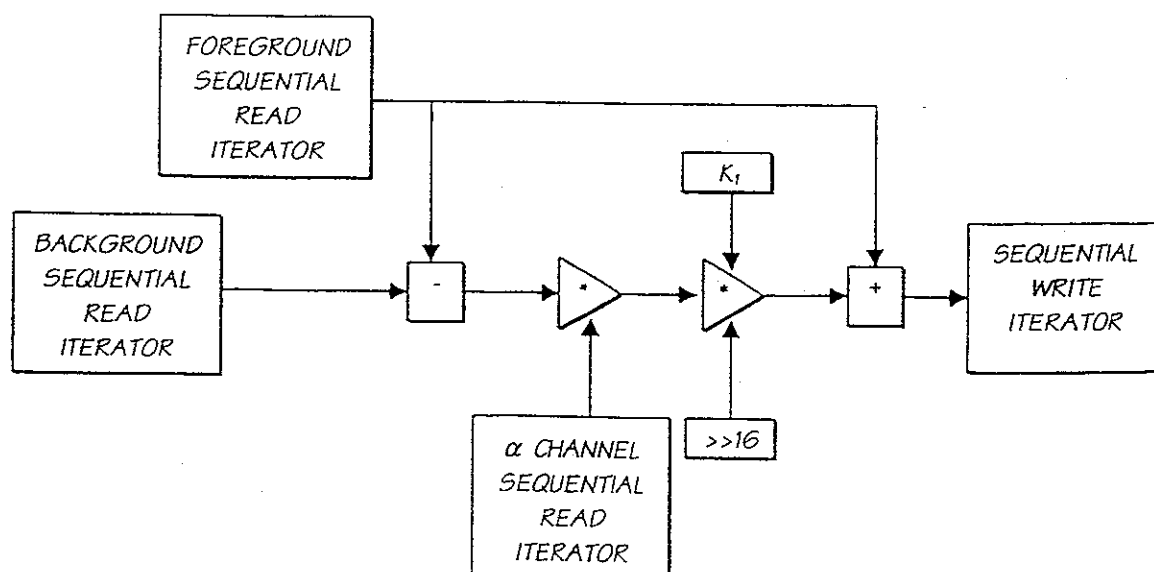


FIG. 83



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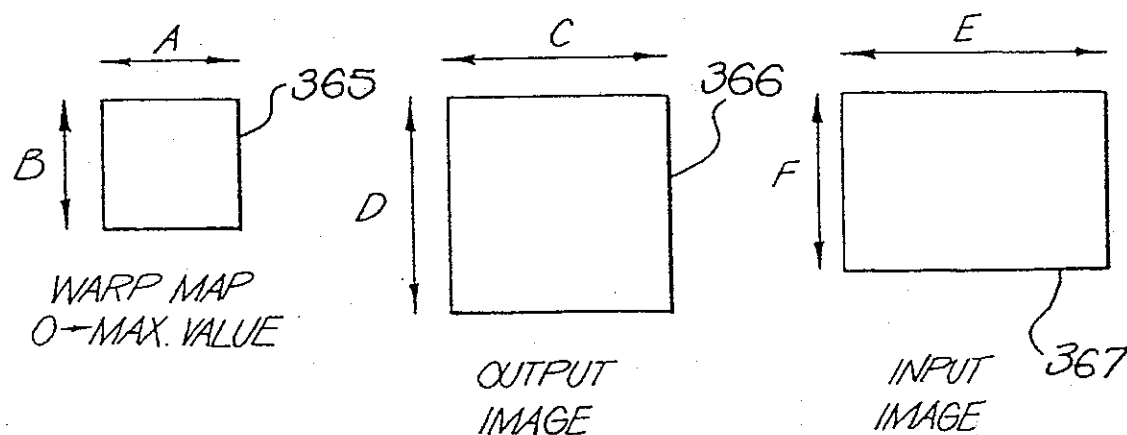


FIG. 85

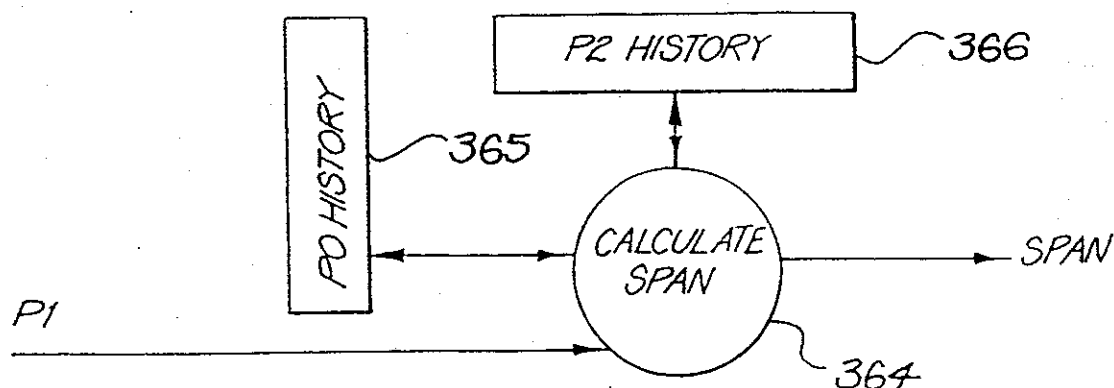


FIG. 86

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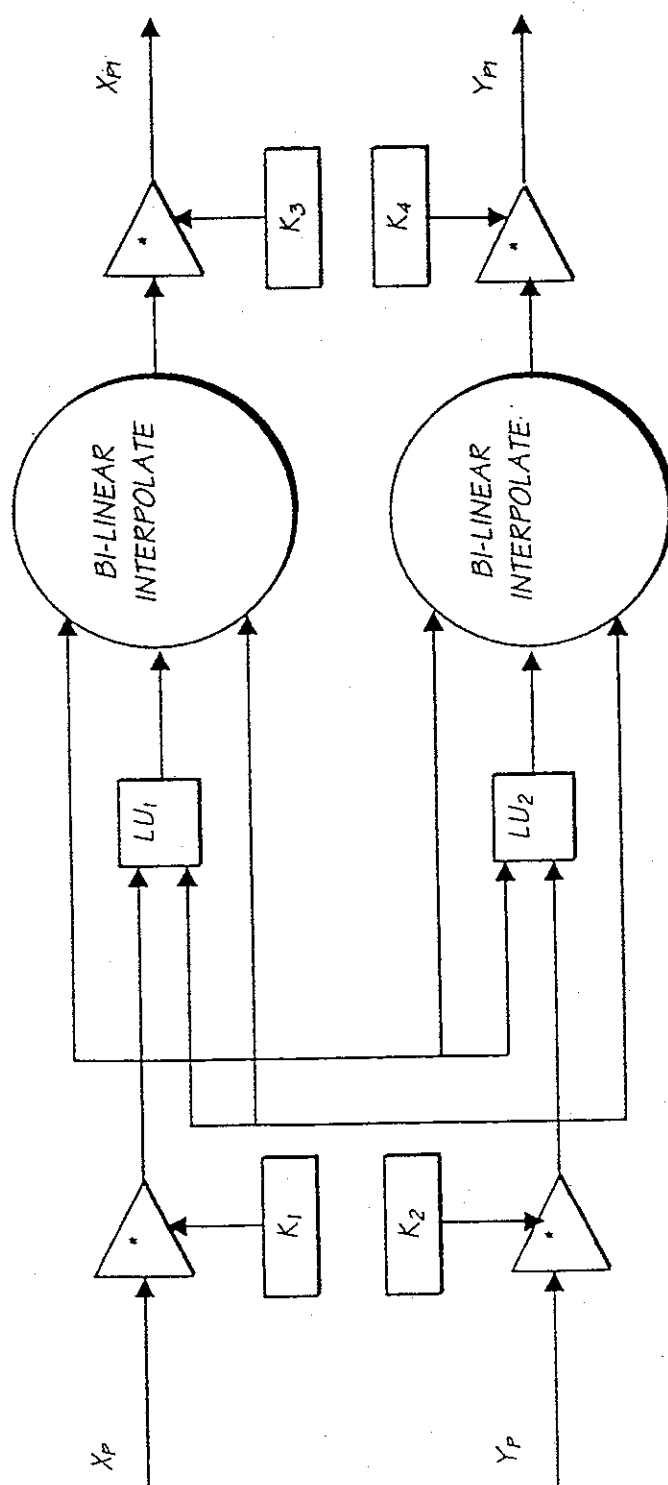


FIG. 87

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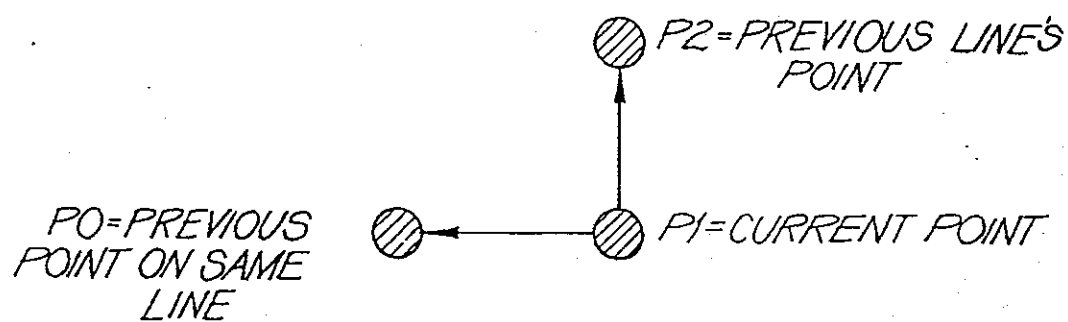
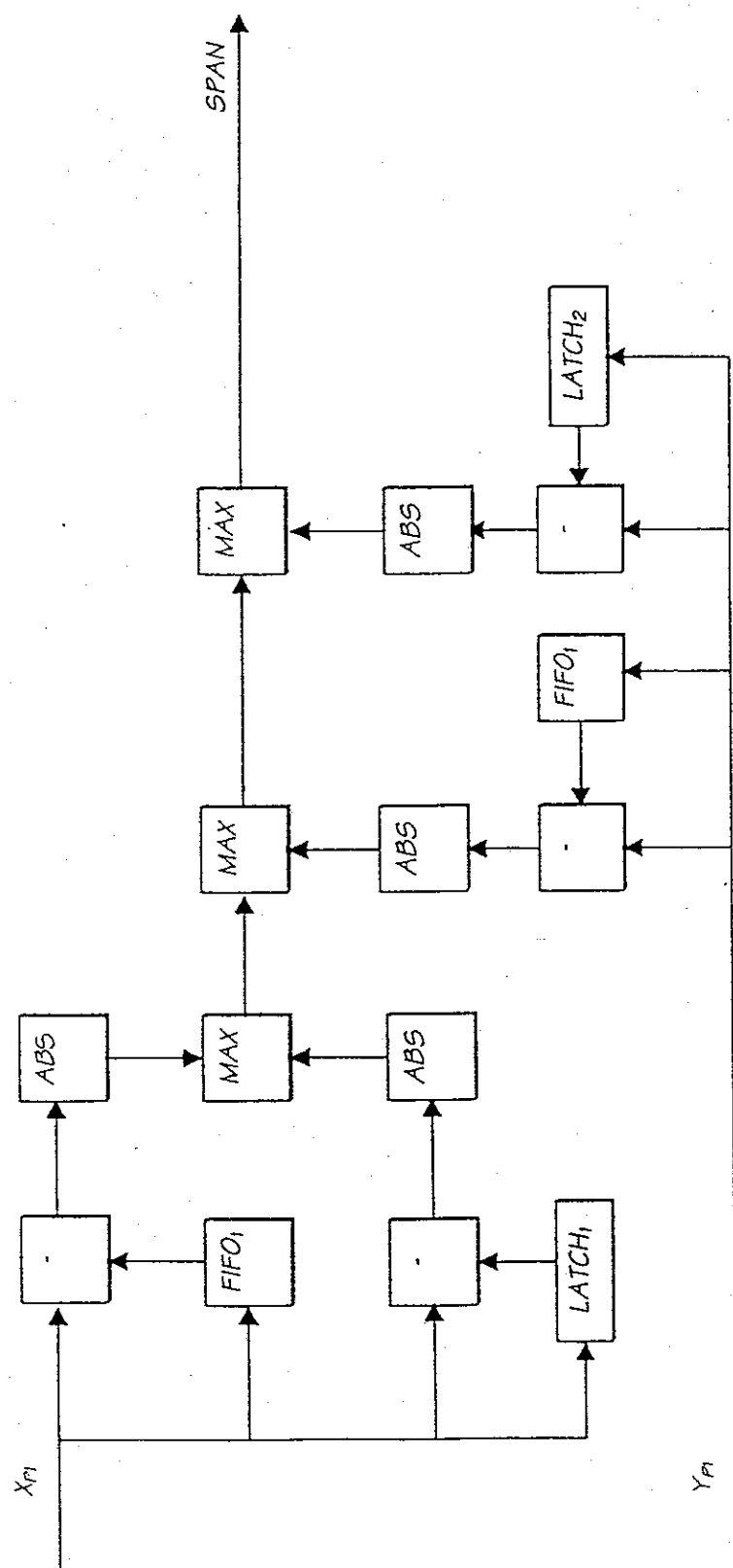


FIG. 88



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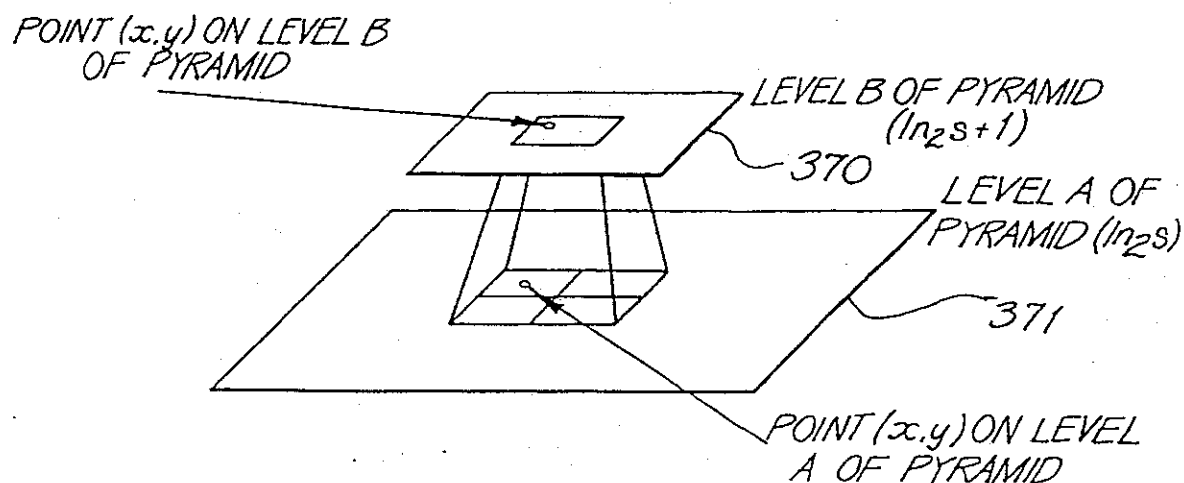


FIG. 90

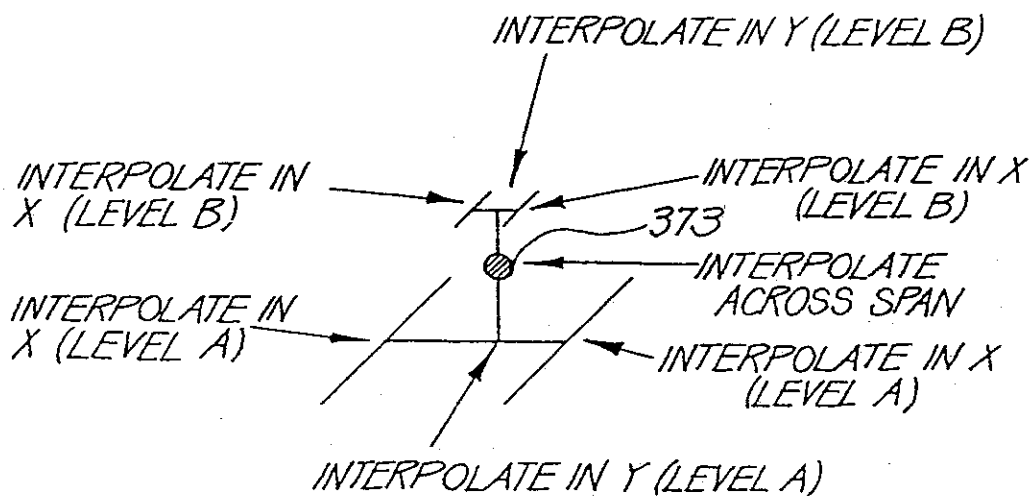


FIG. 91

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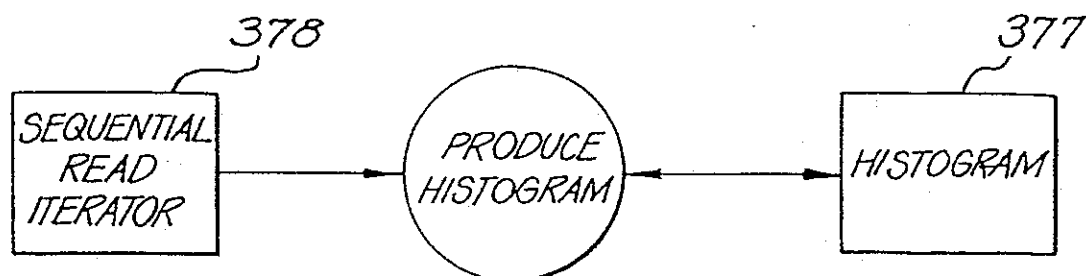


FIG. 92

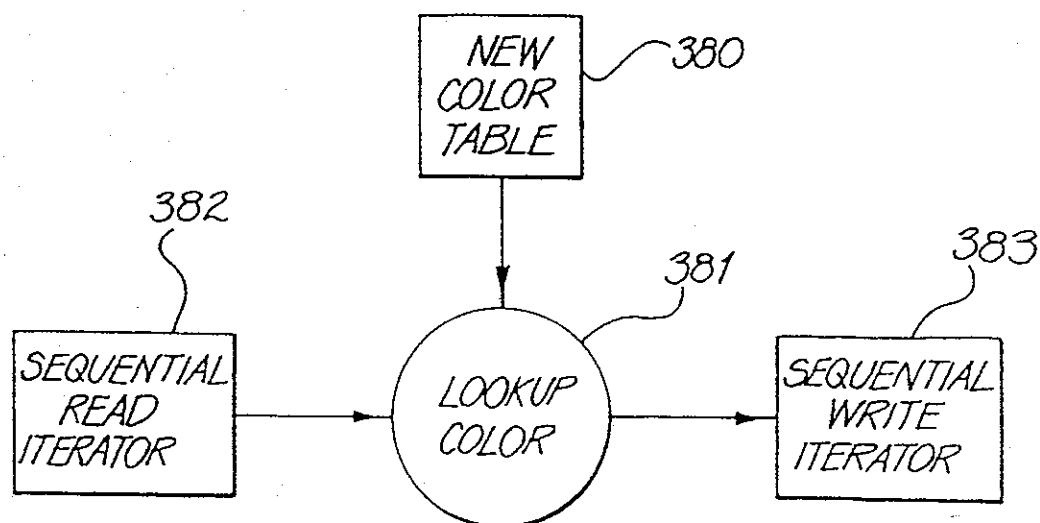


FIG. 93

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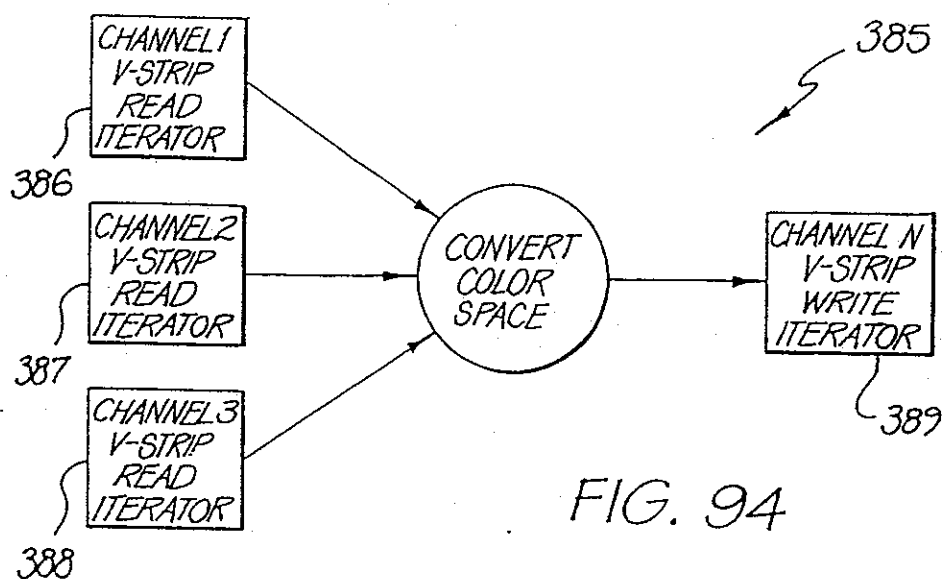


FIG. 94

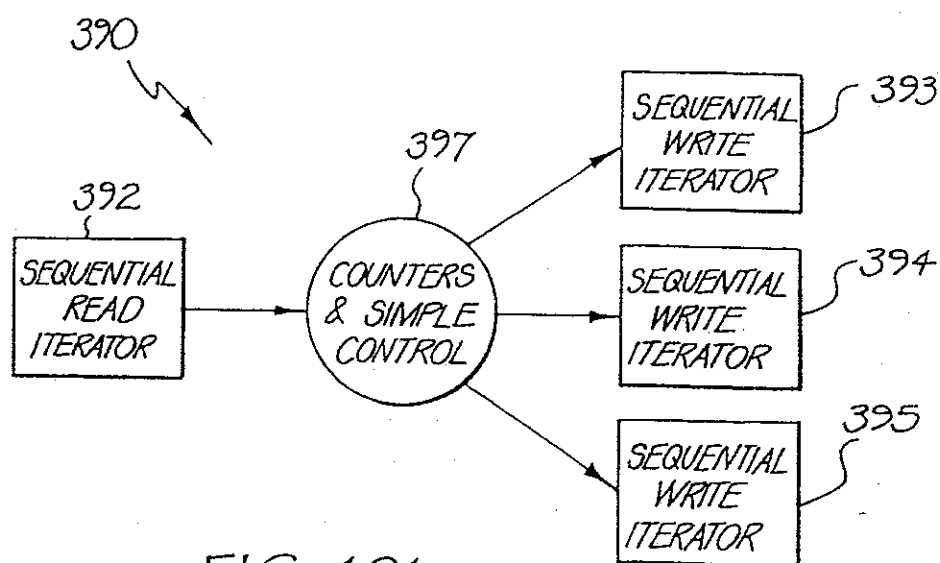


FIG. 101

```

graph LR
    CX[CHANNEL X V-STRIP READ ITERATOR] --> L1[LATCH1]
    CX --> LU1[LU1]
    CY[CHANNEL Y V-STRIP READ ITERATOR] --> LU1
    CZ[CHANNEL Z V-STRIP READ ITERATOR] --> L3[LATCH3]
    L1 --> TI((TRI-LINEAR INTERPOLATE))
    LU1 --> TI
    L3 --> TI
    TI --> L2[LATCH2]
    L2 --> LU1
    TI --> CN[CHANNEL N V-STRIP WRITE ITERATOR]
  
```

FIG. 95

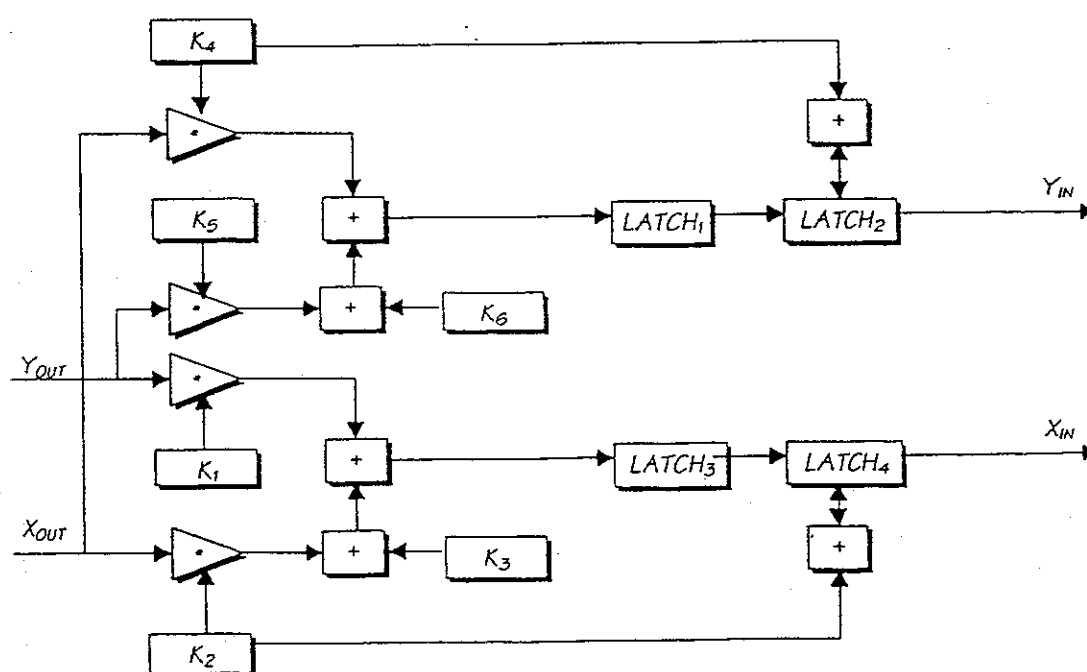


FIG. 96

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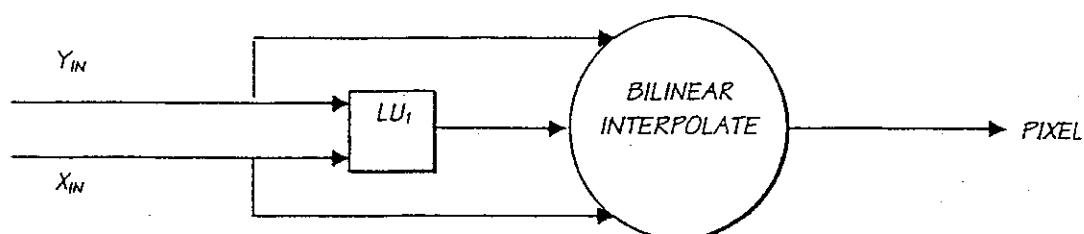


FIG. 97

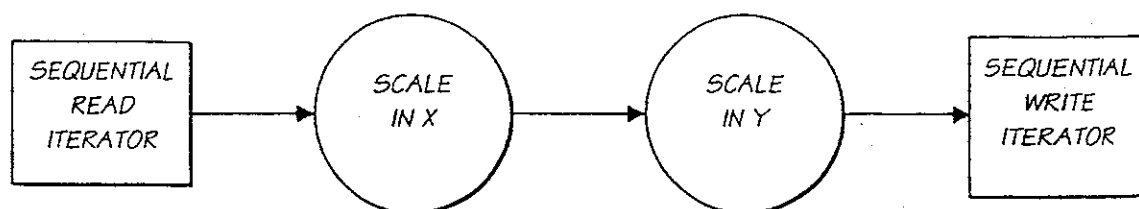


FIG. 98

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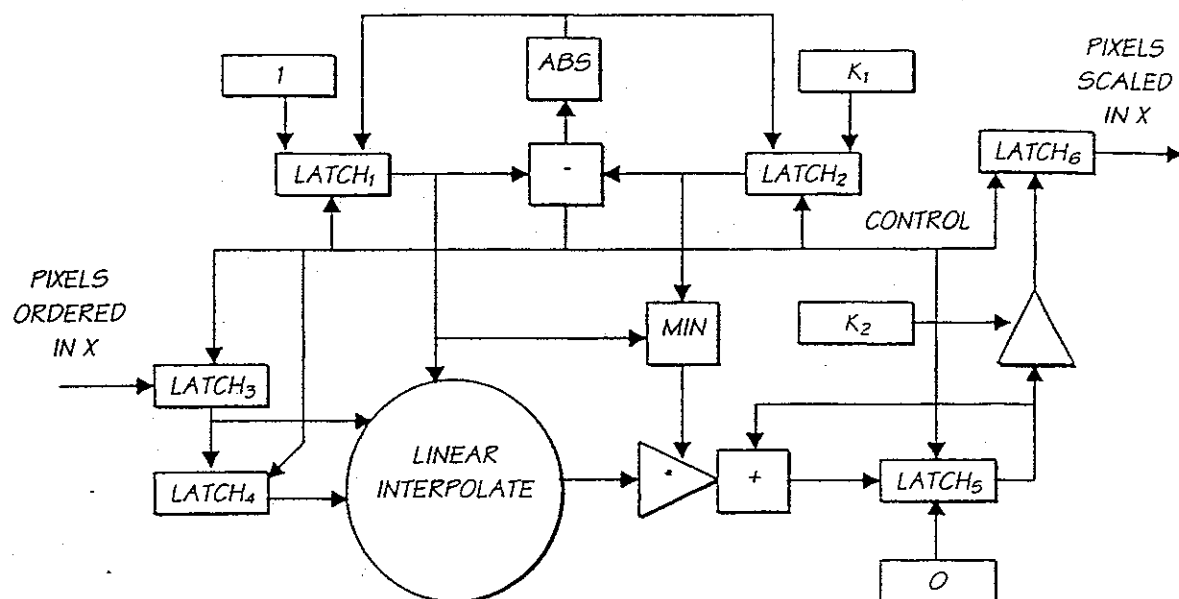


FIG. 99

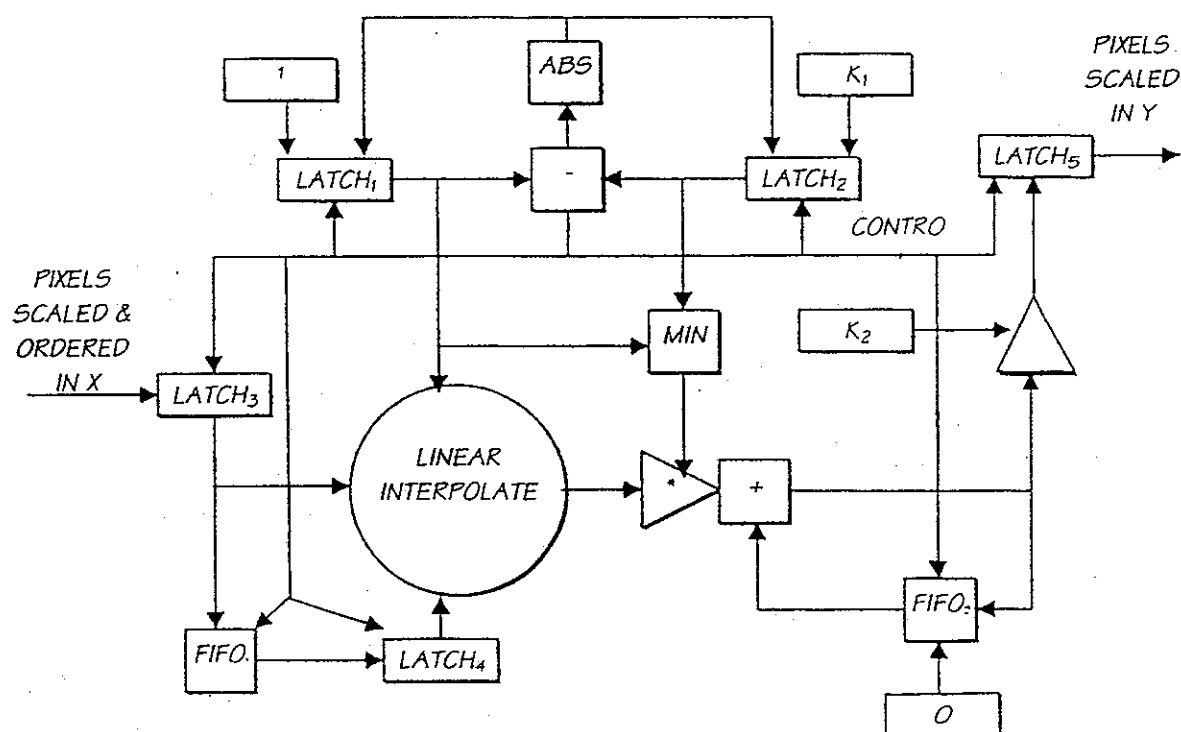
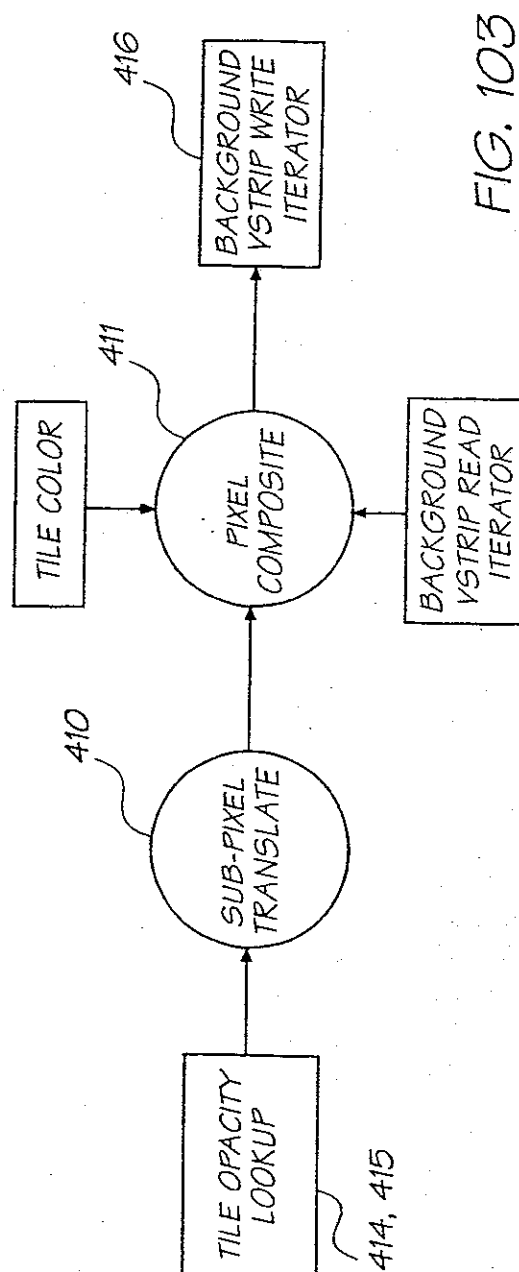
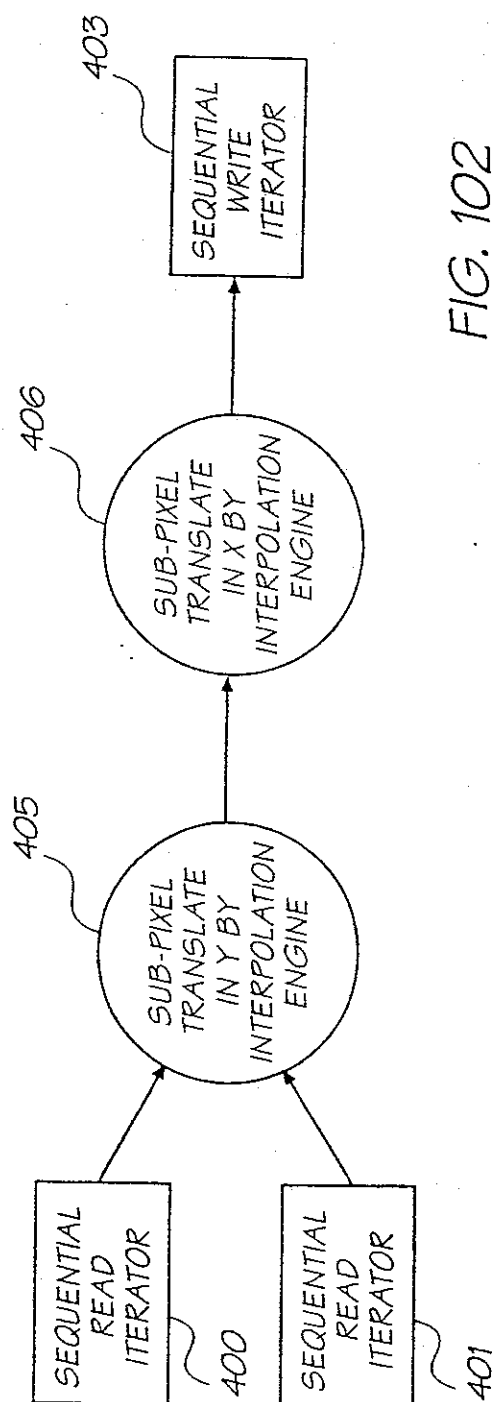
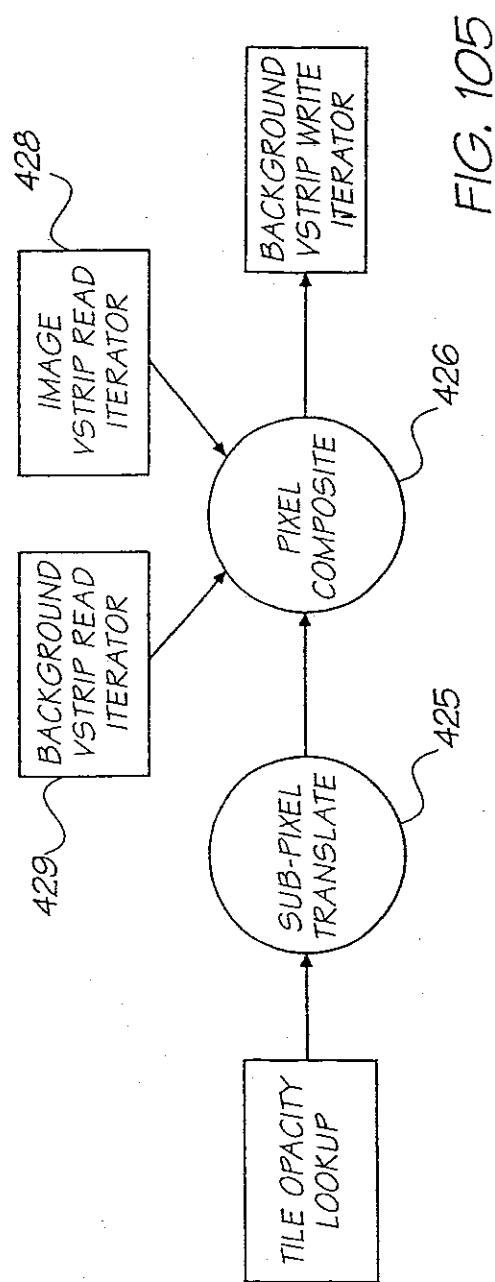
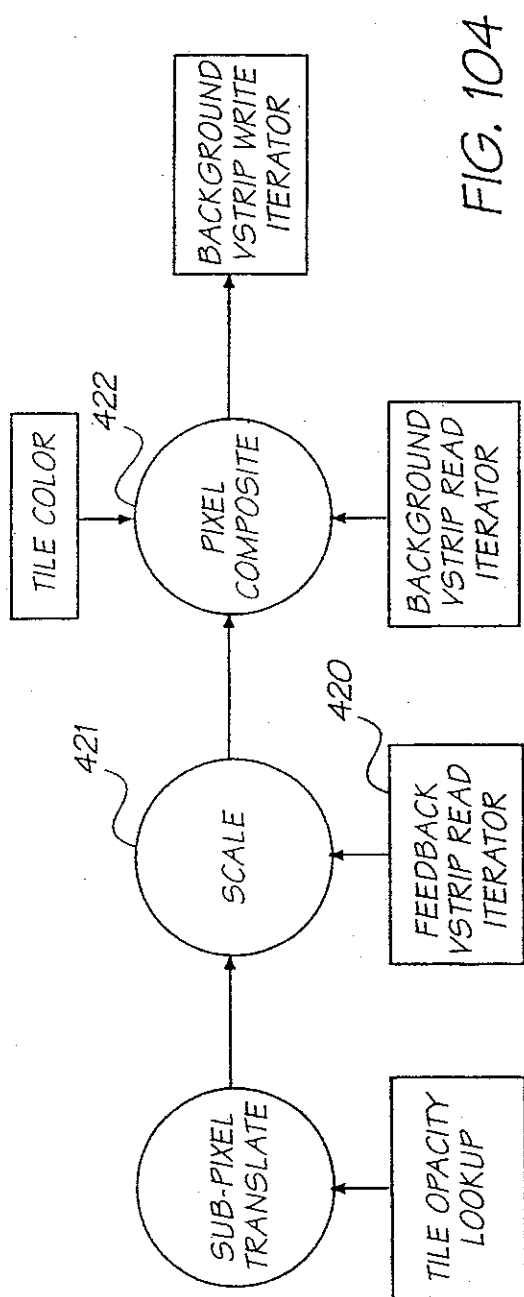


FIG. 100

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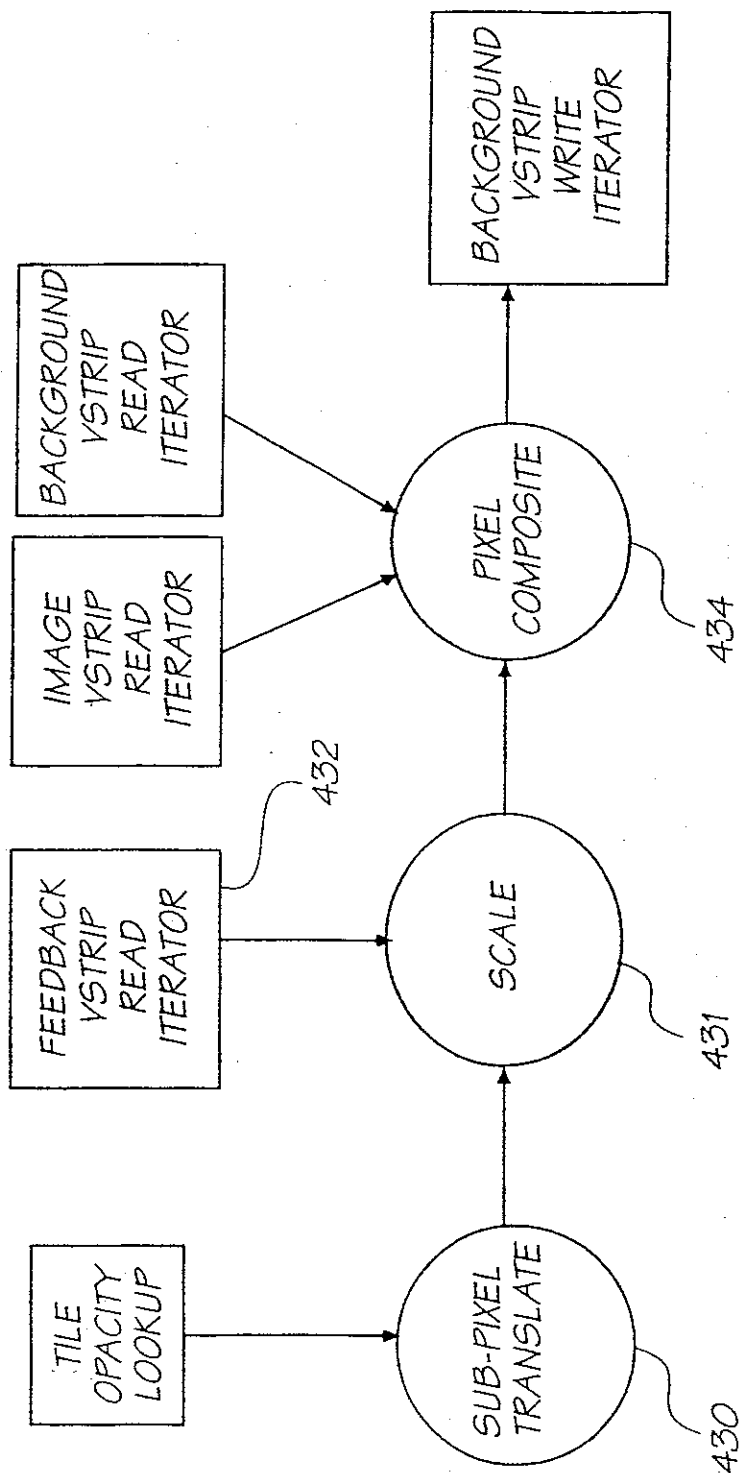


FIG. 106

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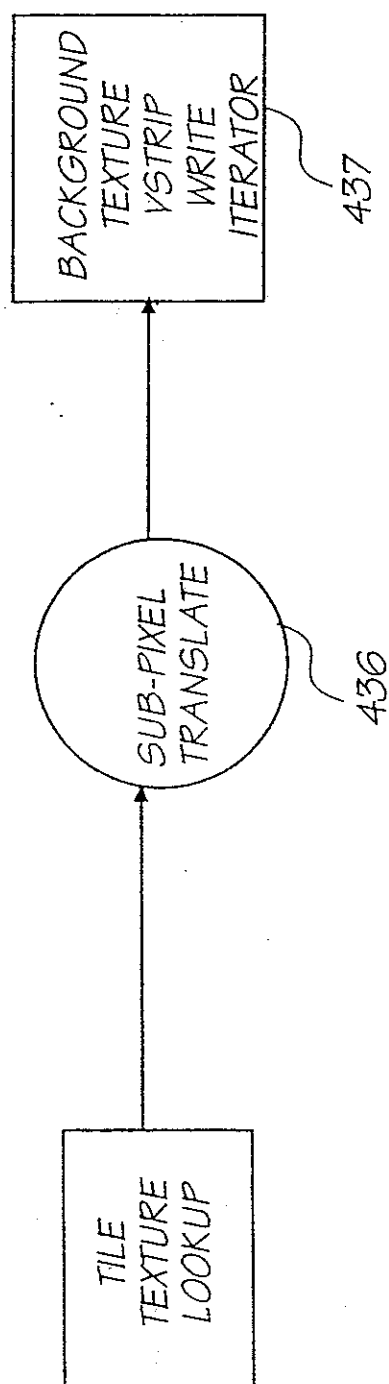


FIG. 107

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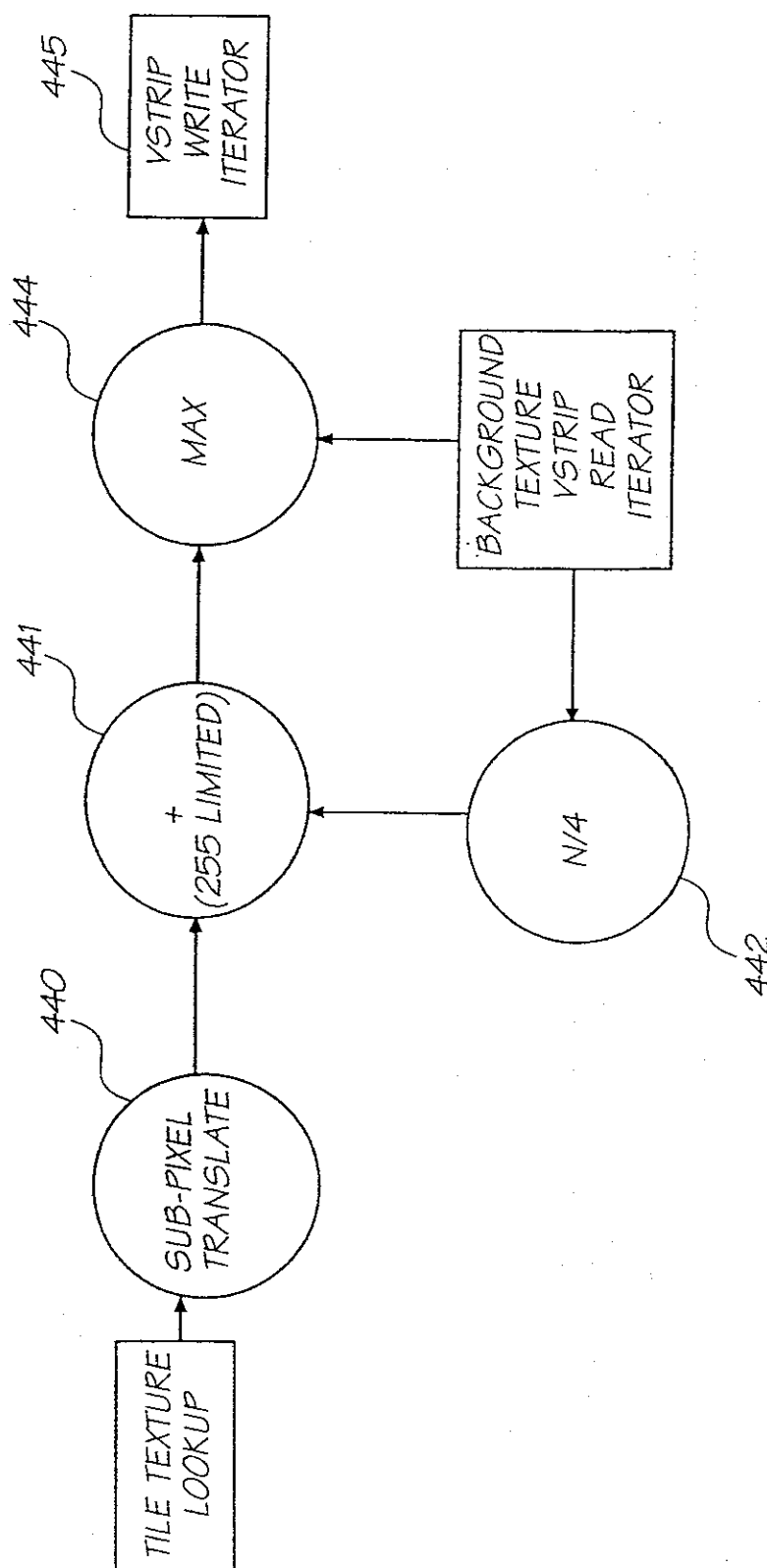


FIG. 108

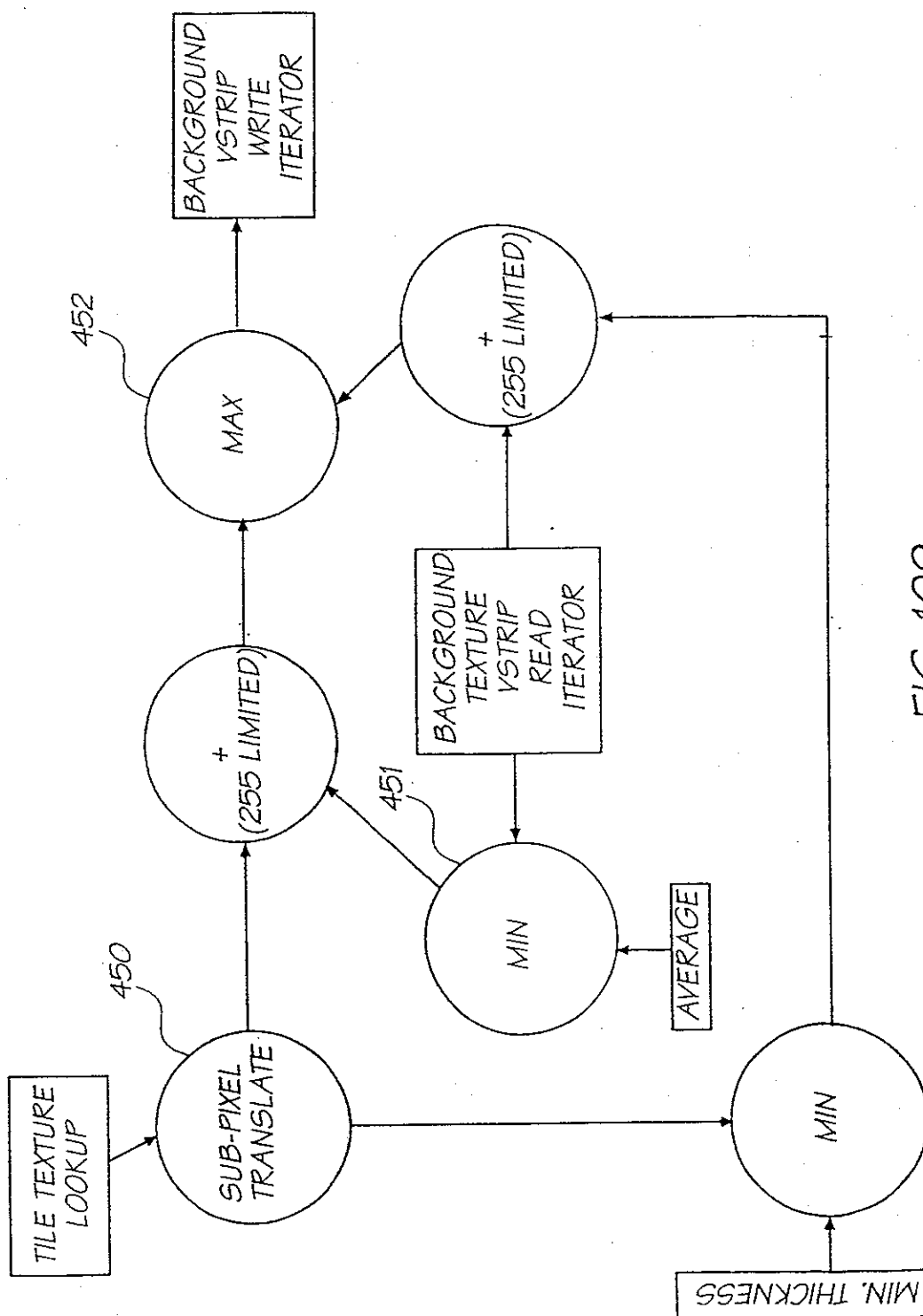


FIG. 109

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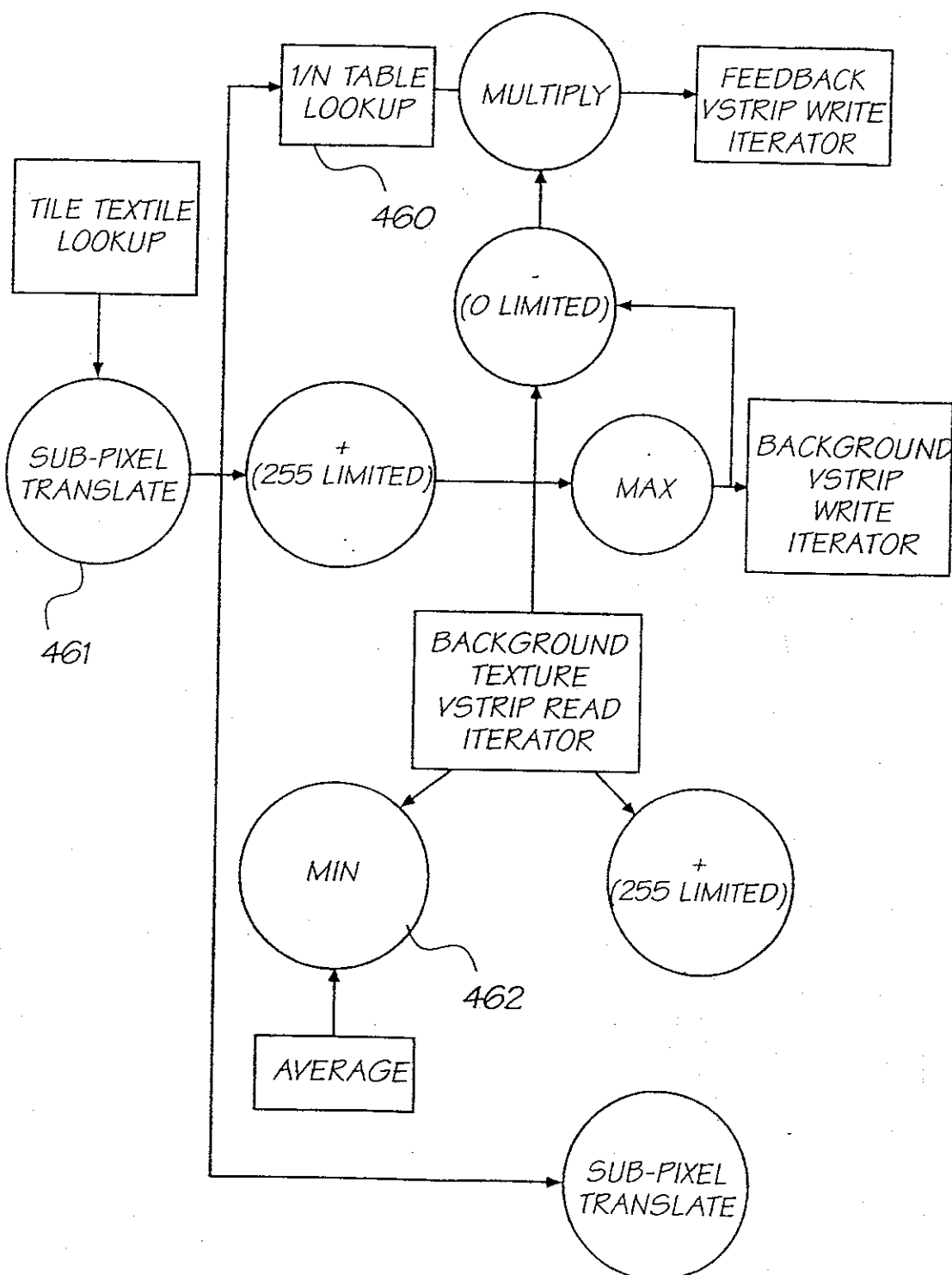


FIG. 110

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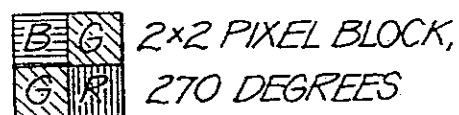
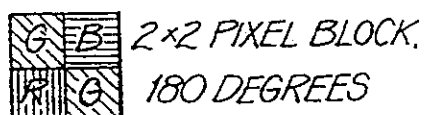
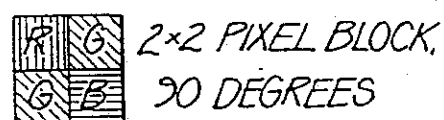
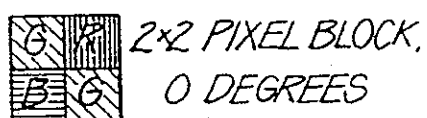
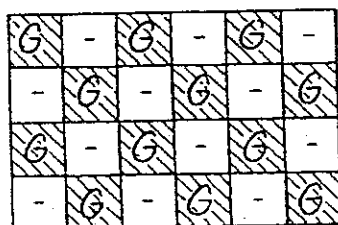


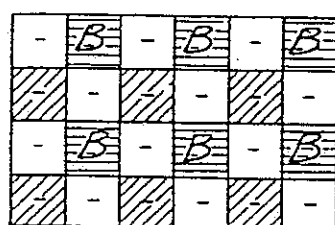
FIG. 111



 LINEAR INTERPOLATED
PIXELS
 ACTUAL PIXELS (NOT
INTERPOLATED)

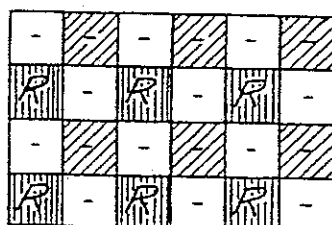
FIG. 112

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-  LINEAR INTERPOLATED PIXELS
-  BI-LINEAR INTERPOLATED PIXELS
-  ACTUAL PIXELS (NOT INTERPOLATED)

FIG. 113



-  LINEAR INTERPOLATED PIXELS
-  BI-LINEAR INTERPOLATED PIXELS
-  ACTUAL PIXELS (NOT INTERPOLATED)

FIG. 114

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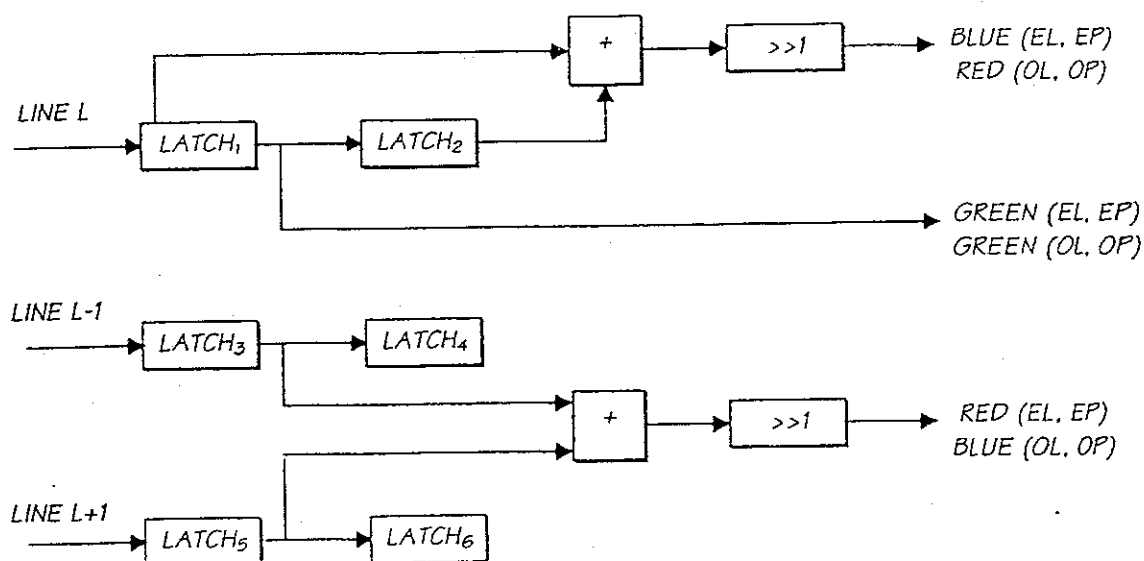


FIG. 115

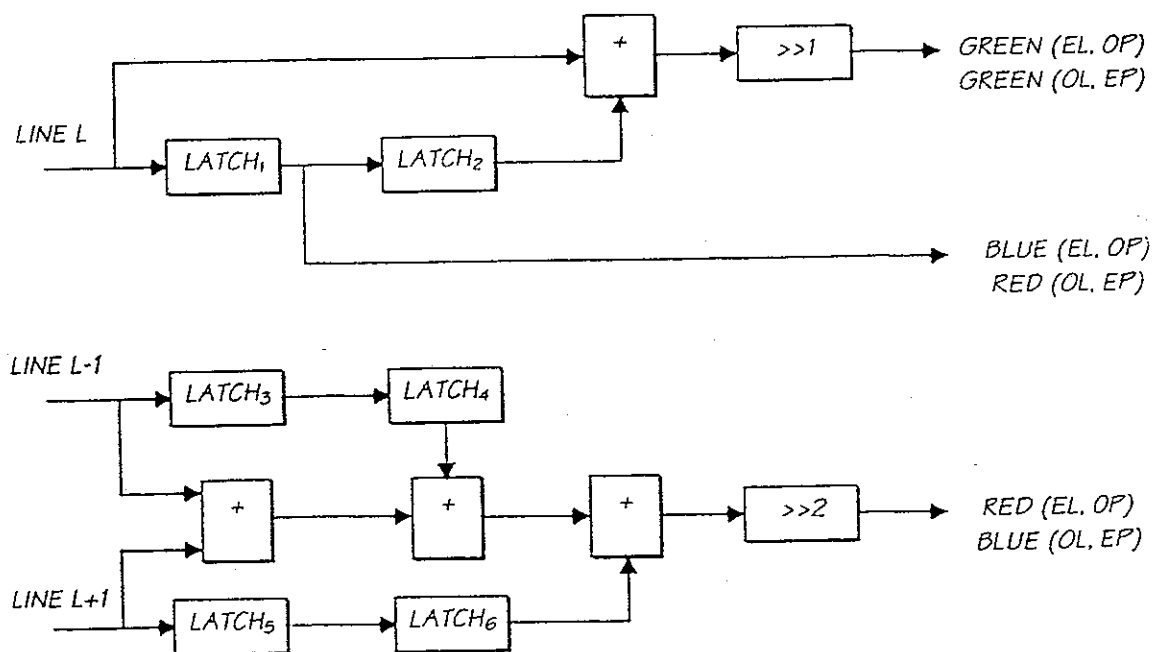


FIG. 116

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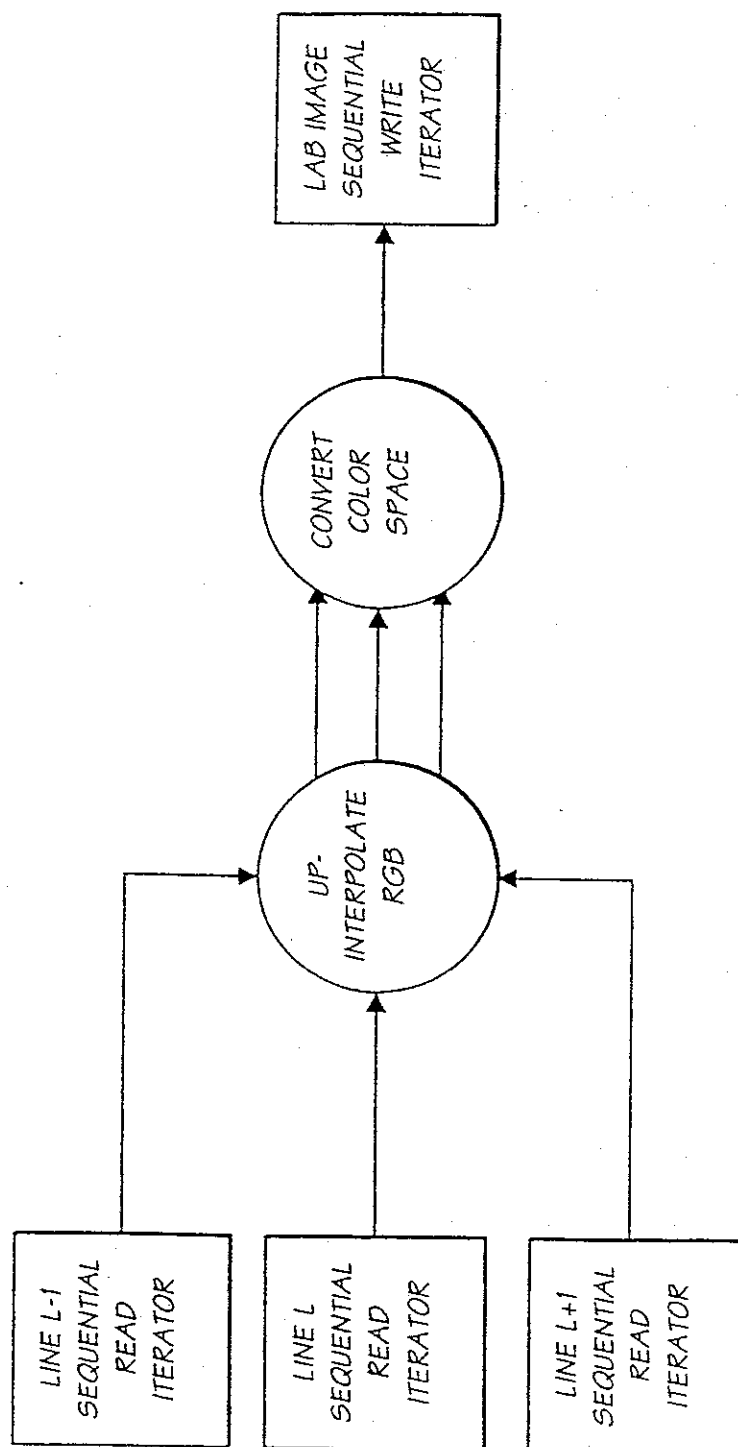


FIG. 117

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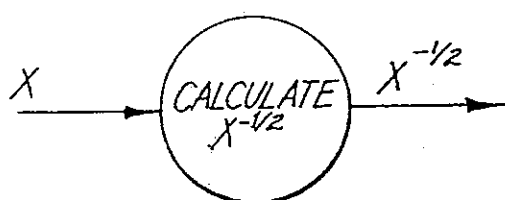


FIG. 118

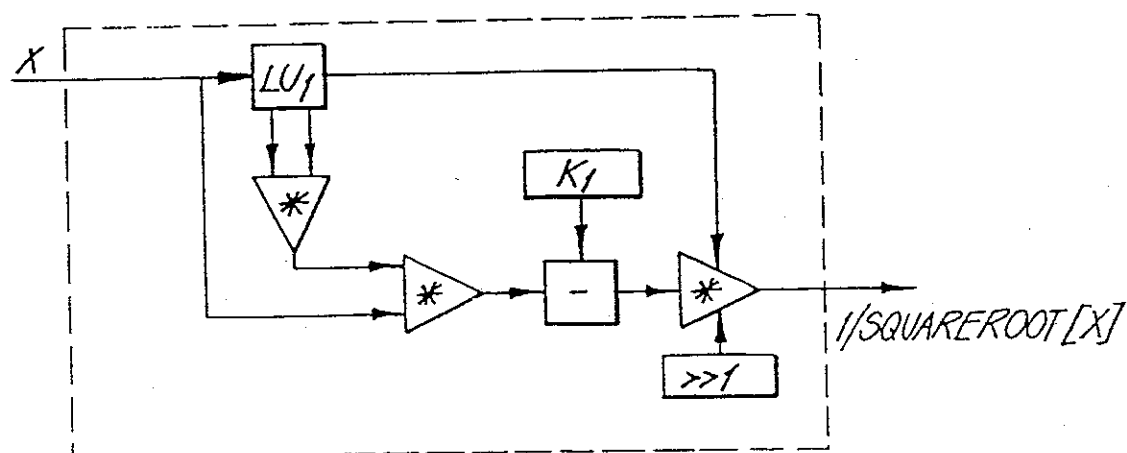


FIG. 119

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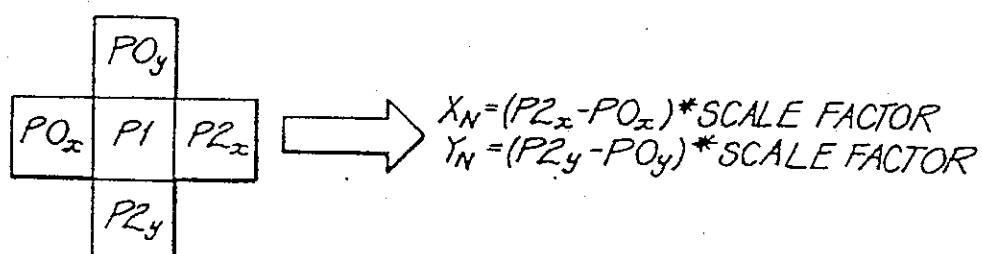


FIG. 120

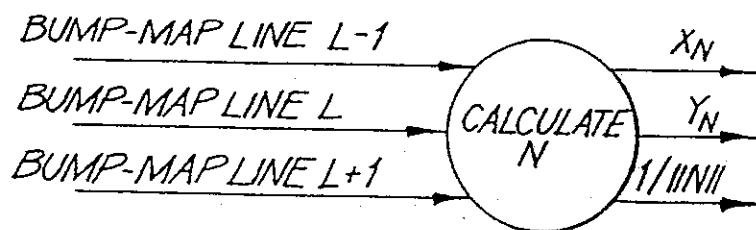


FIG. 121

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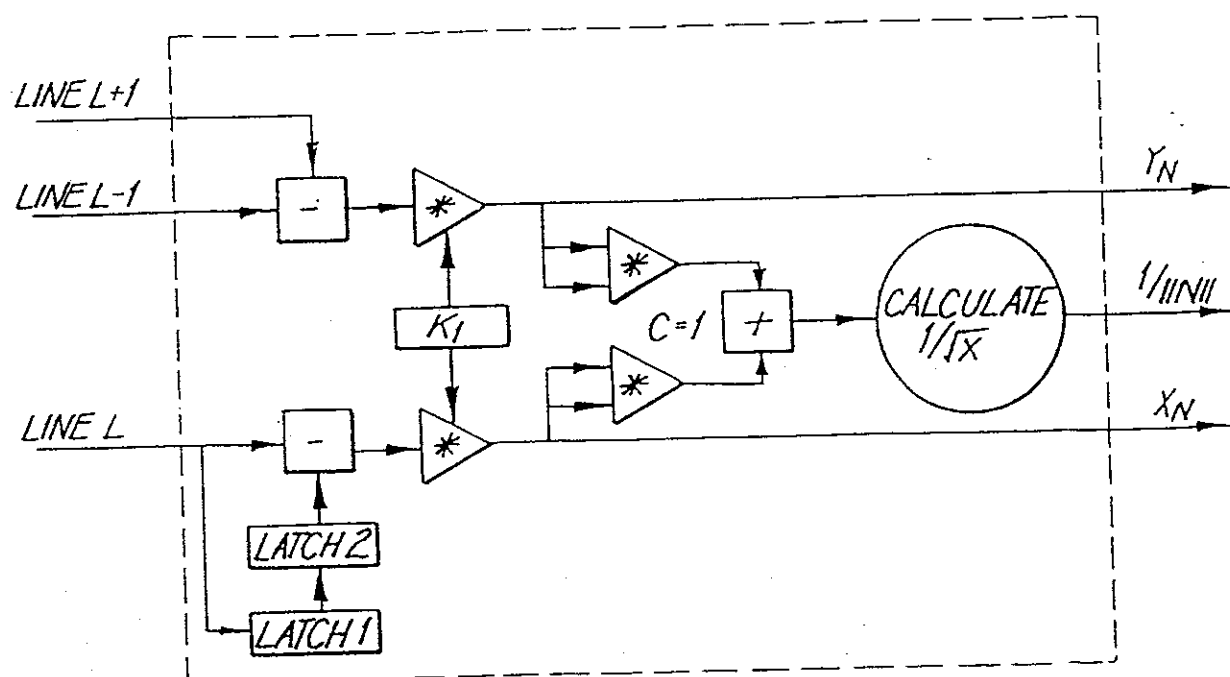


FIG. 122

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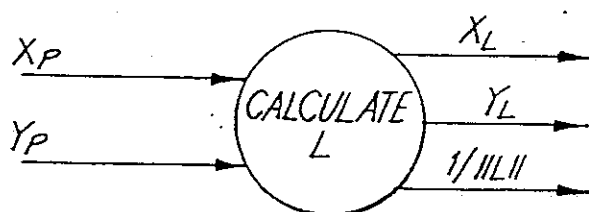


FIG. 123

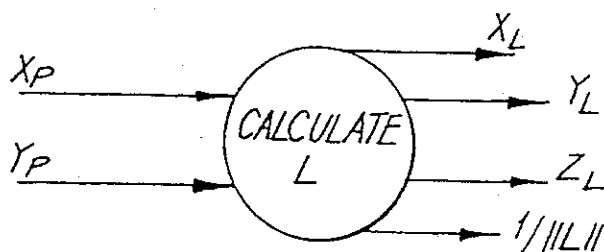


FIG. 124

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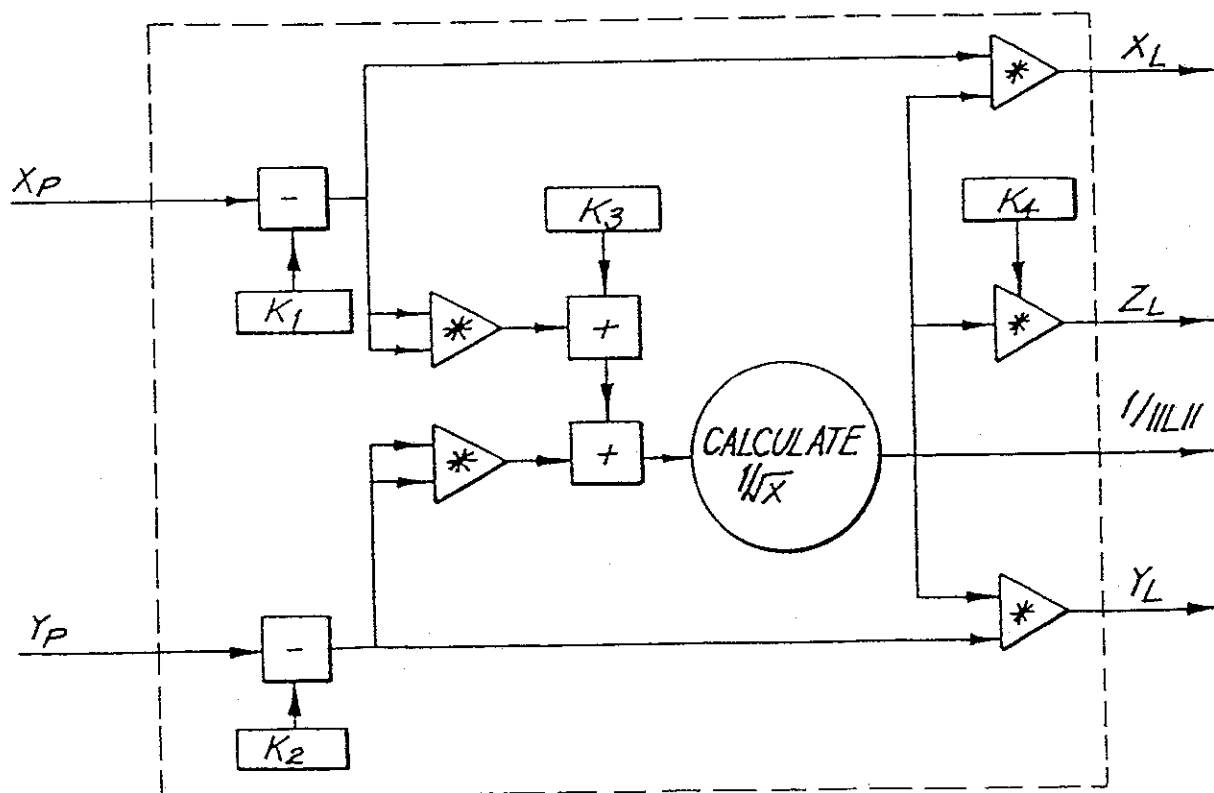


FIG. 125

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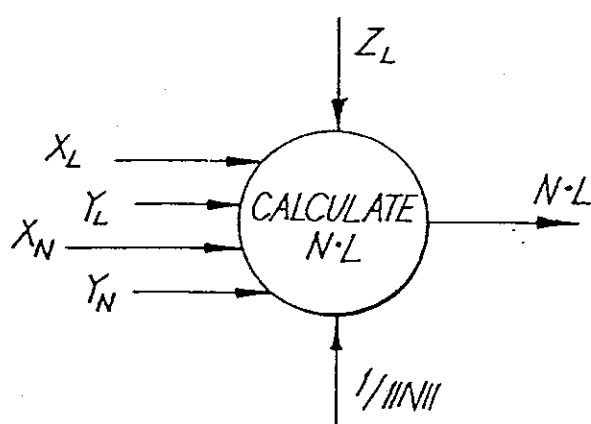


FIG. 126

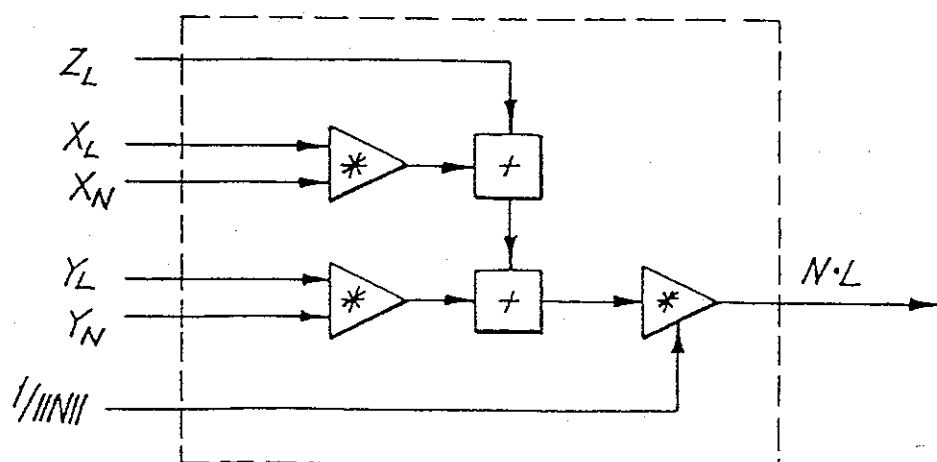


FIG. 127

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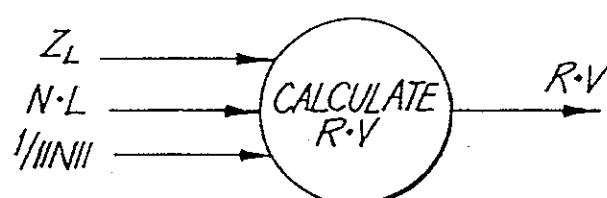


FIG. 128

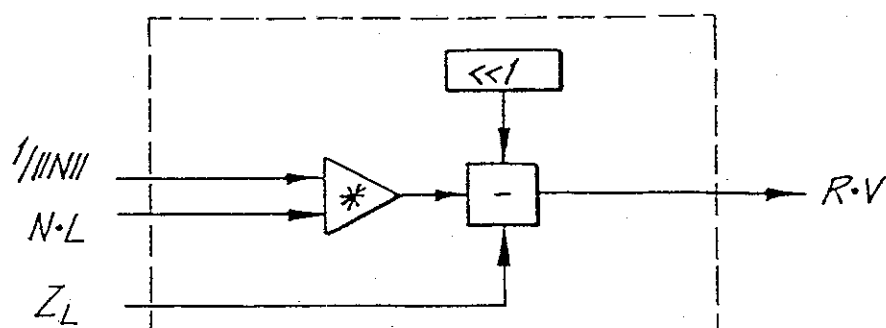


FIG. 129

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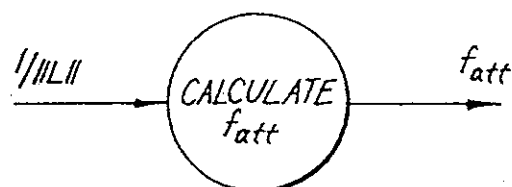


FIG. 130

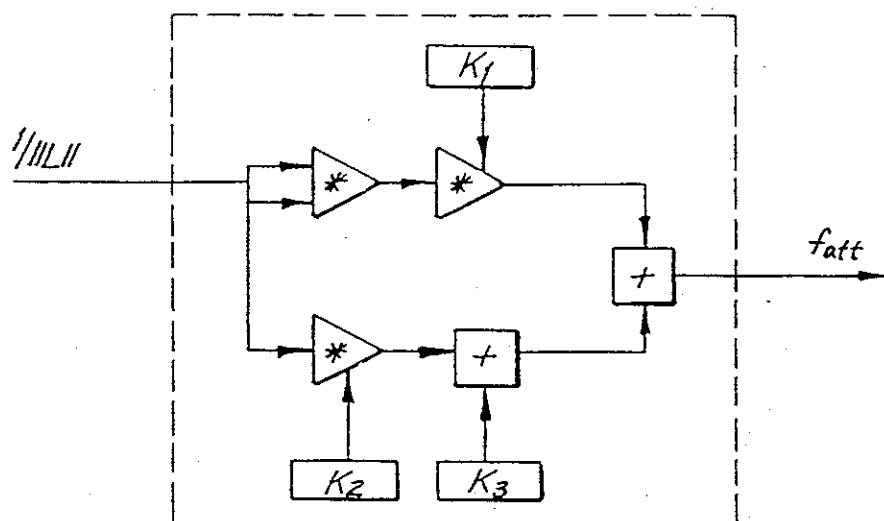


FIG. 131

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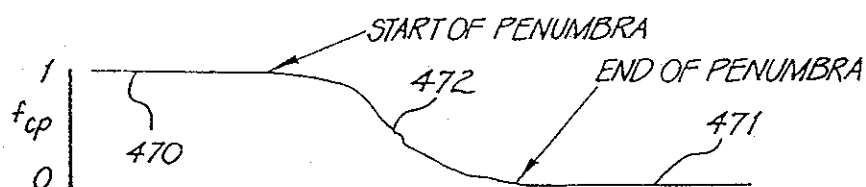


FIG. 132

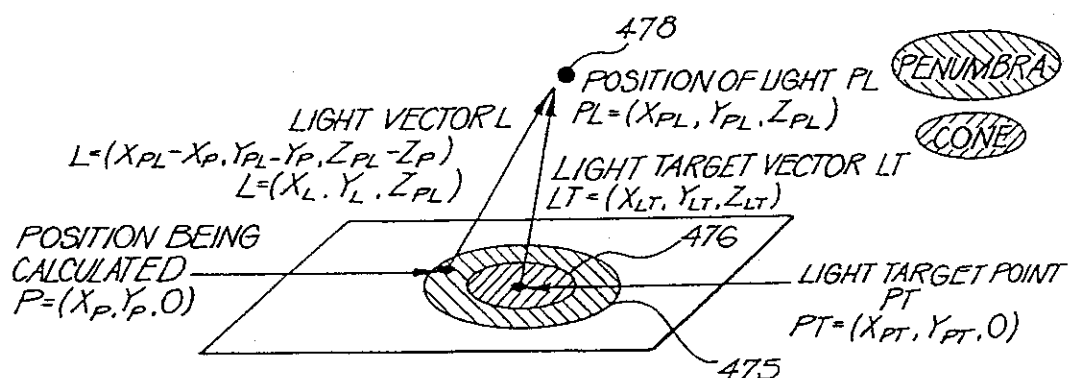


FIG. 133

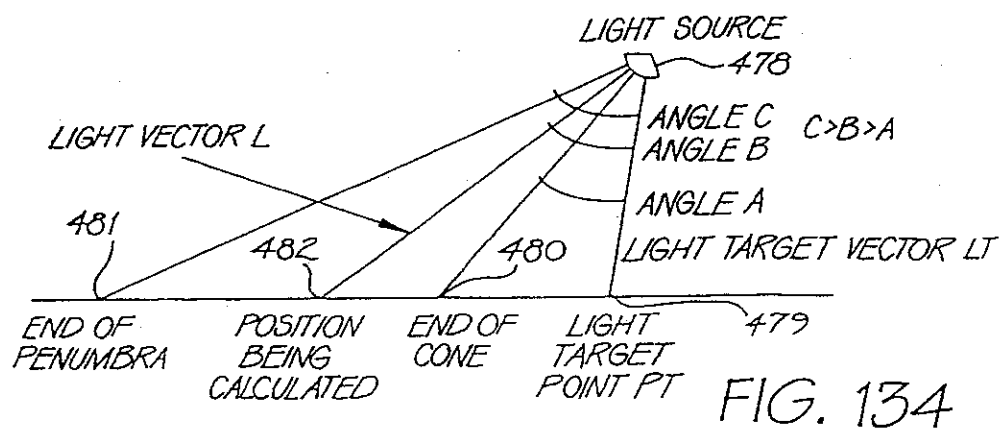


FIG. 134

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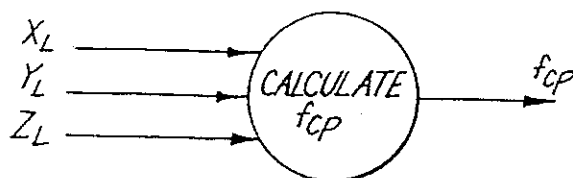


FIG. 135

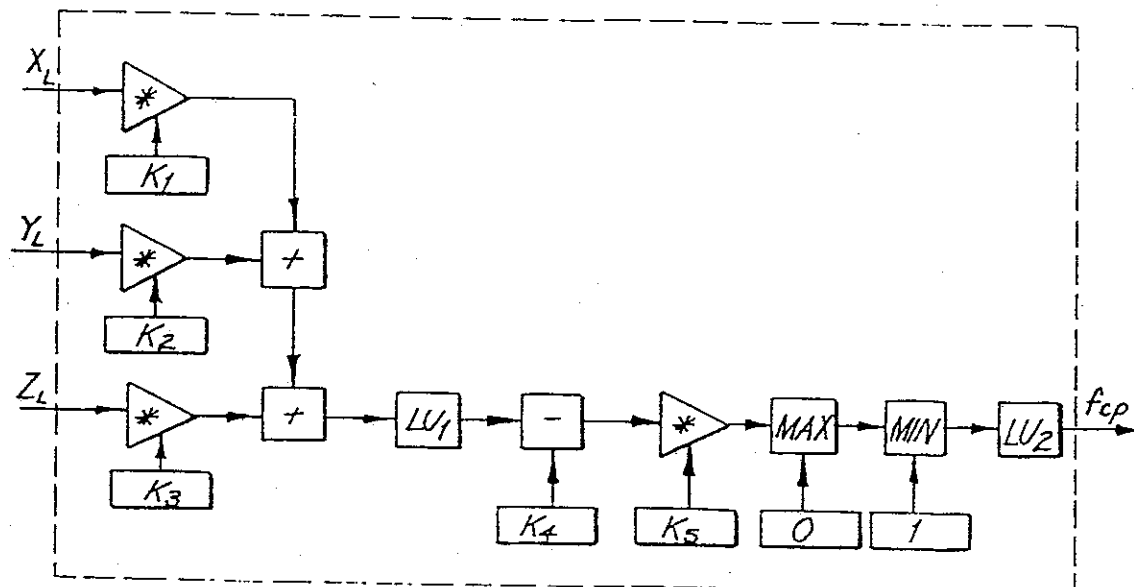


FIG. 136

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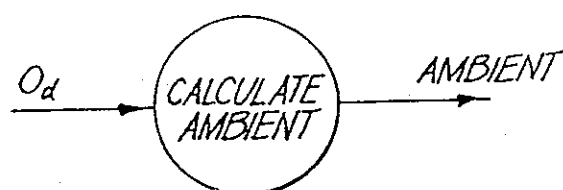


FIG. 137

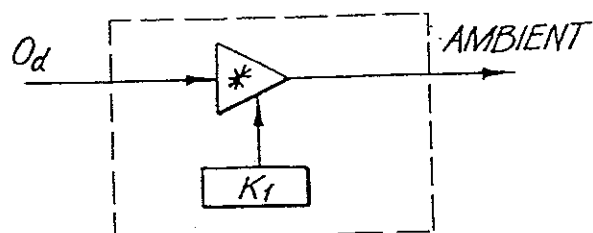


FIG. 138

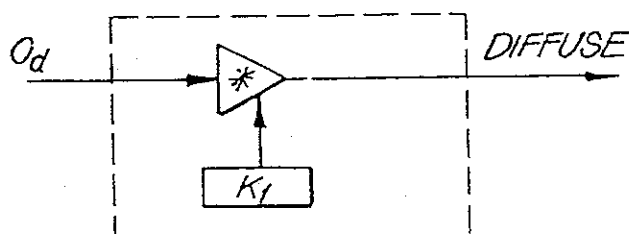


FIG. 139

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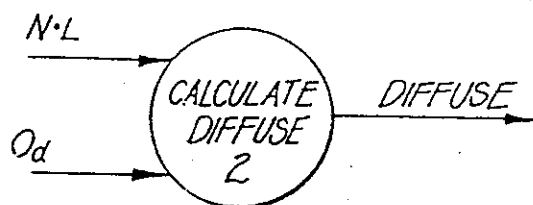


FIG. 140

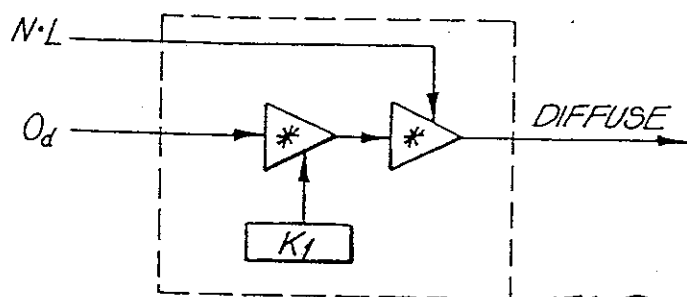


FIG. 141

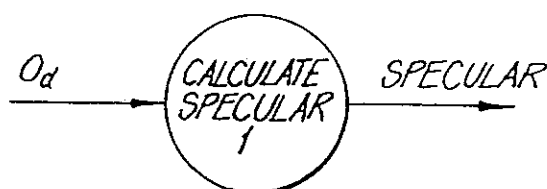


FIG. 142

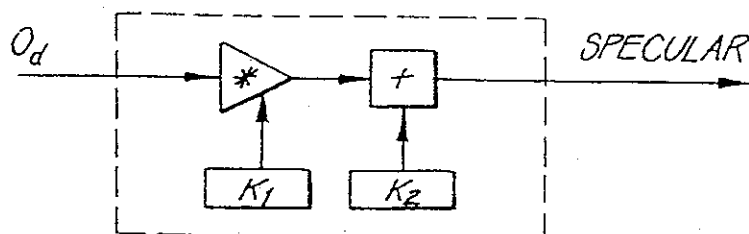


FIG. 143

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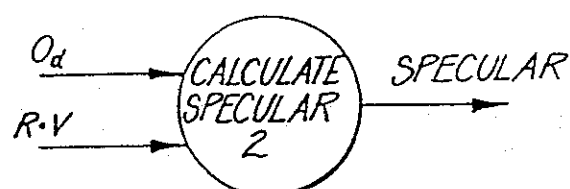


FIG. 144

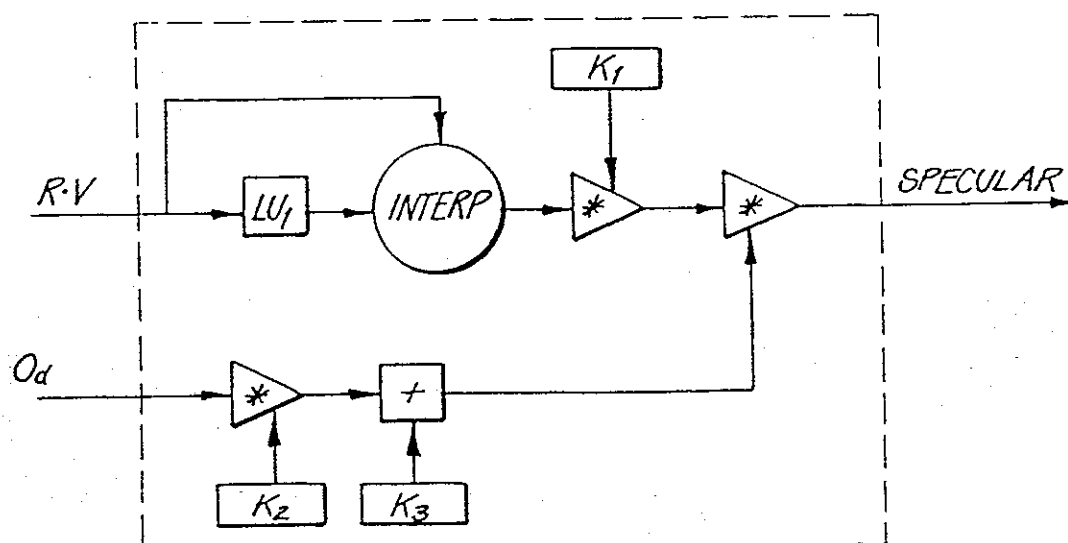


FIG. 145

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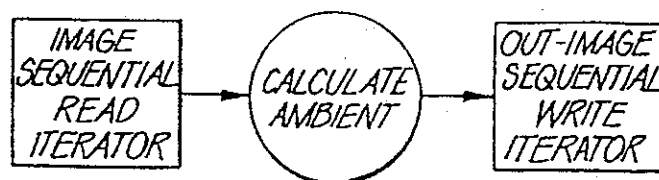


FIG. 146

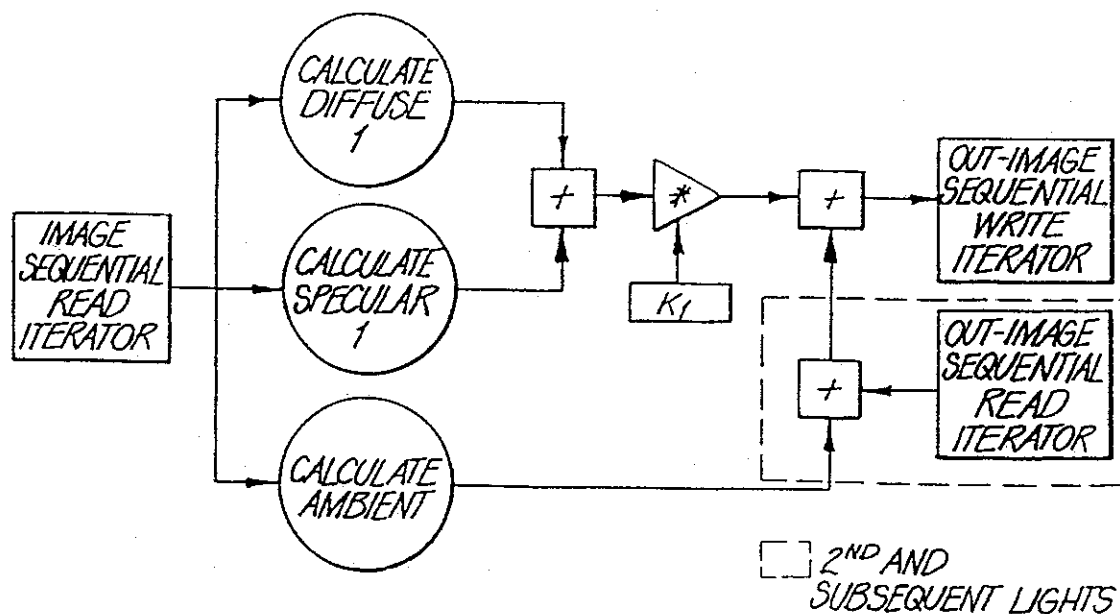


FIG. 147

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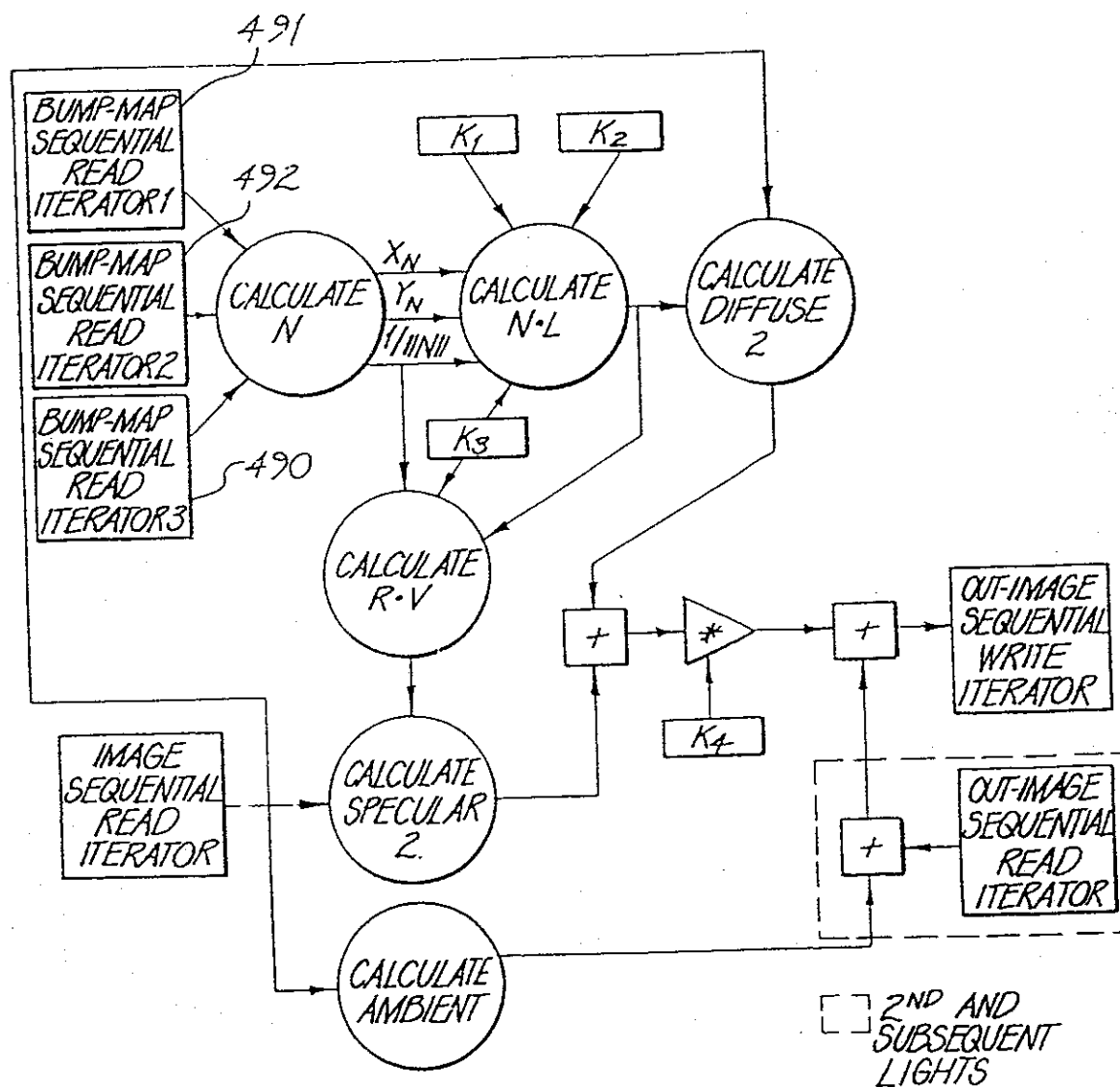


FIG. 148

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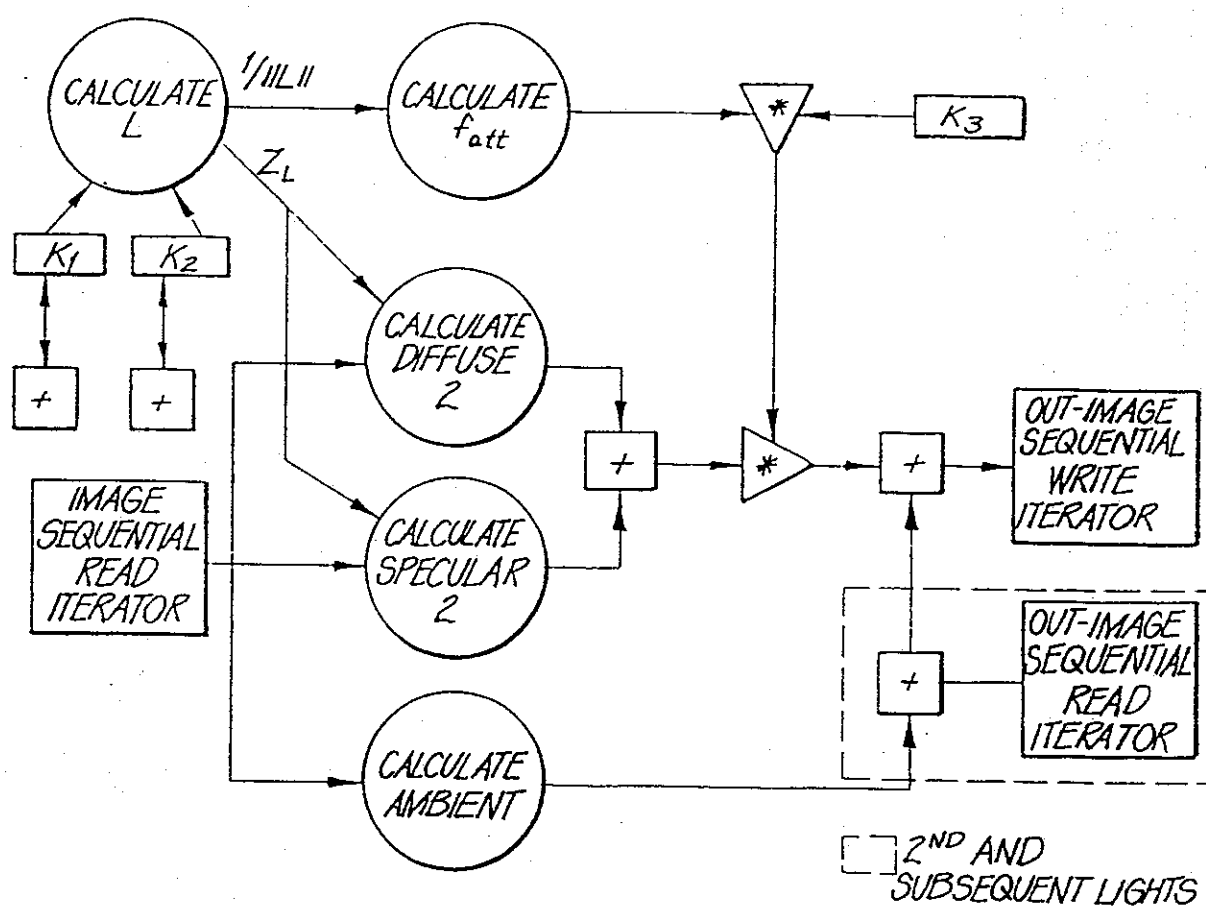


FIG. 149

The flowchart illustrates a ray-tracing algorithm for global illumination, including direct and indirect lighting calculations. The process begins with three parallel input blocks: "BUMP-MAP SEQUENTIAL READ ITERATOR 1", "BUMP-MAP SEQUENTIAL READ ITERATOR 2", and "BUMP-MAP SEQUENTIAL READ ITERATOR 3". These feed into a central "CALCULATE N" block. "CALCULATE N" outputs X_N and Y_N to "CALCULATE N-L", and a value $1/|IIN|$ to "CALCULATE R-V". "CALCULATE N-L" and "CALCULATE L" are interconnected, exchanging X_L , Y_L , and Z_L . "CALCULATE L" also receives inputs from K_1 and K_2 , which are themselves outputs of addition blocks. "CALCULATE L" outputs to "CALCULATE fott", which then passes through a multiplication block with K_3 . The output of this multiplication block is added to the output of "CALCULATE R-V" in an addition block. The result is then multiplied by the output of "CALCULATE SPECULAR 2". This product is added to the output of "CALCULATE AMBIENT" in another addition block. The final result is added to the output of the "OUT-IMAGE SEQUENTIAL READ ITERATOR" in a final addition block. A dashed box encloses the last two addition blocks, labeled "2ND AND SUBSEQUENT LIGHTS". The "OUT-IMAGE SEQUENTIAL WRITE ITERATOR" receives the final output of the algorithm.

FIG. 150

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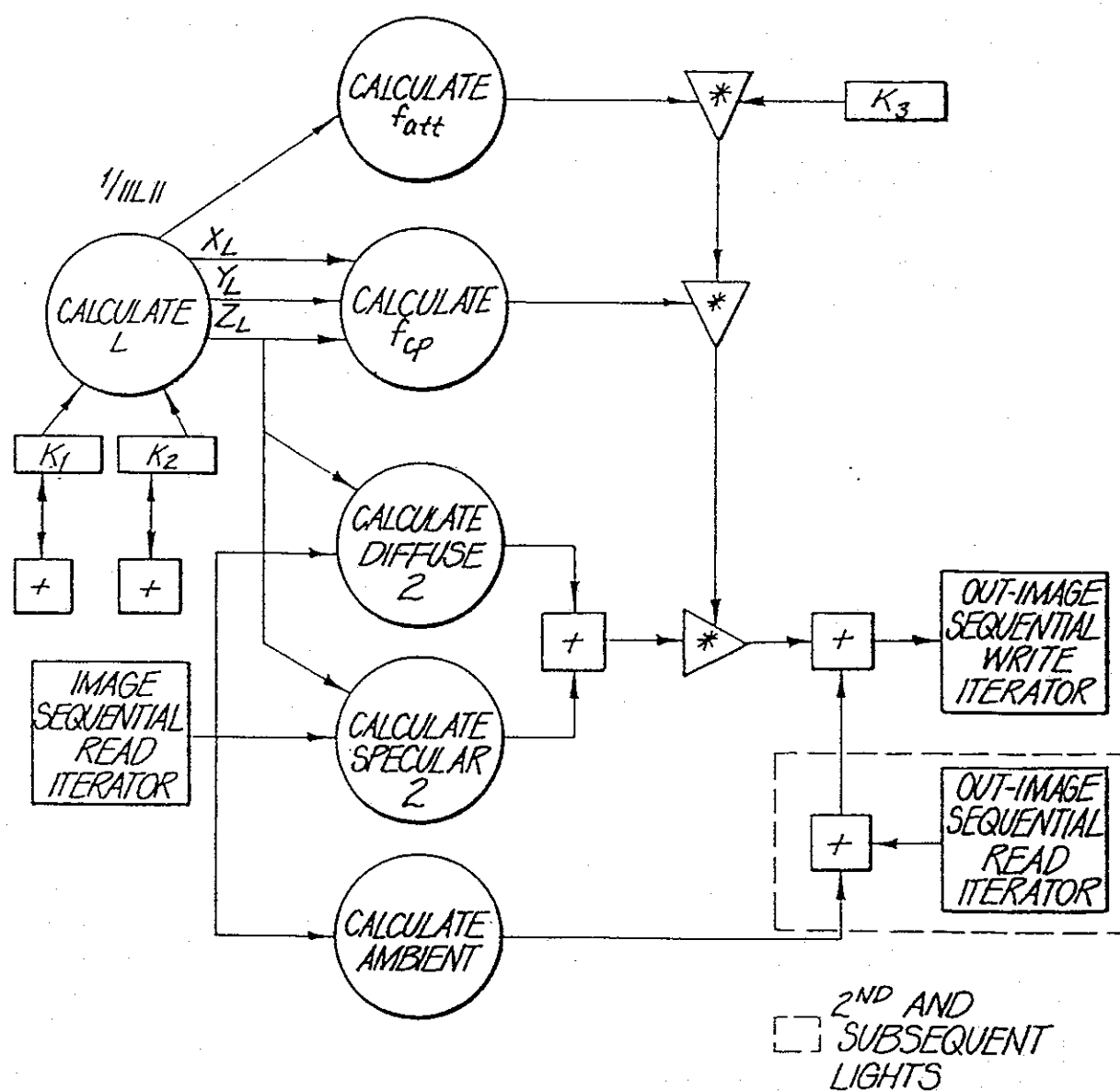


FIG. 151

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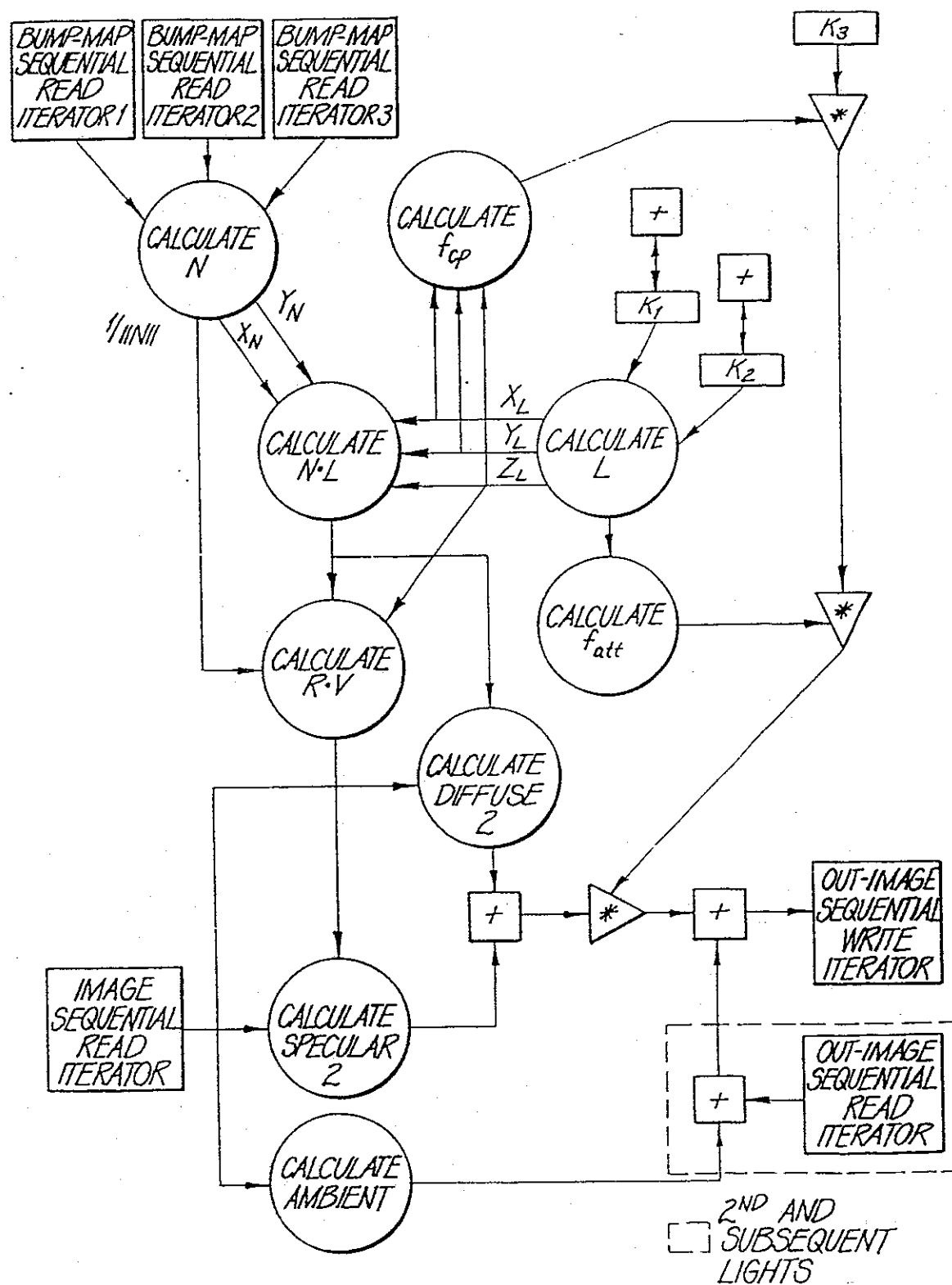
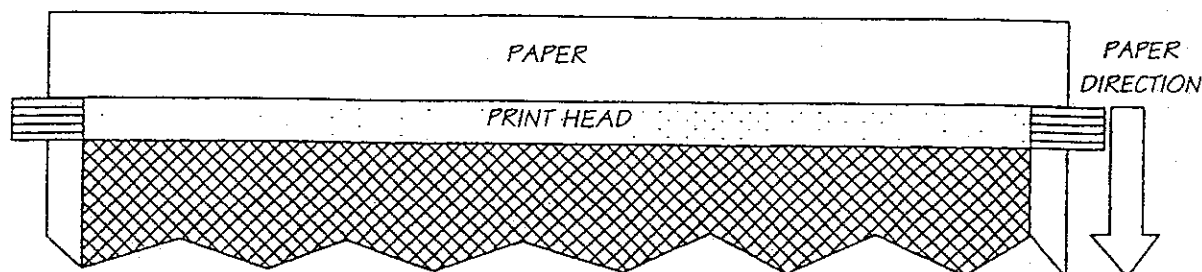
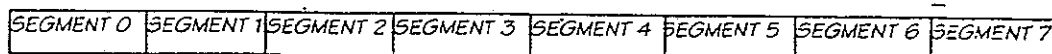


FIG. 152

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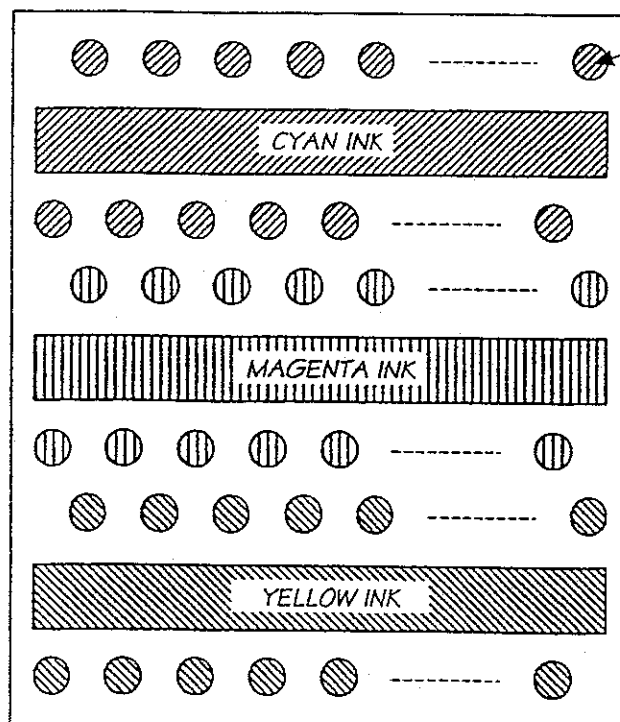


8 PRINT HEAD SEGMENTS IN PRINT HEAD



1250 μ M
(375 DOTS PER SEGMENT ROW, OR 750 DOTS PER SEGMENT)

1 DOT IS 16.6 μ M IN DIAMETER
(A 100 μ M SQUARE = 6 X 6 = 36 DOTS)



466.6 μ M
(28 DOTS)

33.3 μ M
(2 DOTS)

133.3 μ M
(8 DOTS)

EACH SEGMENT CONTAINING 6 ROWS OF DOTS, ODD AND EVEN

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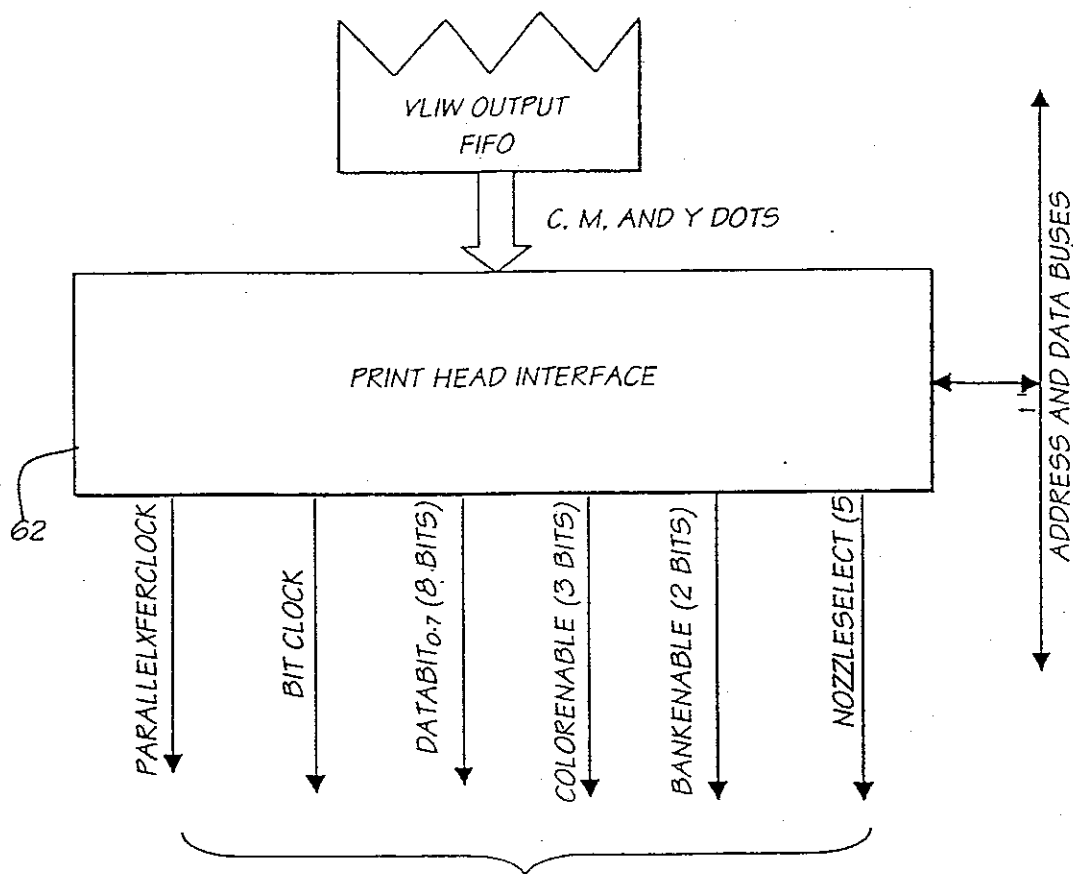


FIG. 154

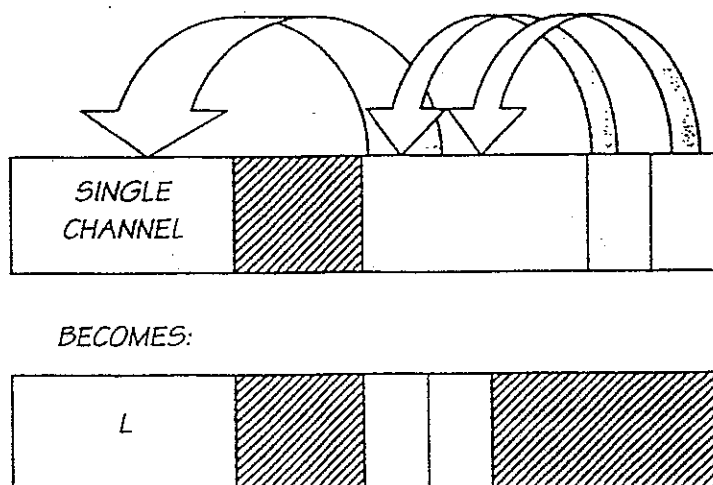


FIG. 155

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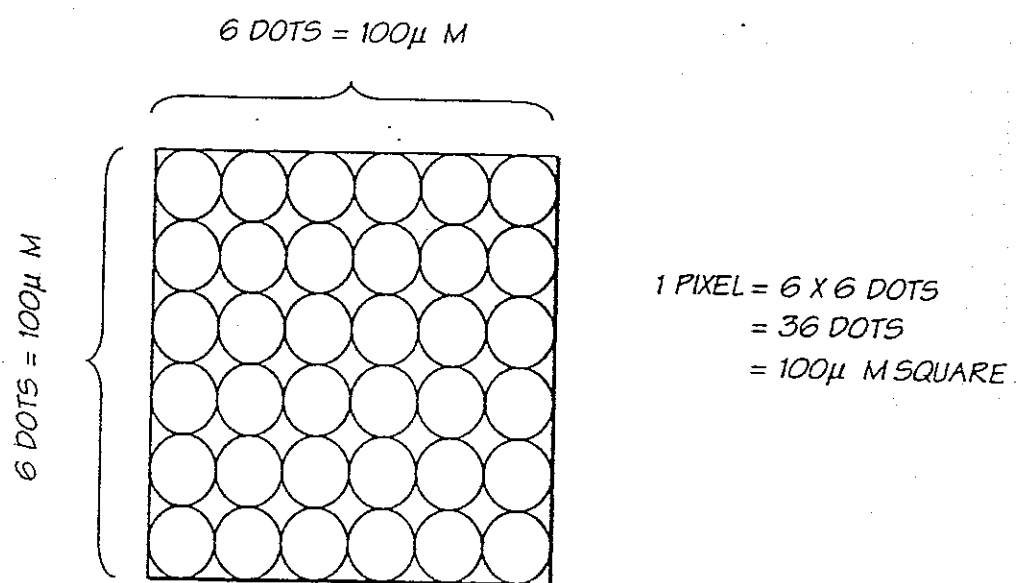


FIG. 156

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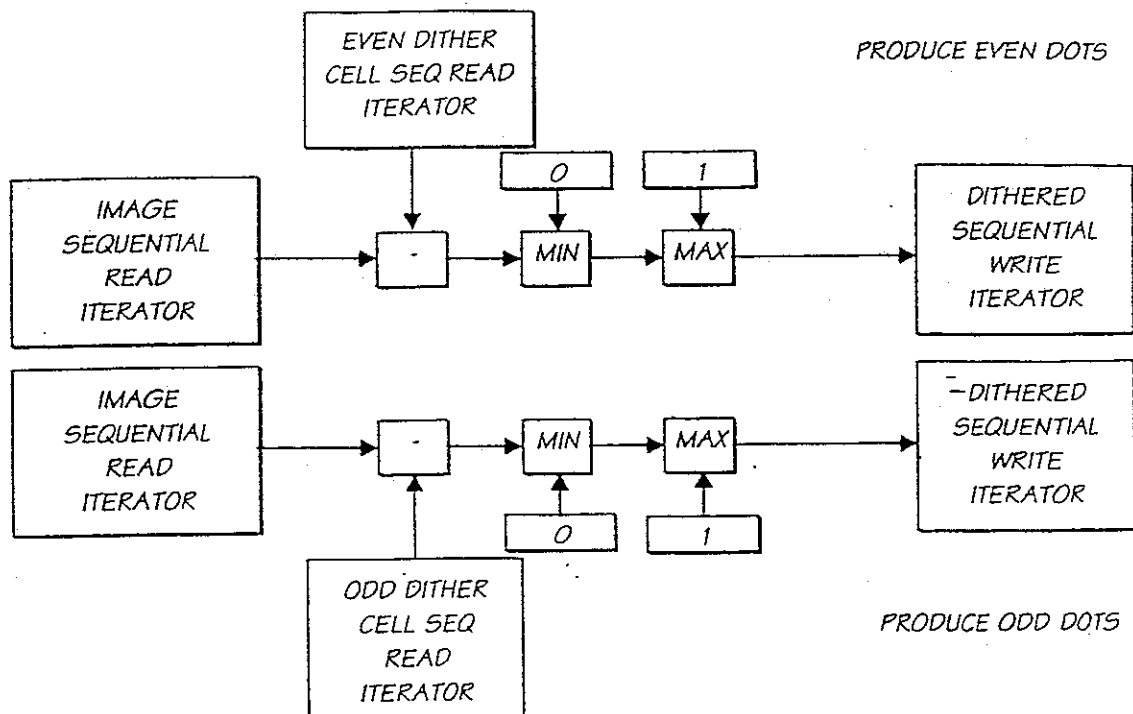
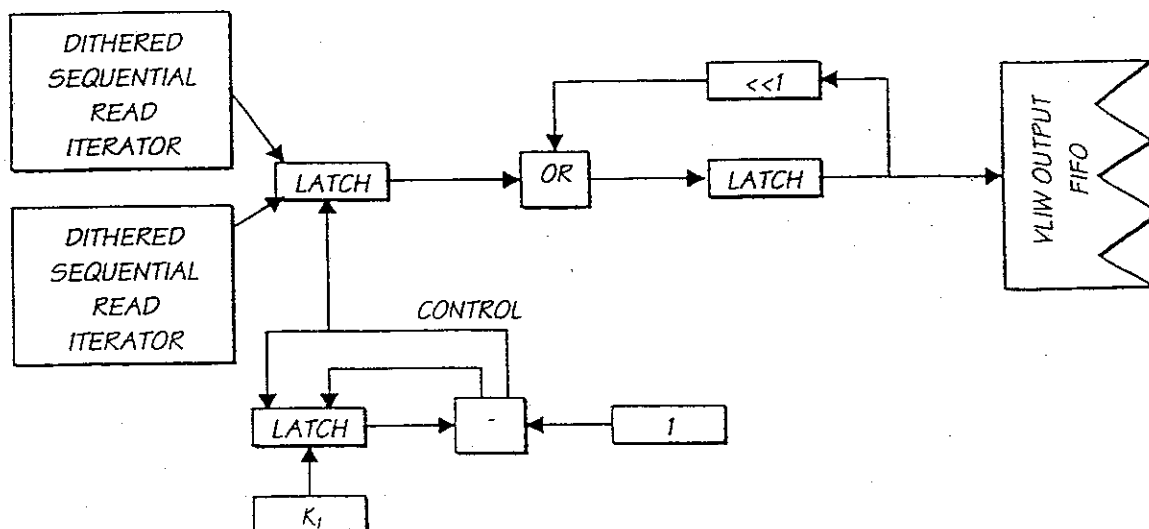
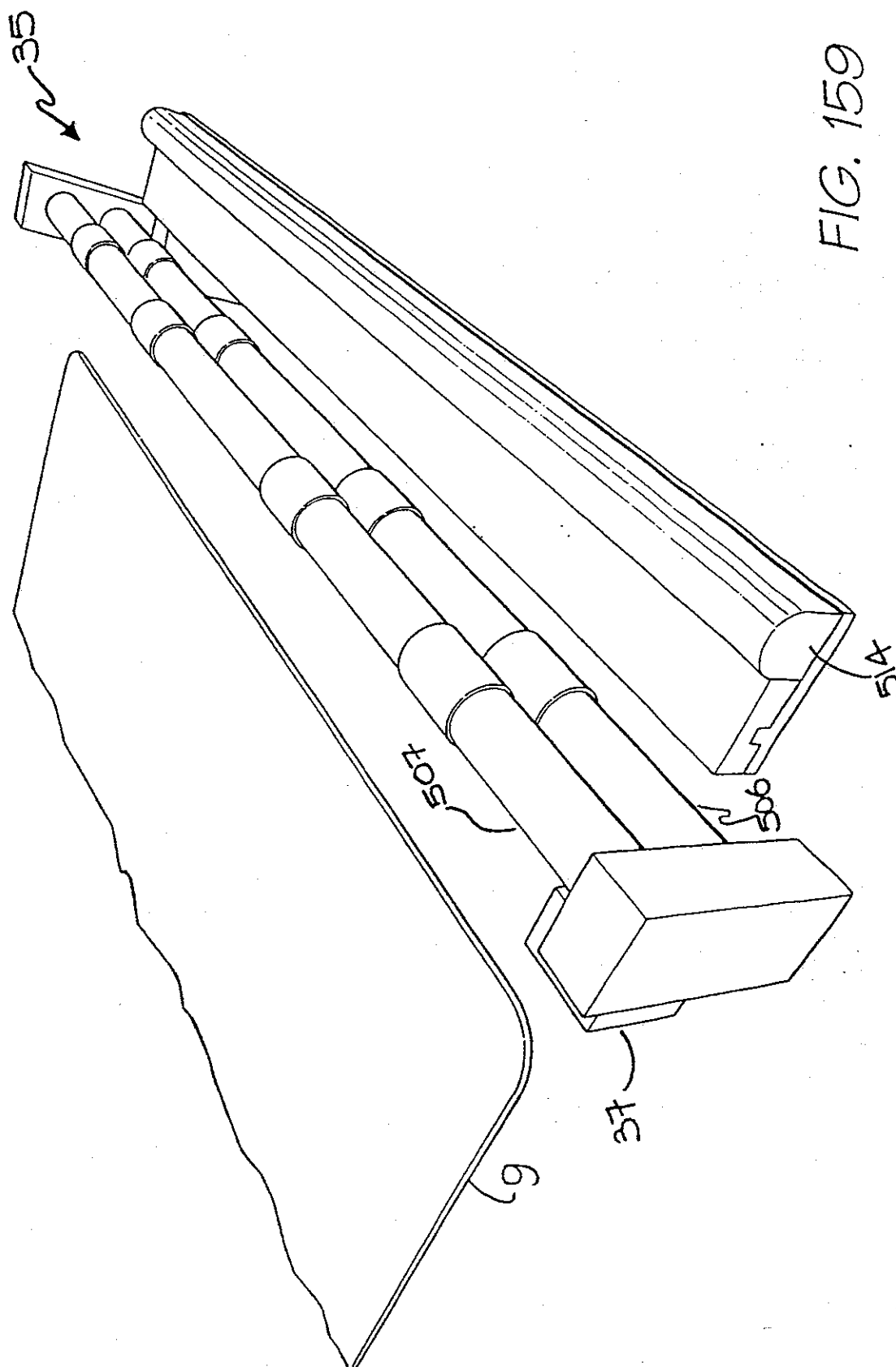
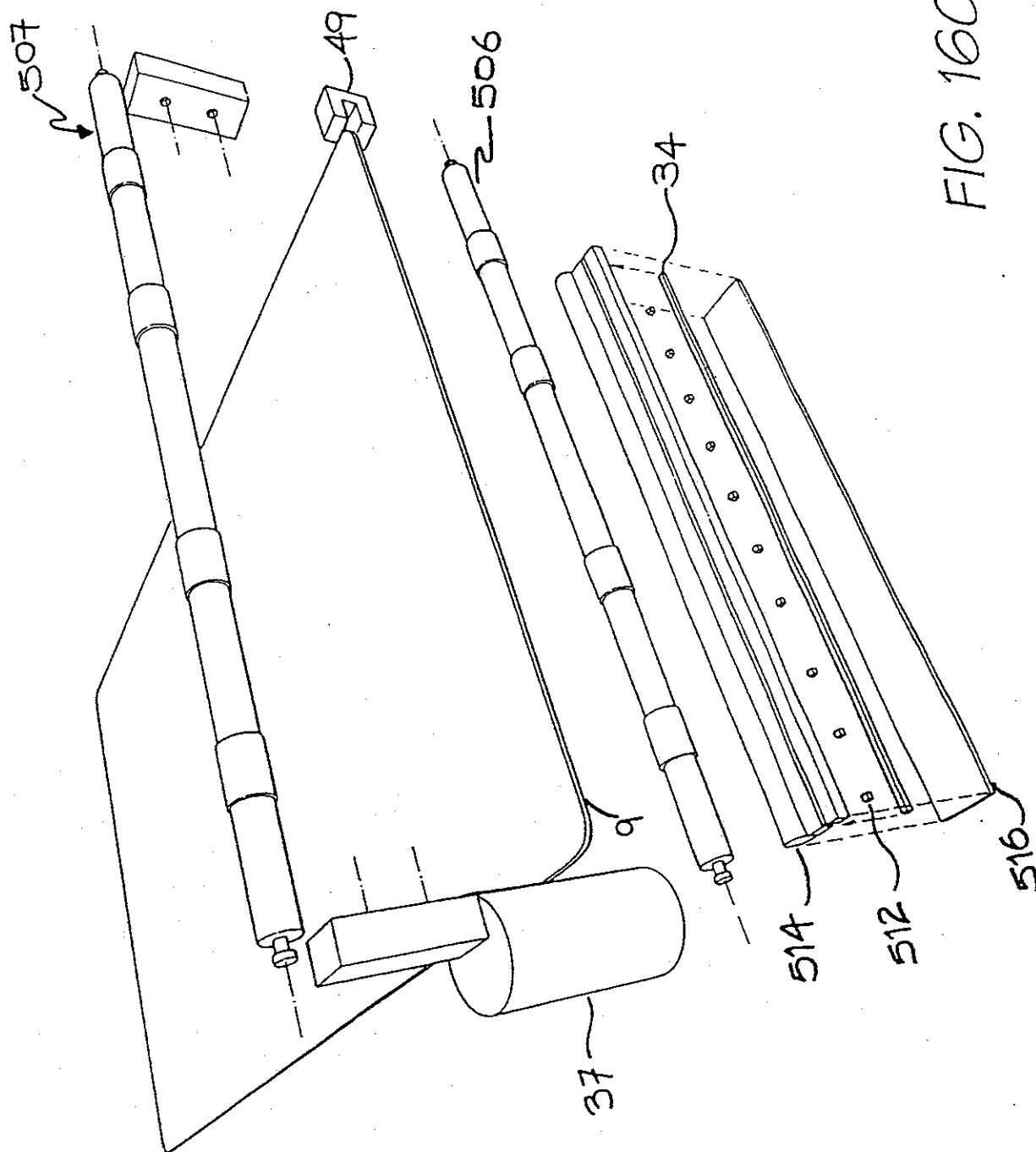


FIG. 157

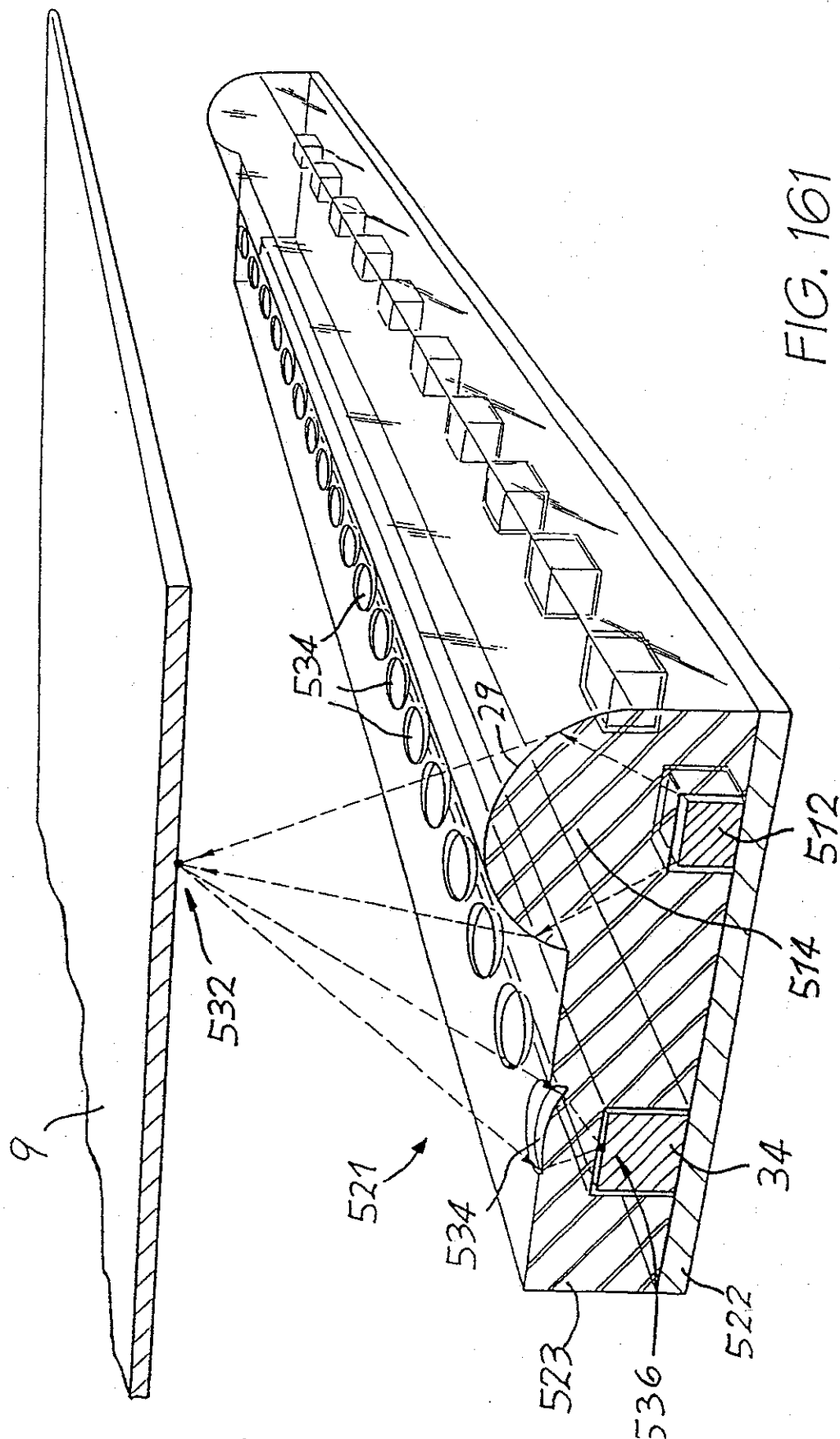


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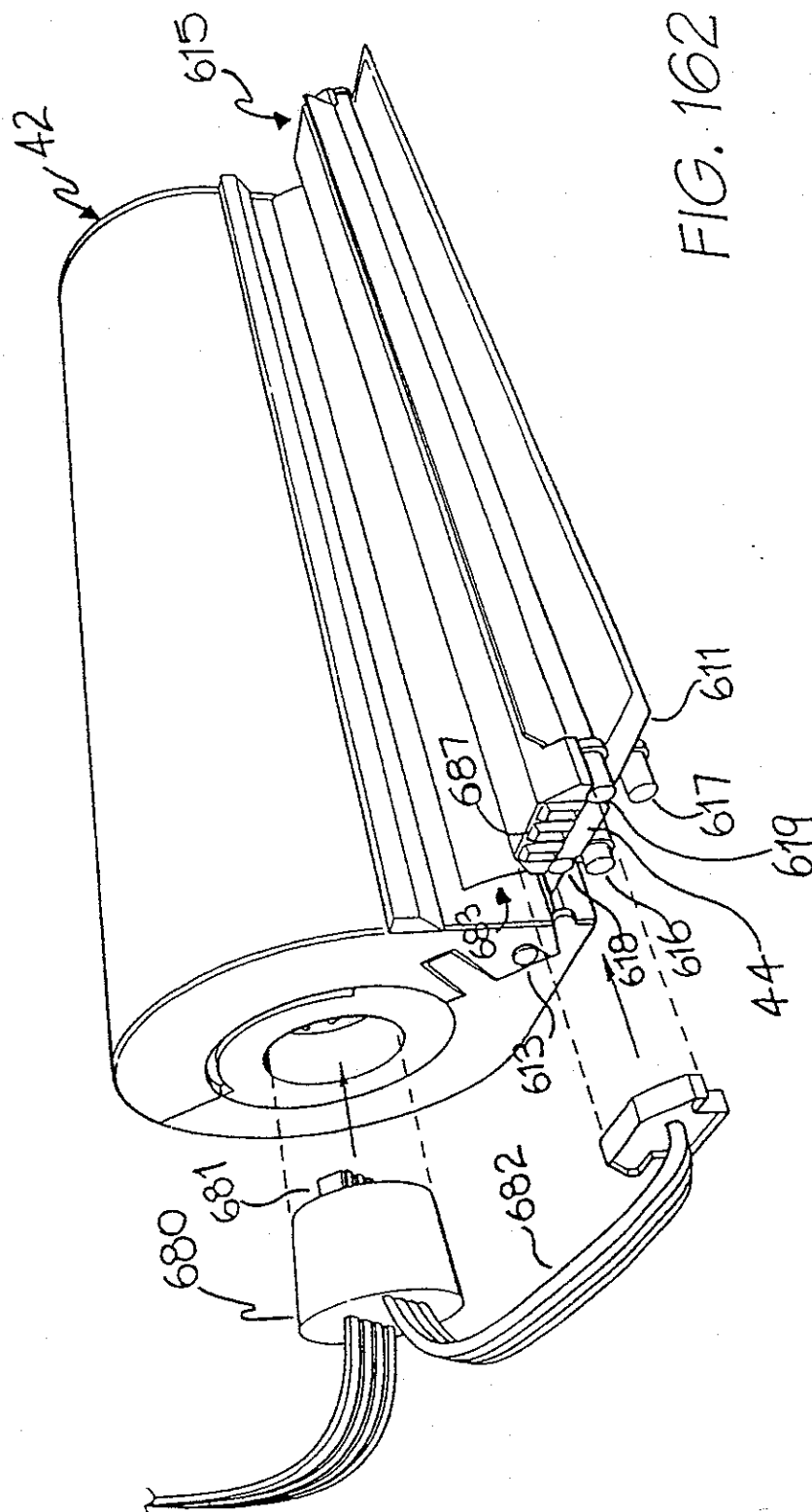




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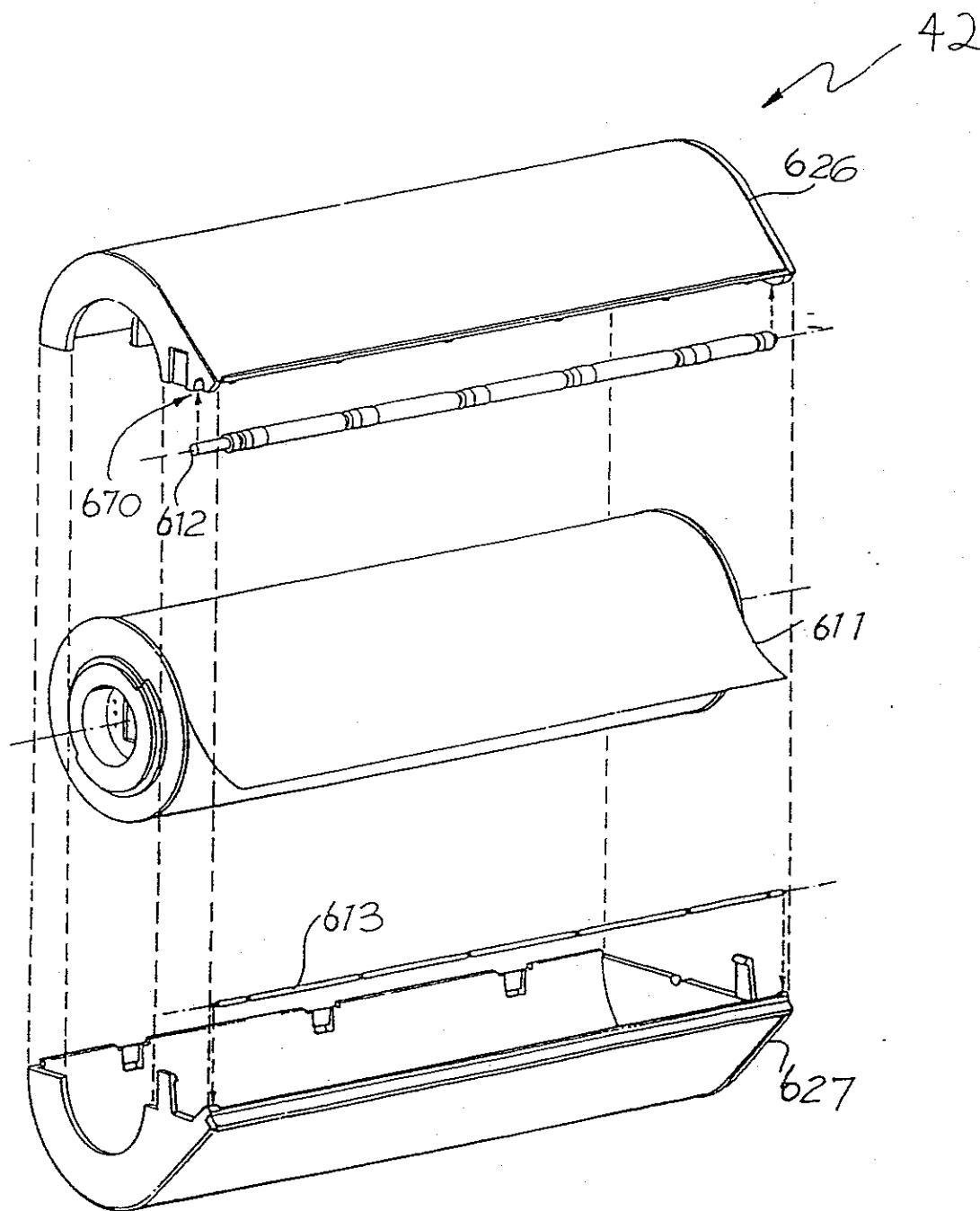


FIG. 163

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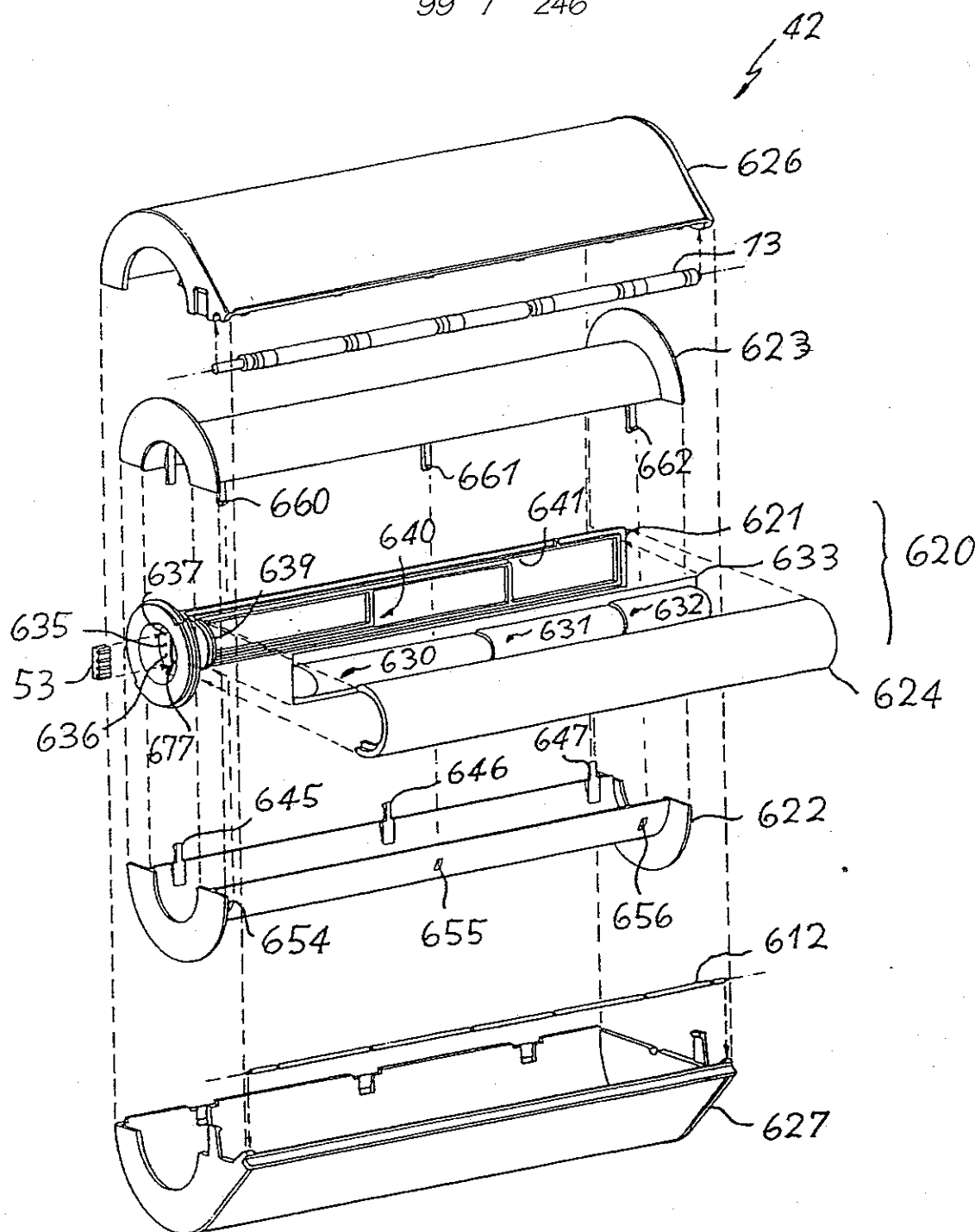


FIG. 164

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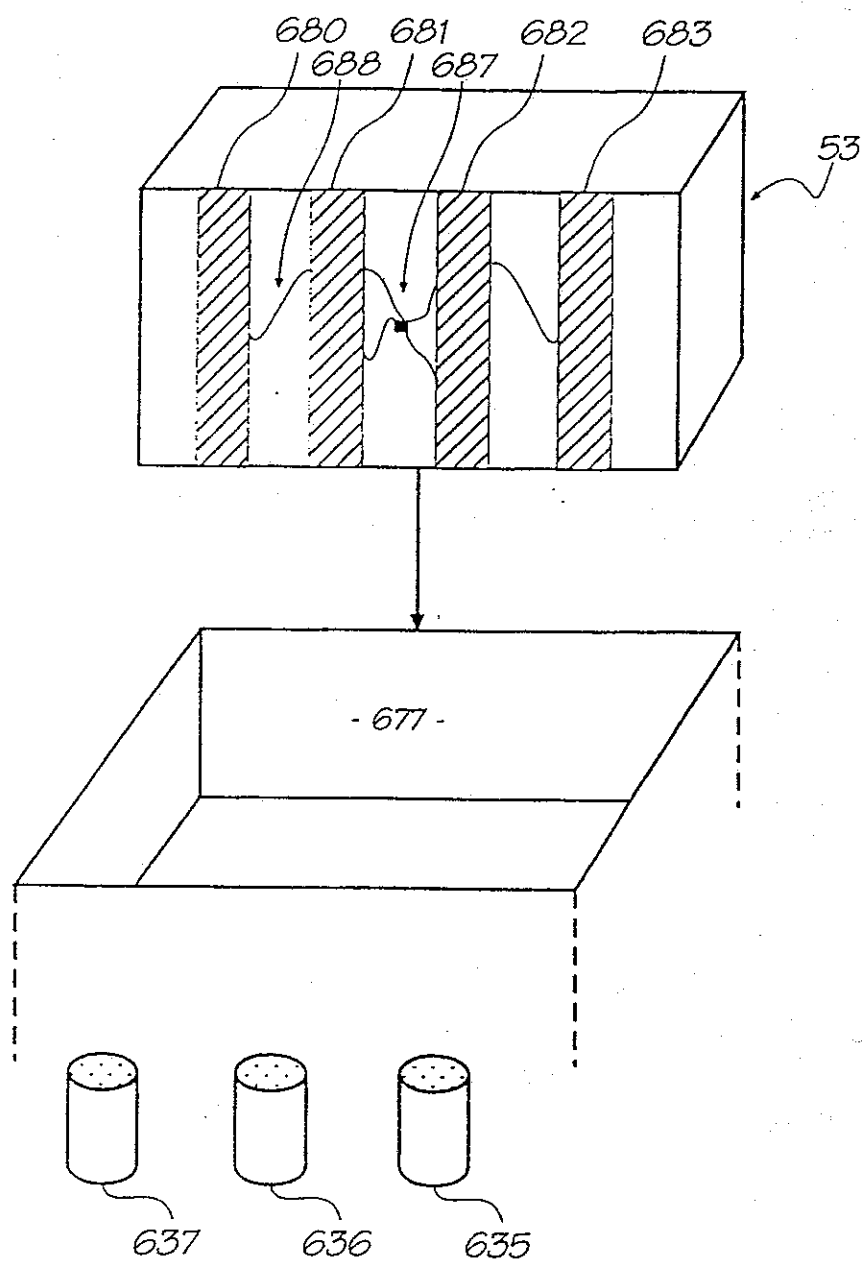


FIG. 165

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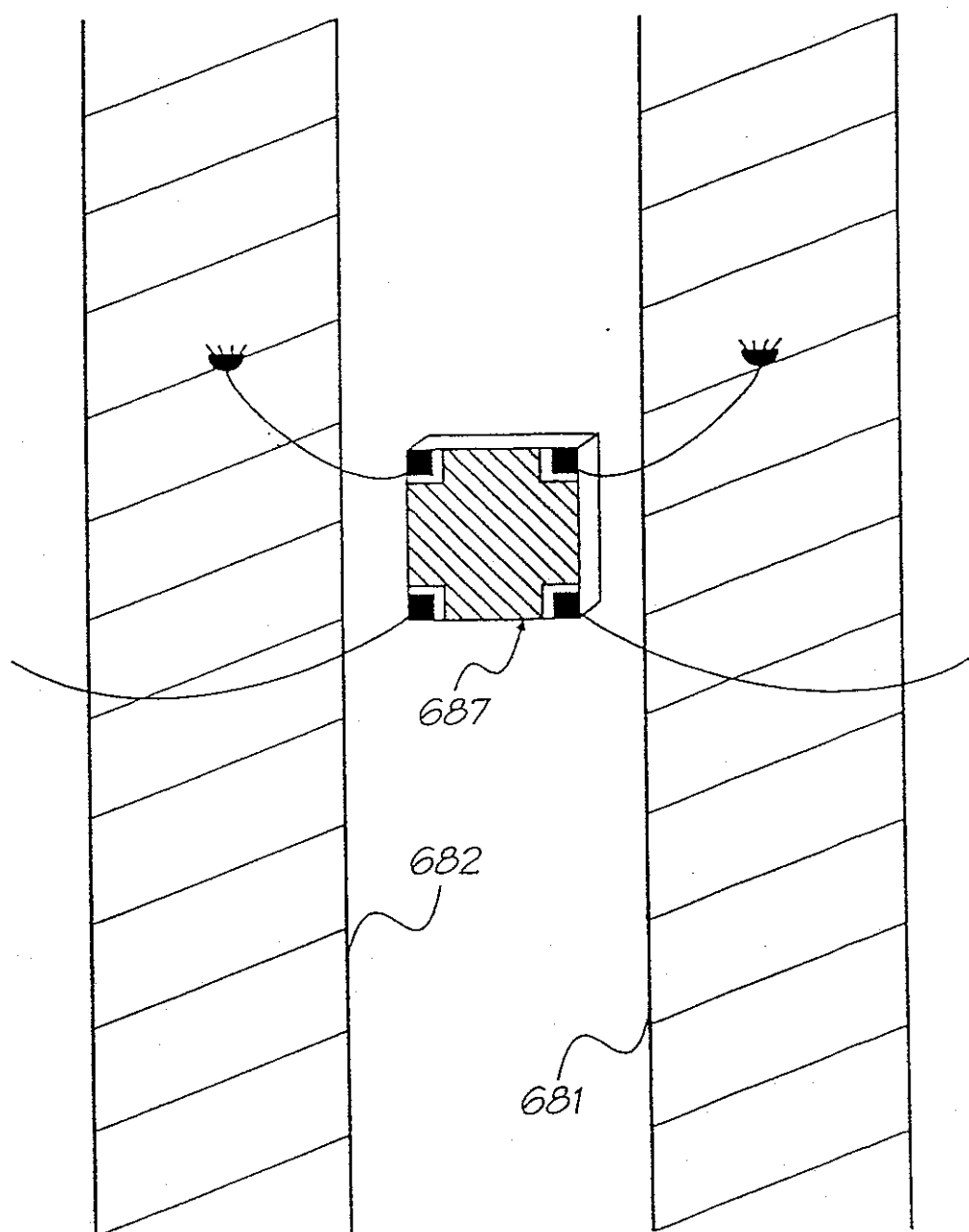


FIG. 166

SUBSTITUTE SHEET (Rule 26)

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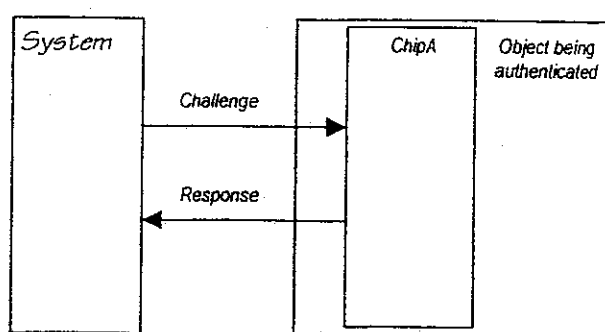


FIG. 167

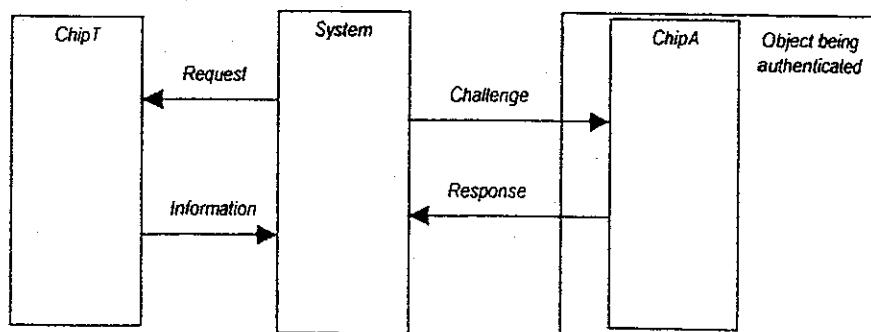


FIG. 168

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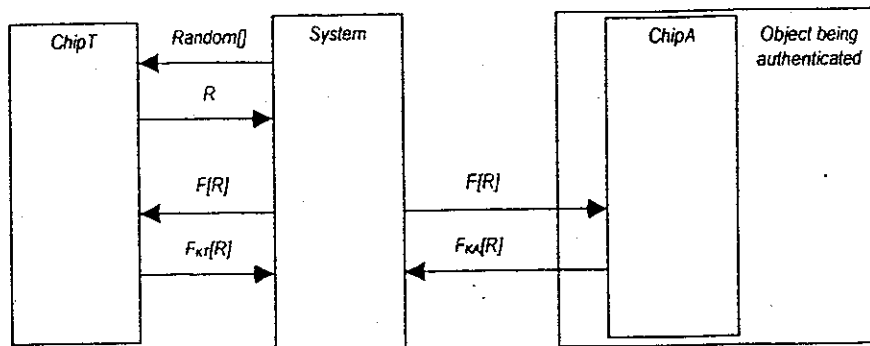


FIG. 169

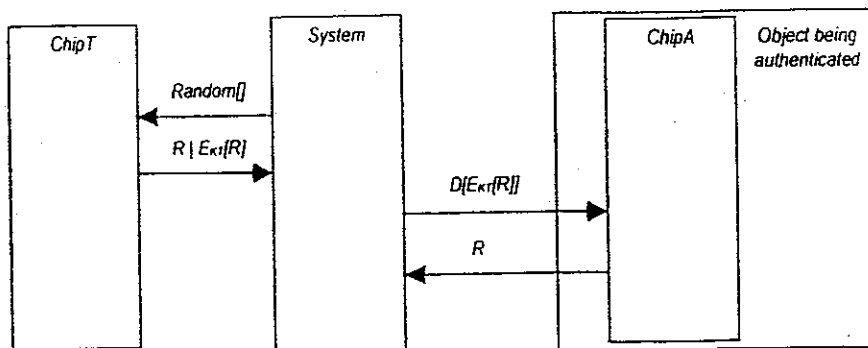


FIG. 170

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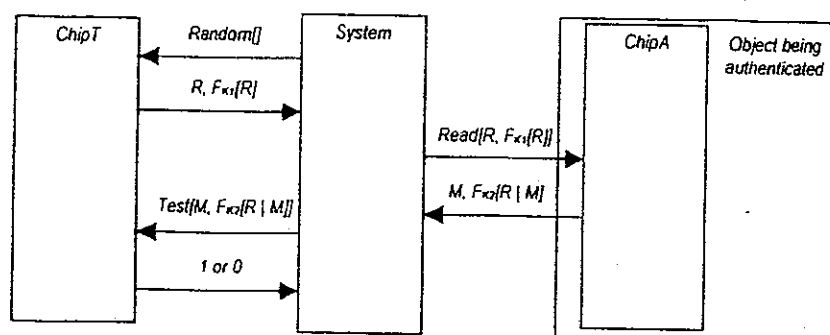


FIG. 171

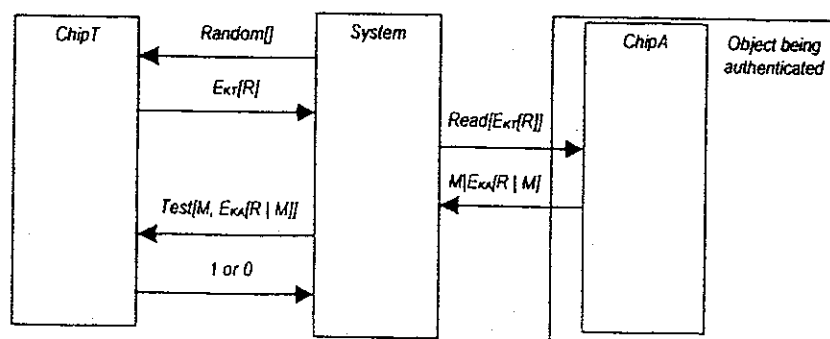


FIG. 172

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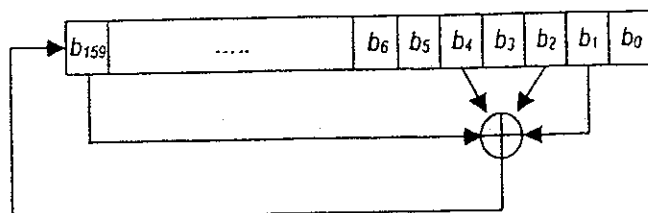


FIG. 173

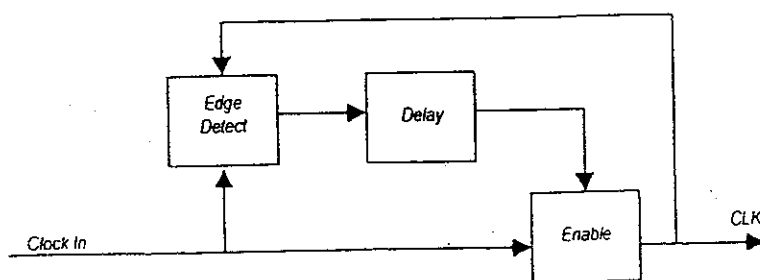


FIG. 174

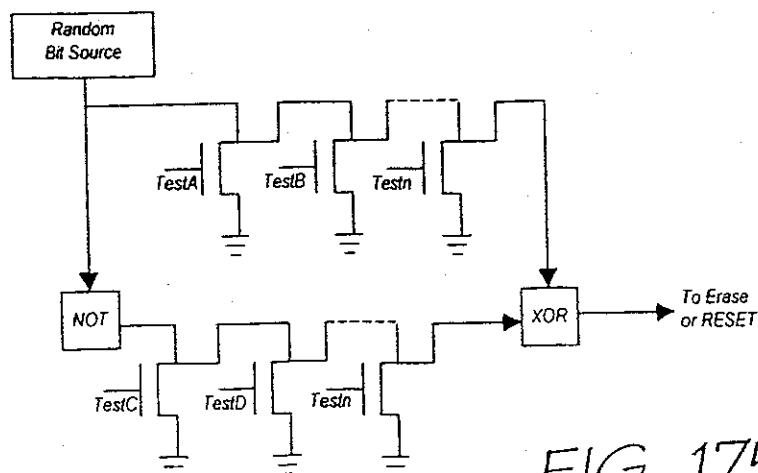


FIG. 175

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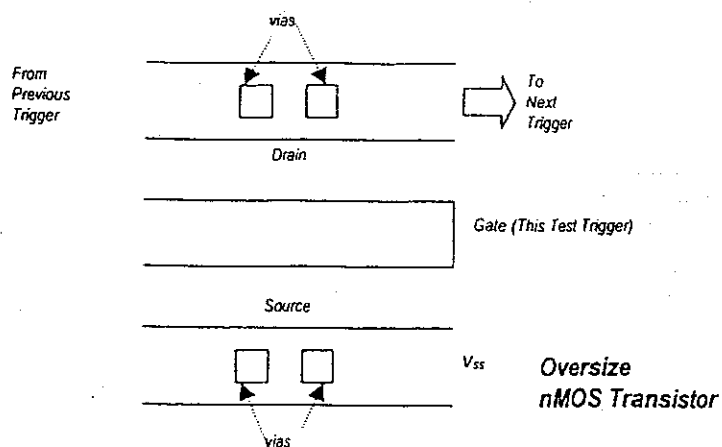


FIG. 176

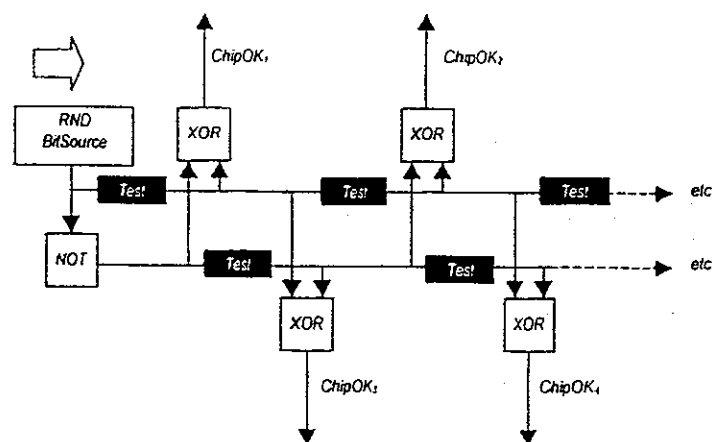


FIG. 177

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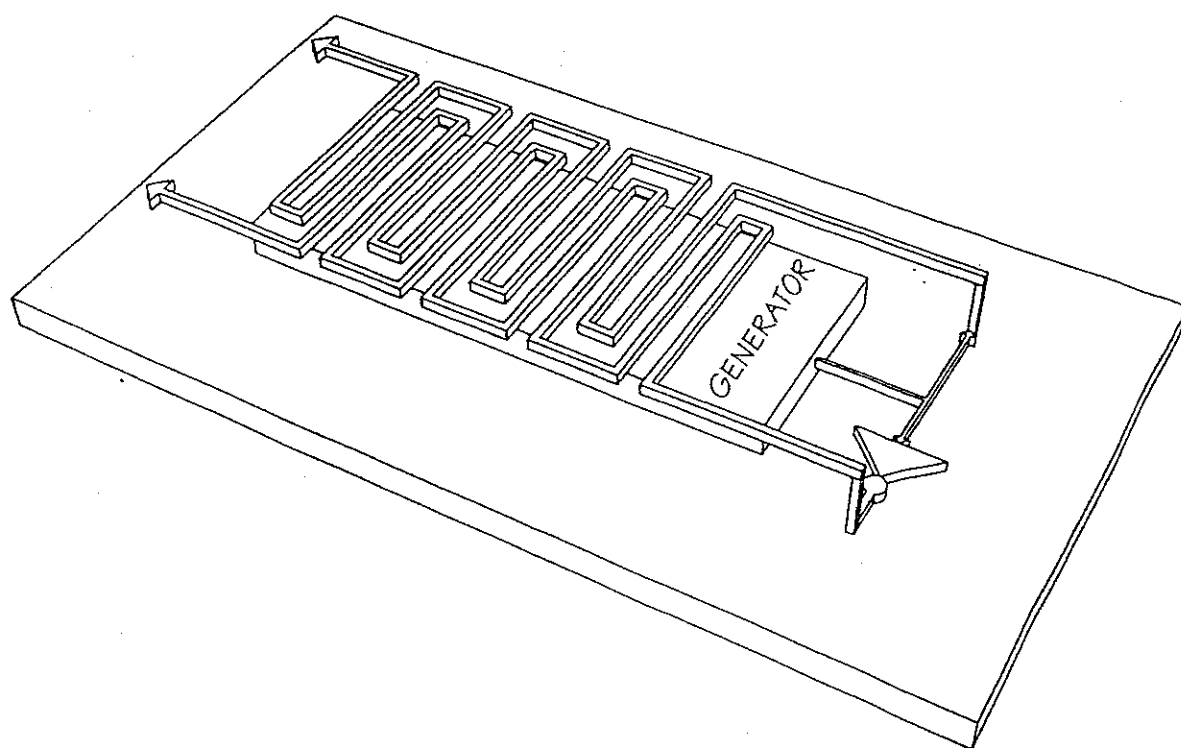


FIG. 178

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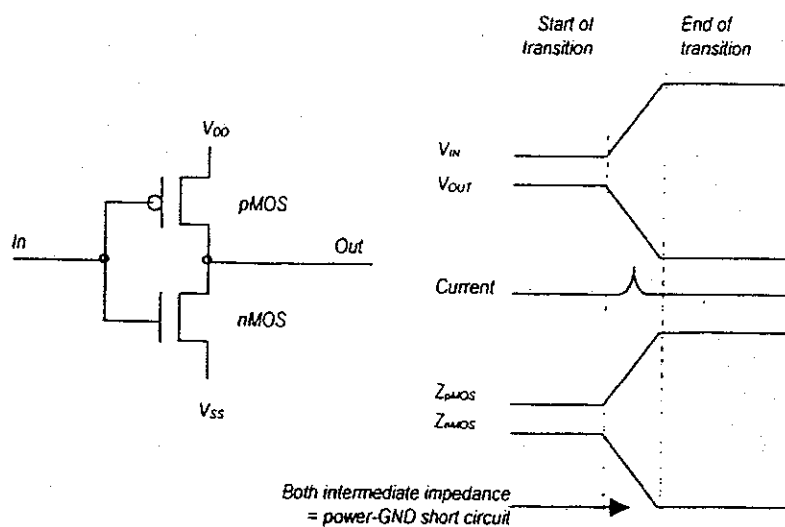


FIG. 179

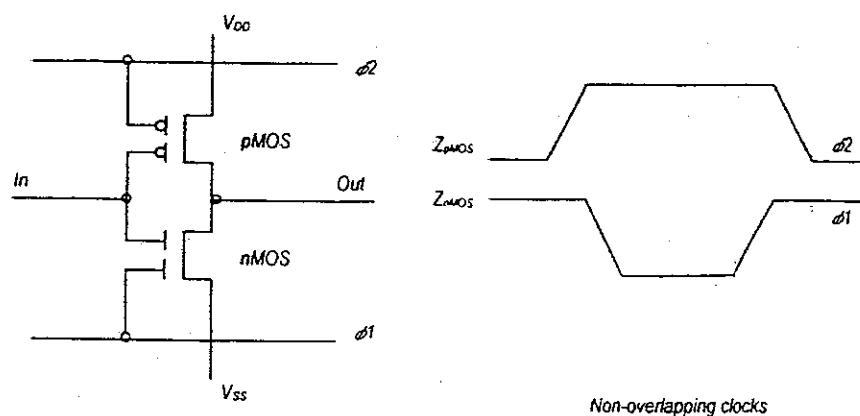


FIG. 180

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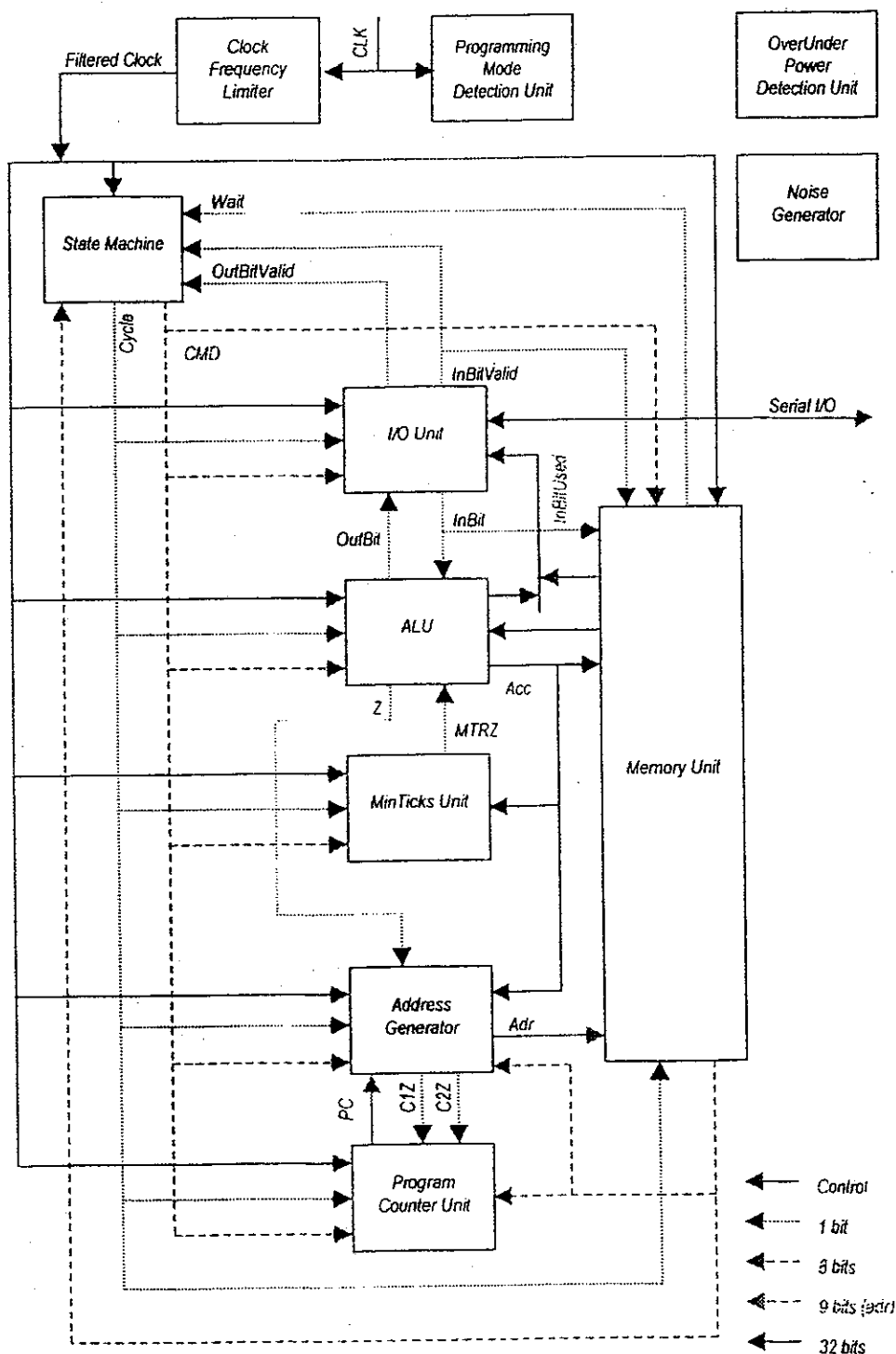


FIG. 181

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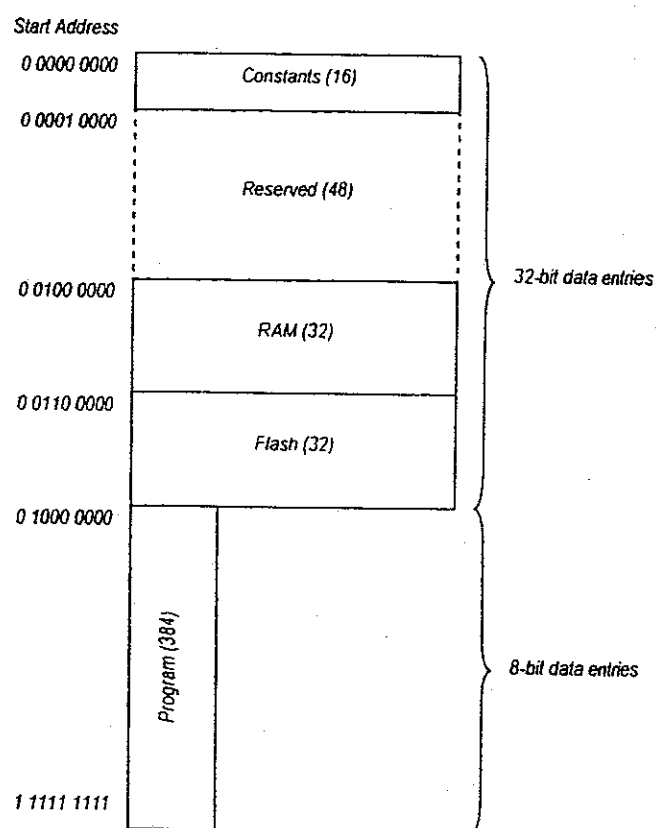


FIG. 182

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Start Address		
0 0000 0000	0x00000000	4 x 32-bit constants
	0x36363636	
	0x5C5C5C5C	
	0xFFFFFFFF	
0 0000 0100	0x5A827999 (y_0)	4 x 32-bit y constants as used by SHA-1.
	0x6ED9EBA1 (y_1)	
	0x8F1BBCDC (y_2)	
	0xCA62C1D6 (y_3)	
0 0000 1000	0x67452301 (h_0)	5 x 32-bit h constants as used by SHA-1.
	0xEFCDAB89 (h_1)	
	0x98BADCFE (h_2)	
	0x10325476 (h_3)	
	0xC3D2E1F0 (h_4)	
	Reserved (3)	Unused and unreferenced
0 0000 1111		

FIG. 183

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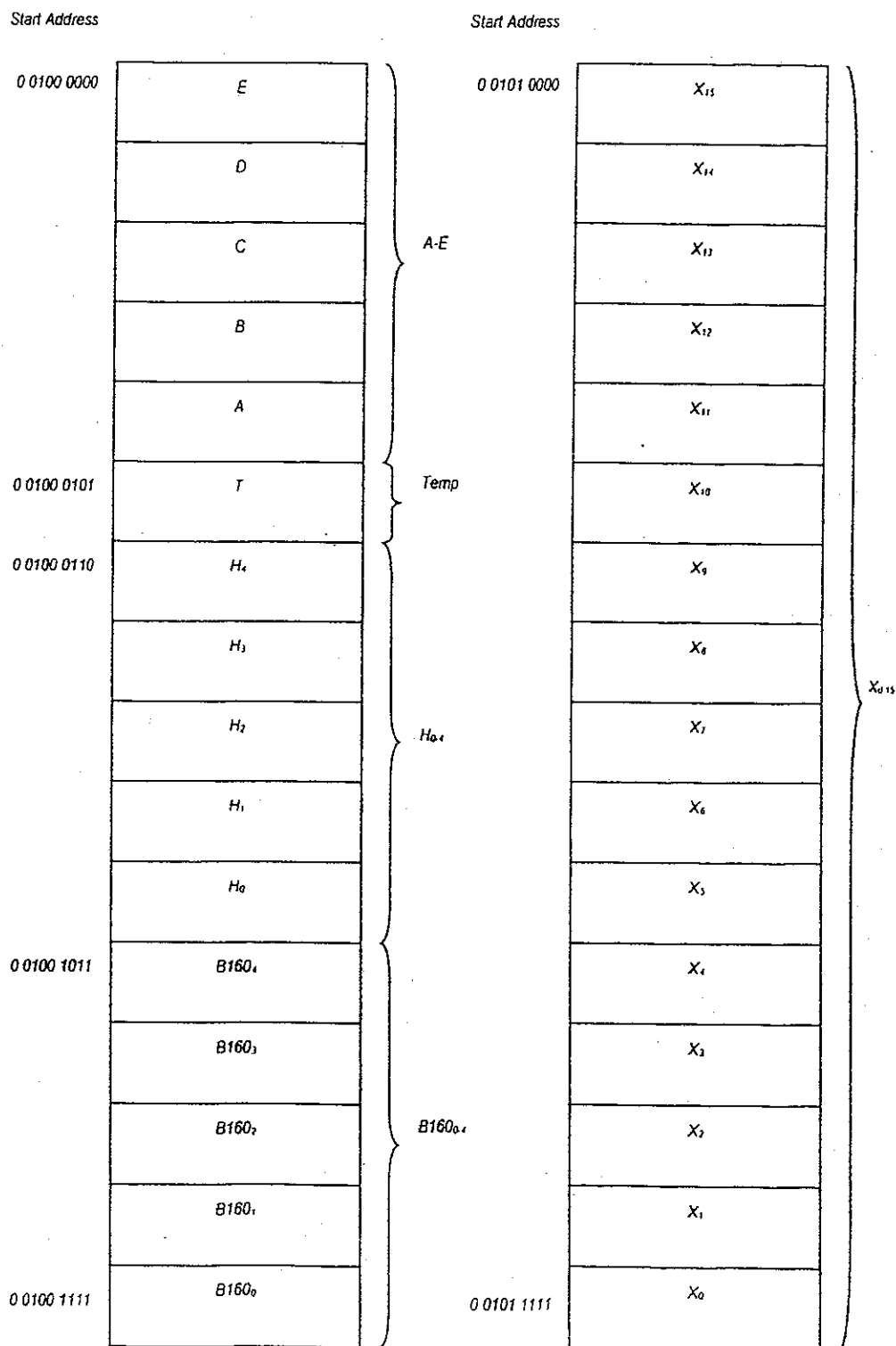


FIG. 184

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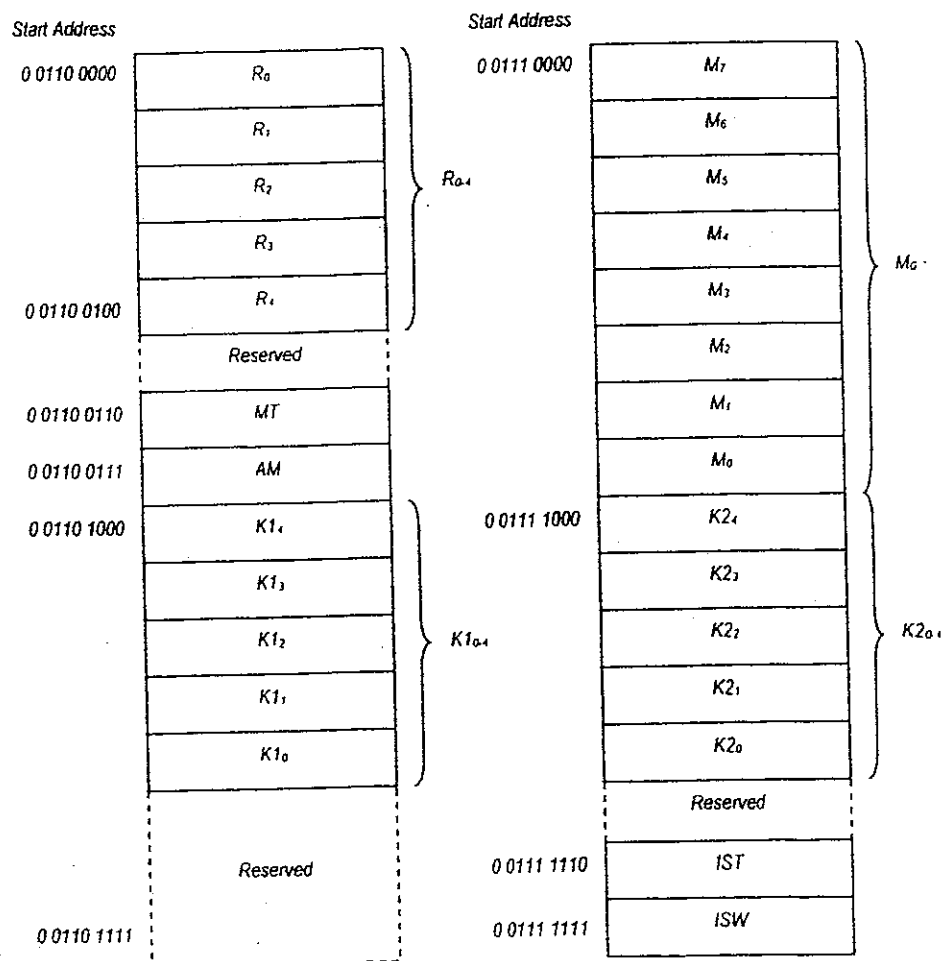


FIG. 185

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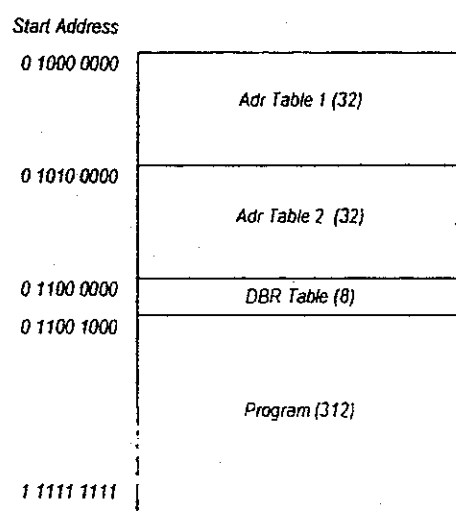


FIG. 186

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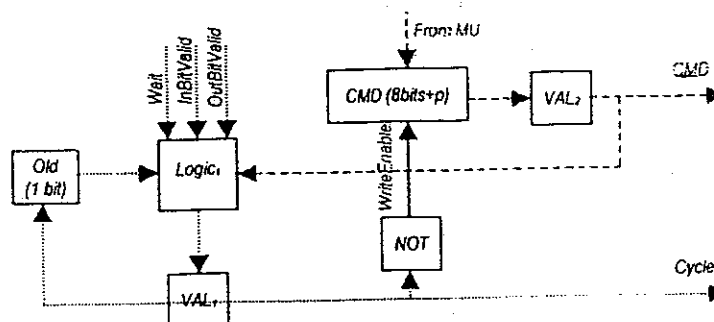


FIG. 187

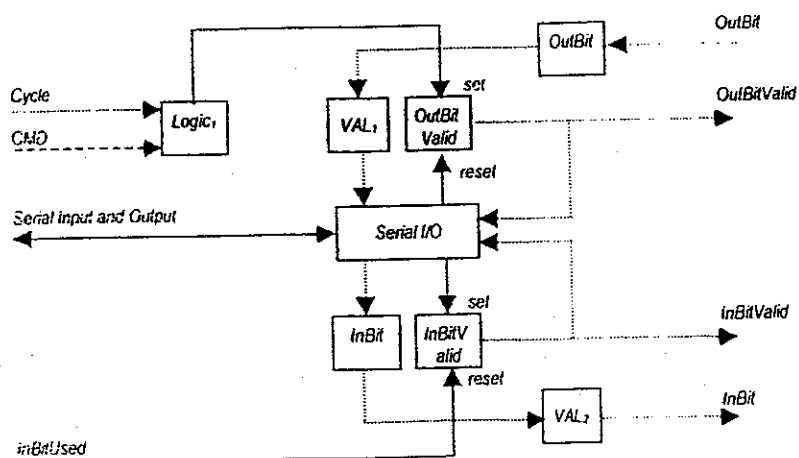


FIG. 188

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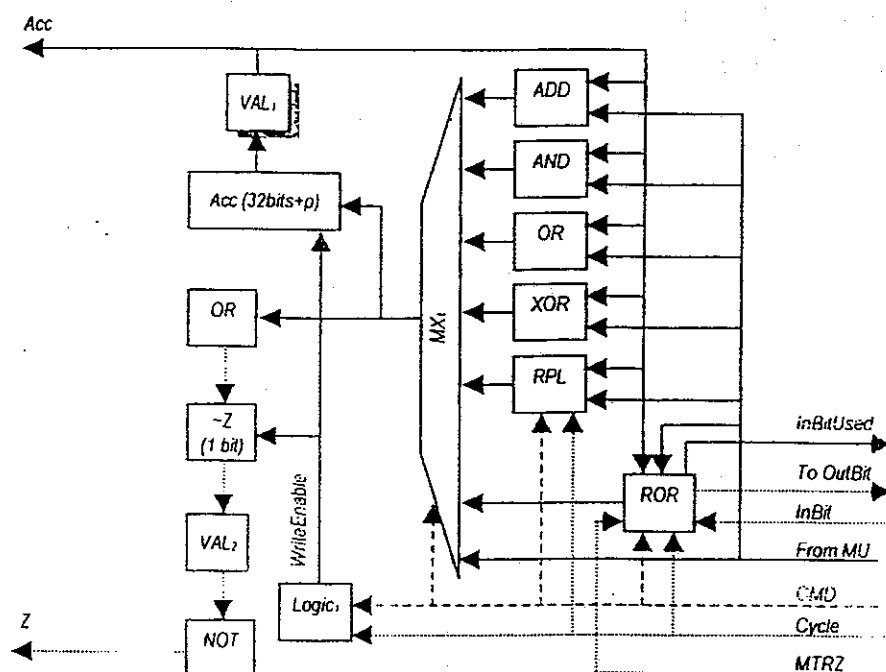


FIG. 189

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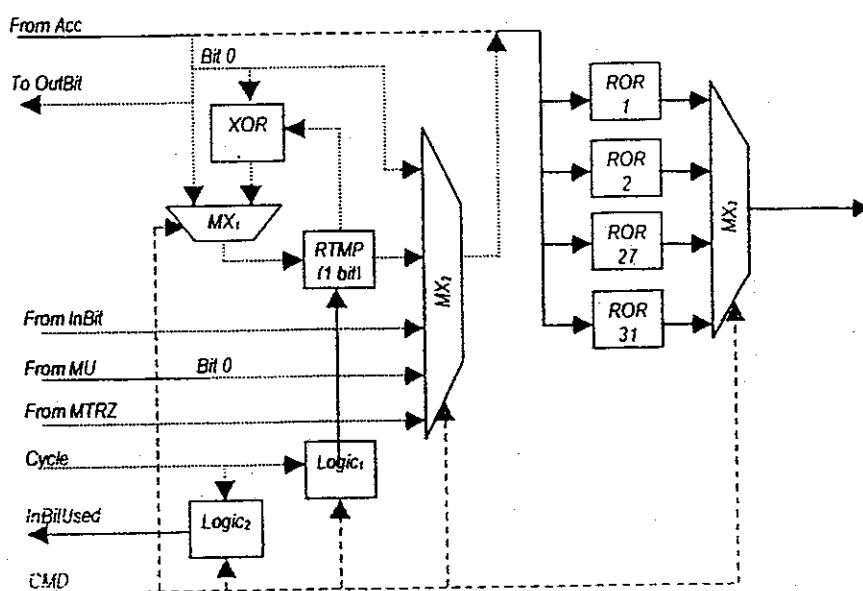


FIG. 190

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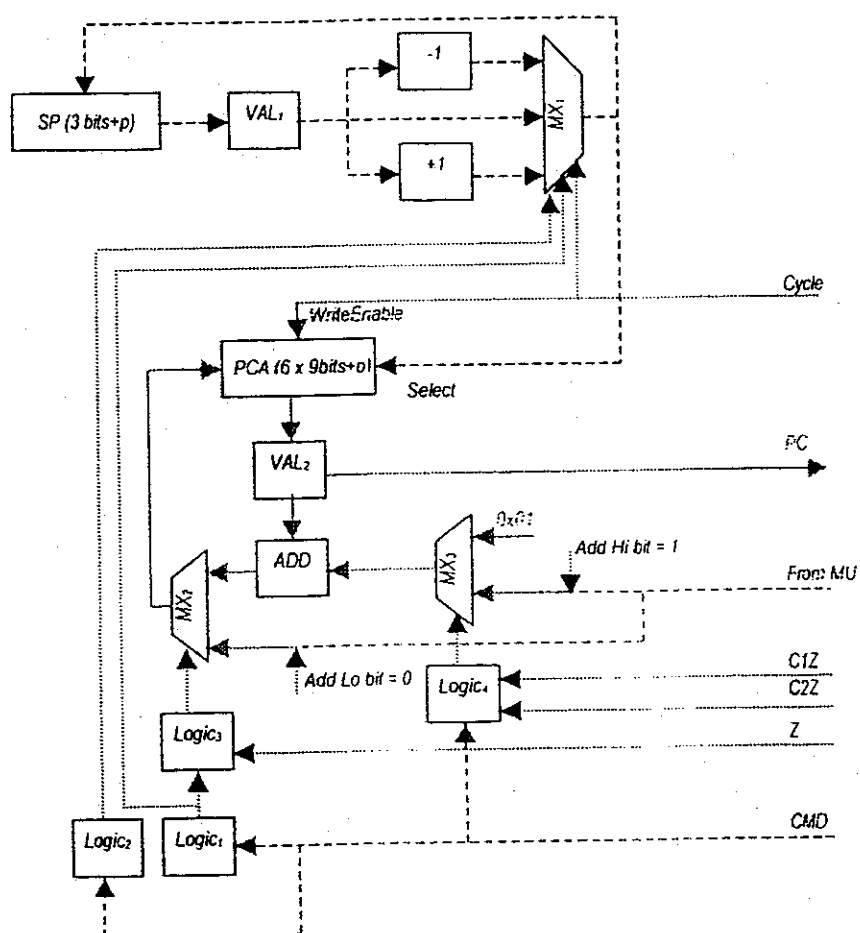


FIG. 192

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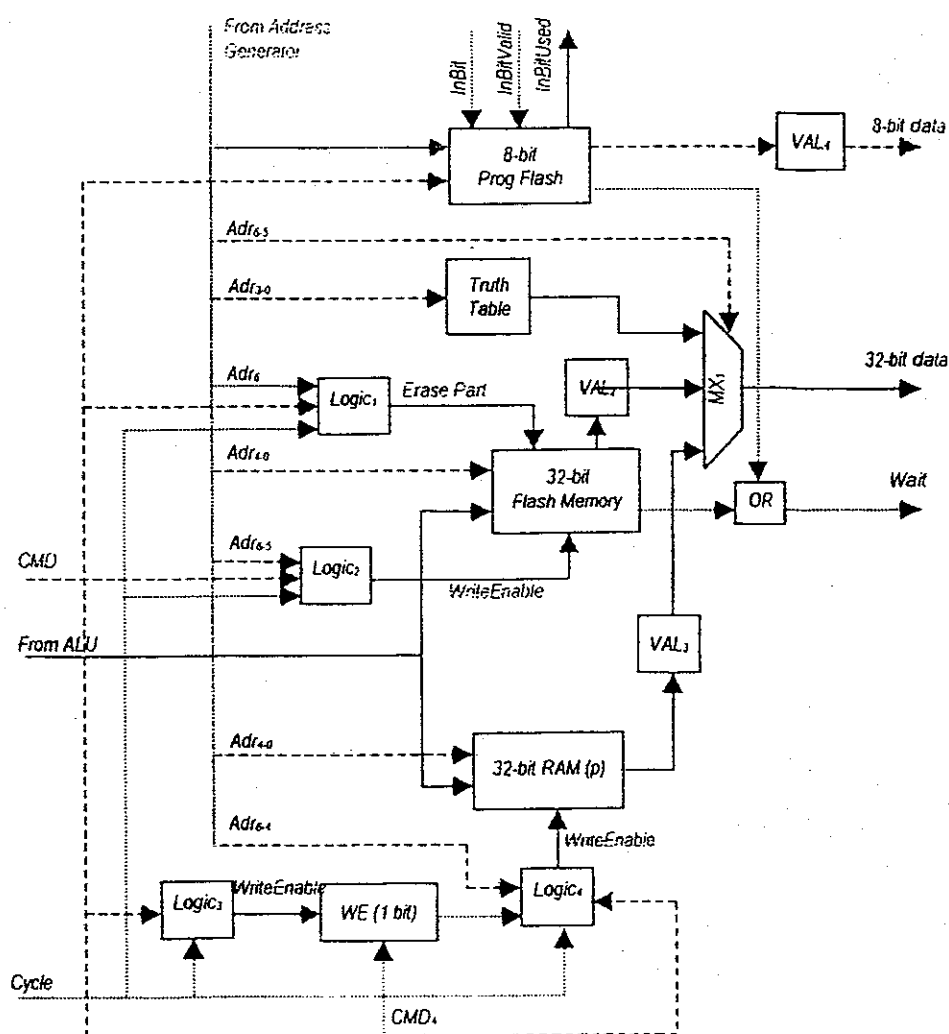


FIG. 193

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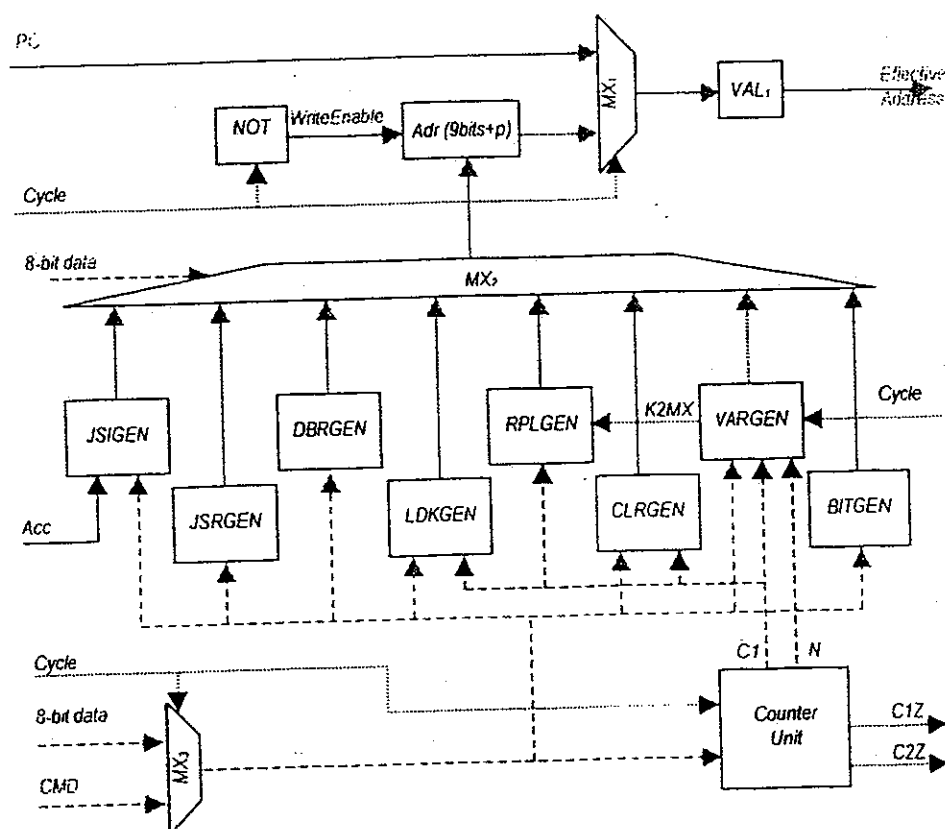


FIG. 194

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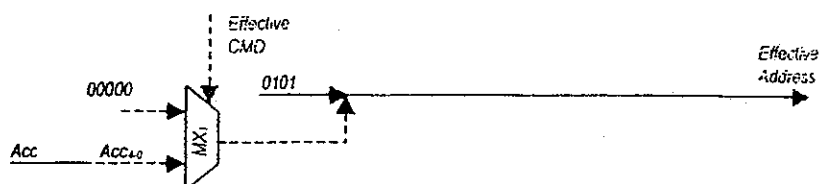


FIG. 195

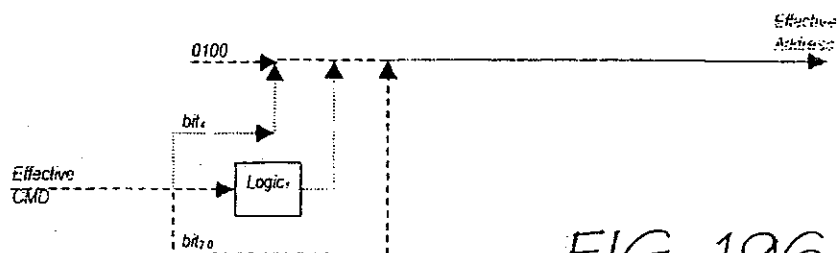


FIG. 196

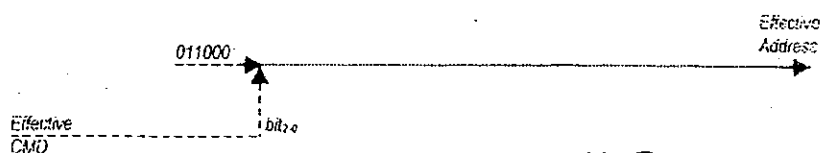


FIG. 197

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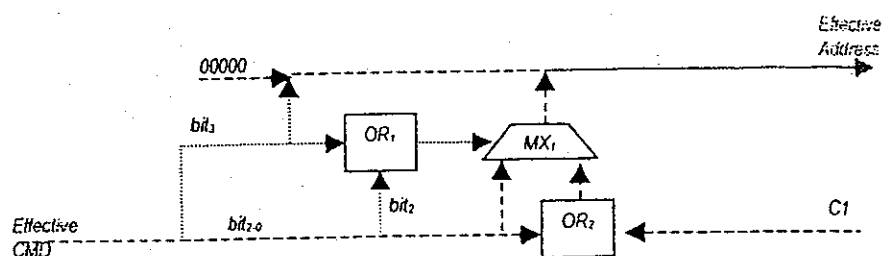


FIG. 198

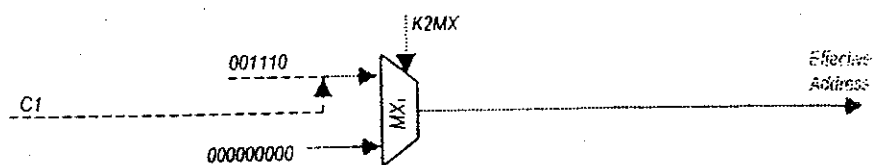


FIG. 199

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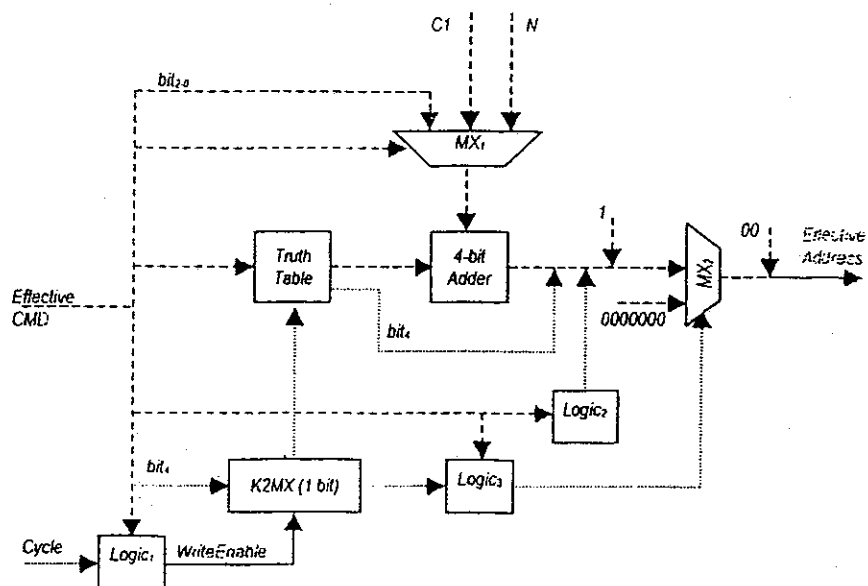


FIG. 200

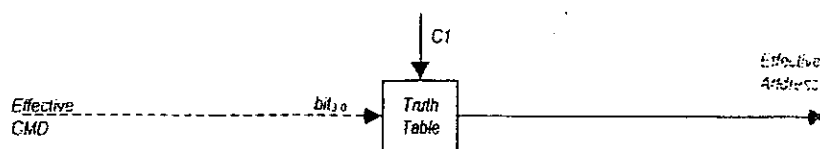


FIG. 201

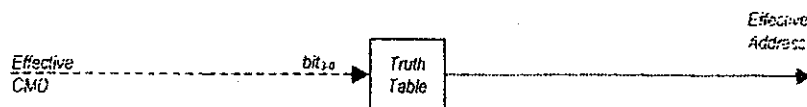


FIG. 202

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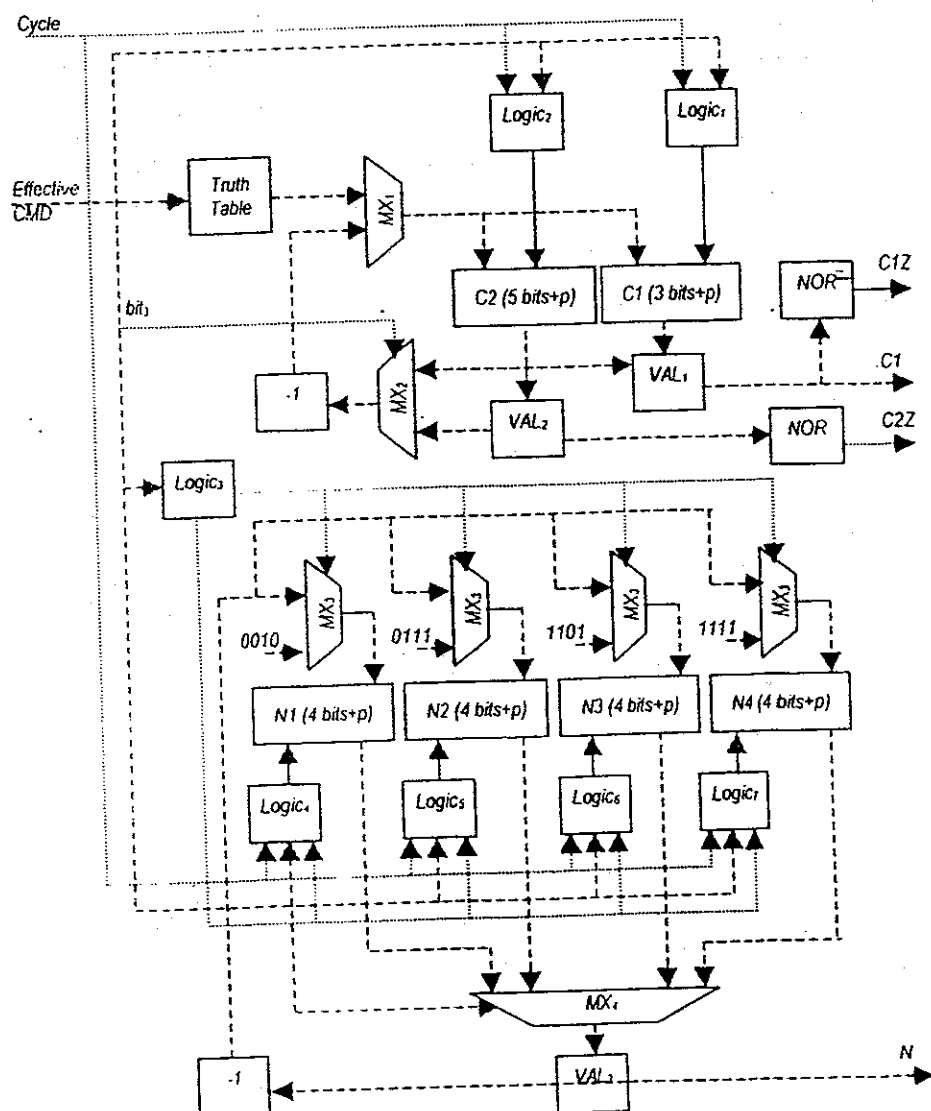
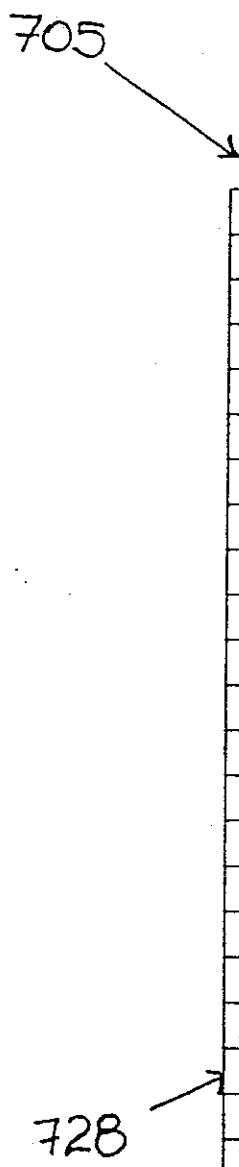


FIG. 203

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Data Type	Bits
Factory code	16
Batch number	32
Serial number	48
Manufacturing date	16
Media length	24
Media type	8
Preprinted media length	16
Cyan ink viscosity	8
Magenta ink viscosity	8
Yellow ink viscosity	8
Cyan drop volume	8
Magenta drop volume	8
Yellow drop volume	8
Cyan ink color	24
Magenta ink color	24
Yellow ink color	24
Remaining-media length indicator	16
Authentication key	128
Copyrightable bit pattern	512
Reserved for camera use	88
Total	1024

728

FIG. 204

```
graph TD; 774[INK COLOUR VISCOSITY] --> 771[PULSE PROFILE CHARACTERIZER]; 775[INK COLOUR DROP VOLUME] --> 771; 776[INK COLOUR] --> 771; 771 --> 761[PRINT HEAD CONTROLLER]; 762[MEDIA TYPE] --> 771; 763[REMAINING ROLL MEDIA] --> 771;
```

The diagram illustrates a system for pulse profile characterization. It features a central block labeled **PULSE PROFILE CHARACTERIZER** (771). Five input blocks provide data to this central unit: **INK COLOUR VISCOSITY** (774), **INK COLOUR DROP VOLUME** (775), **INK COLOUR** (776), **MEDIA TYPE** (762), and **REMAINING ROLL MEDIA** (763). The output of the **PULSE PROFILE CHARACTERIZER** (771) is sent to the **PRINT HEAD CONTROLLER** (761).

FIG. 205

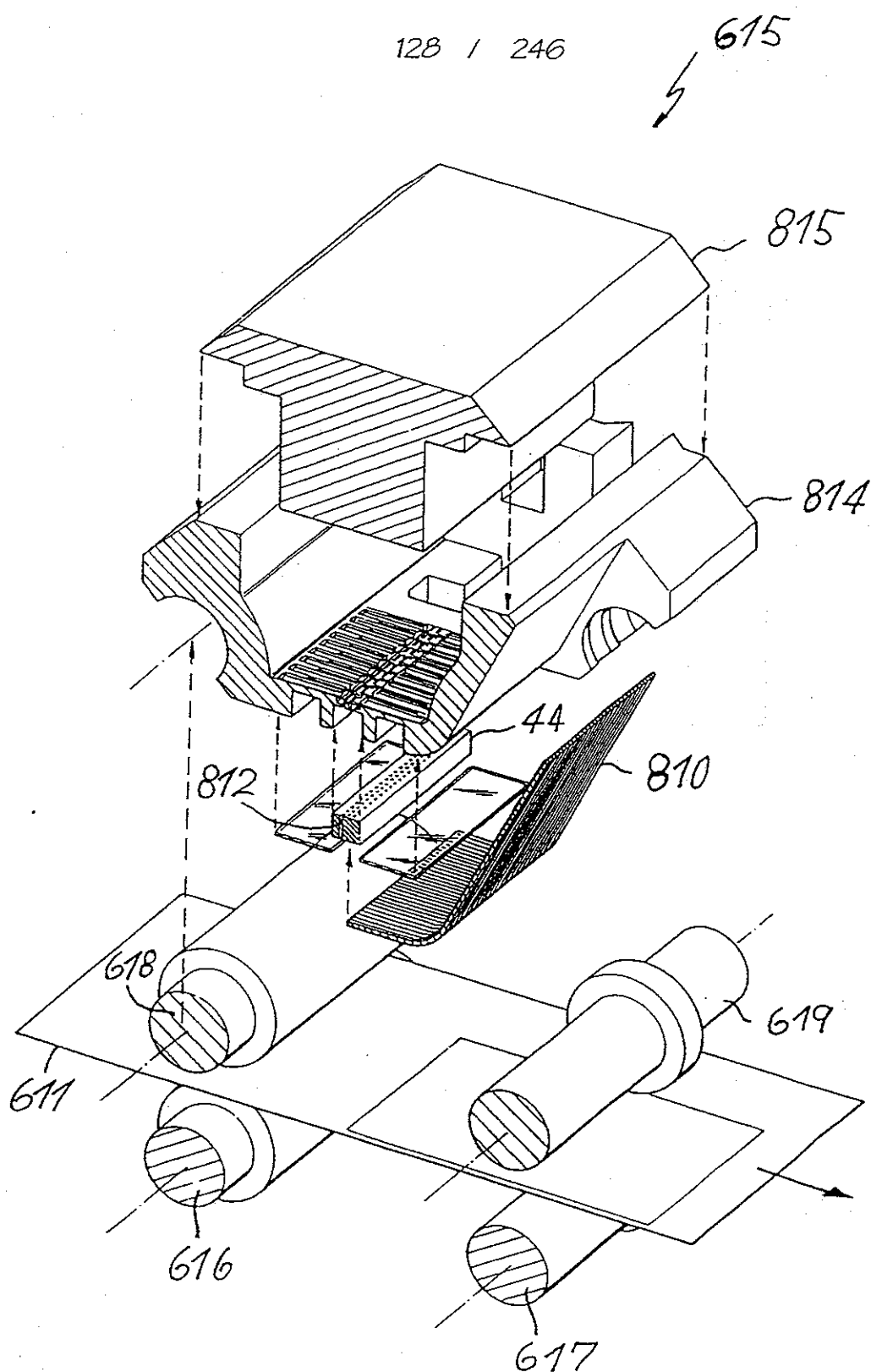


FIG 206

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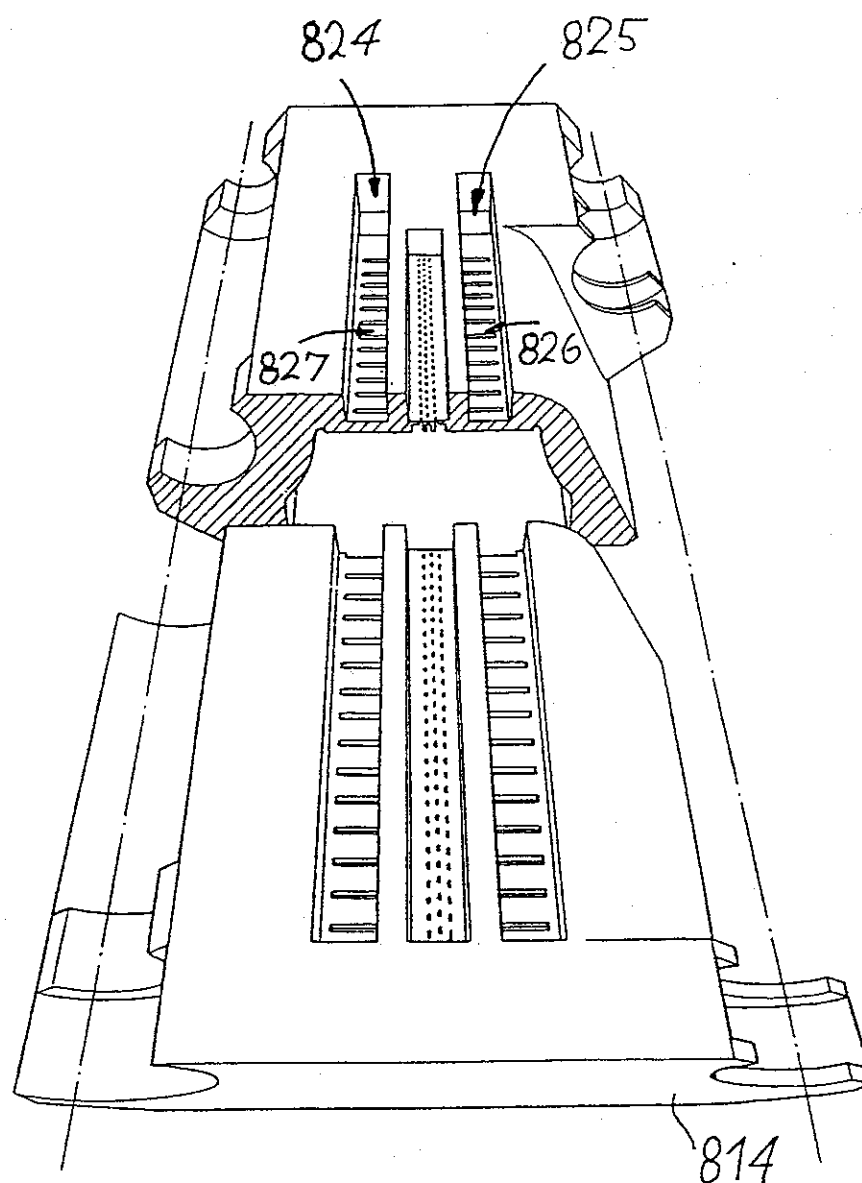


FIG. 207

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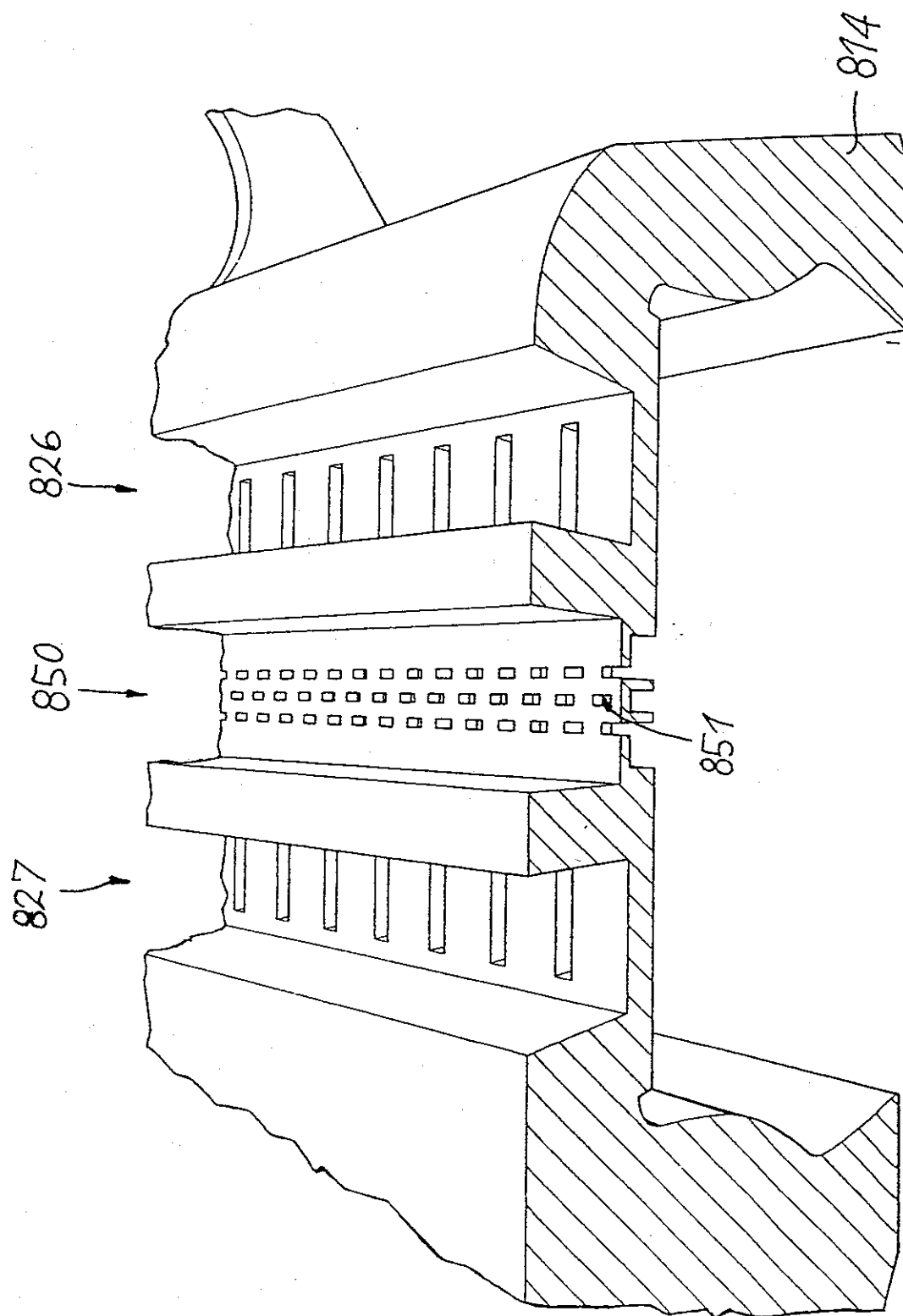


FIG. 208

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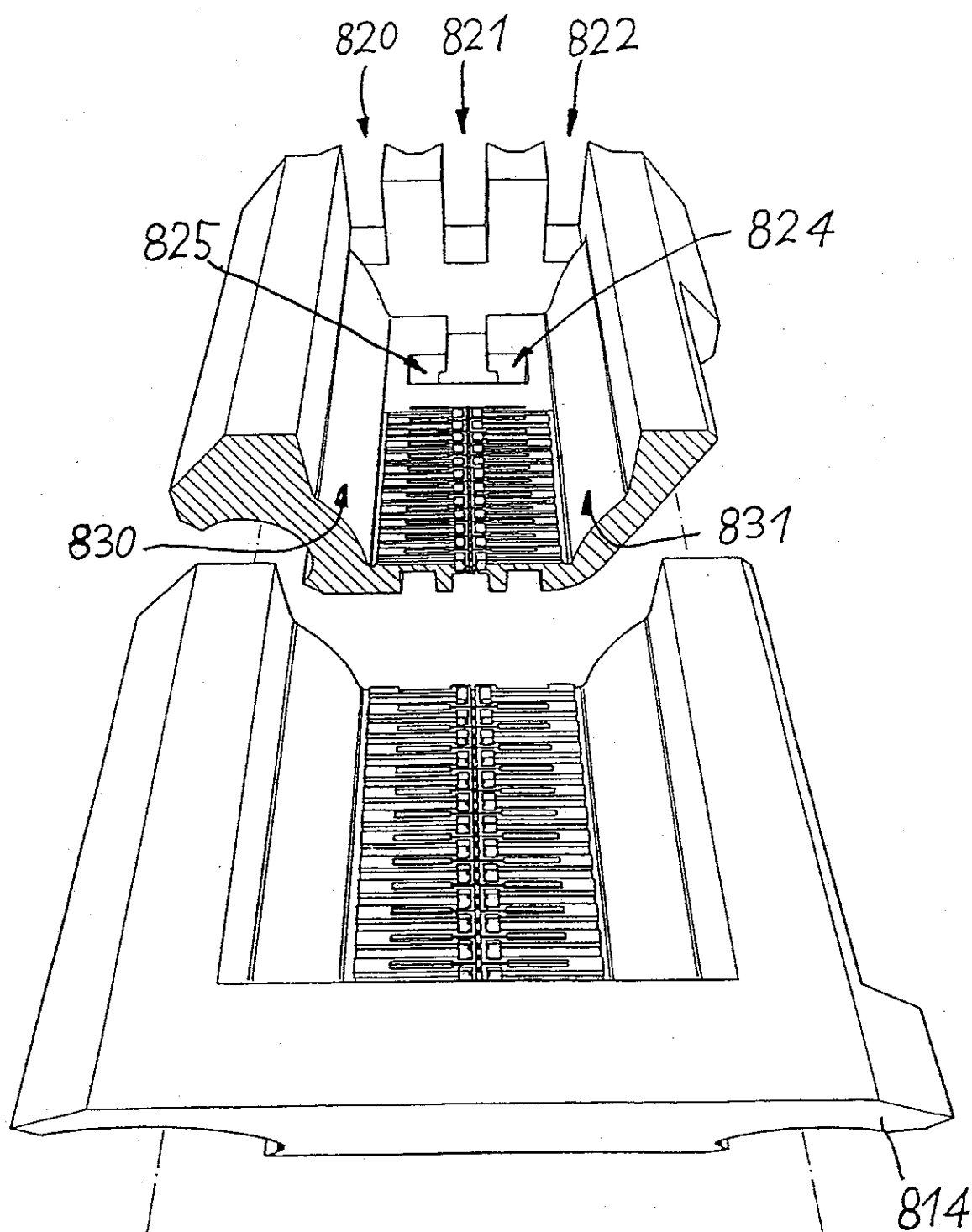


FIG. 209

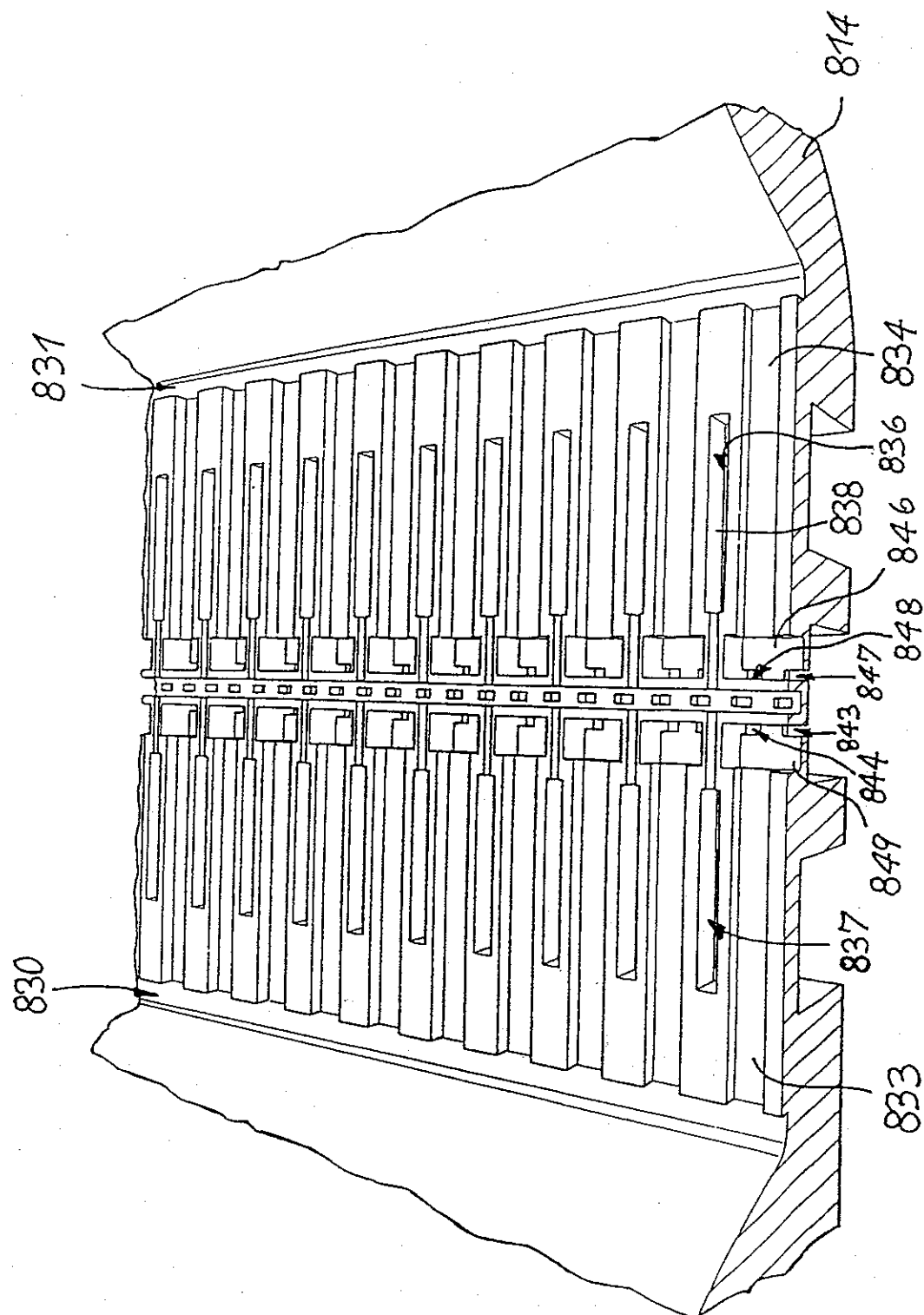


FIG. 210

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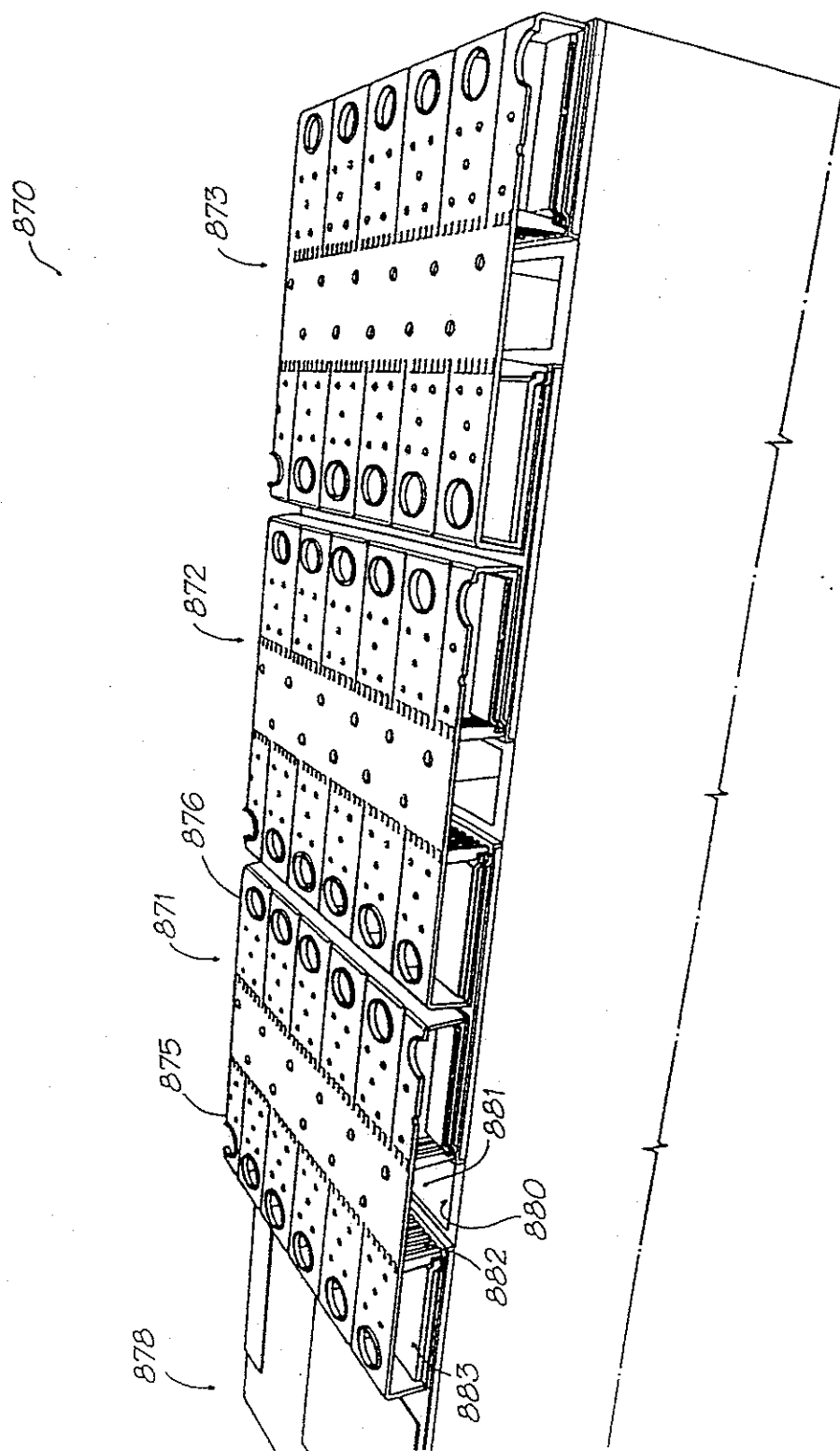


FIG. 211

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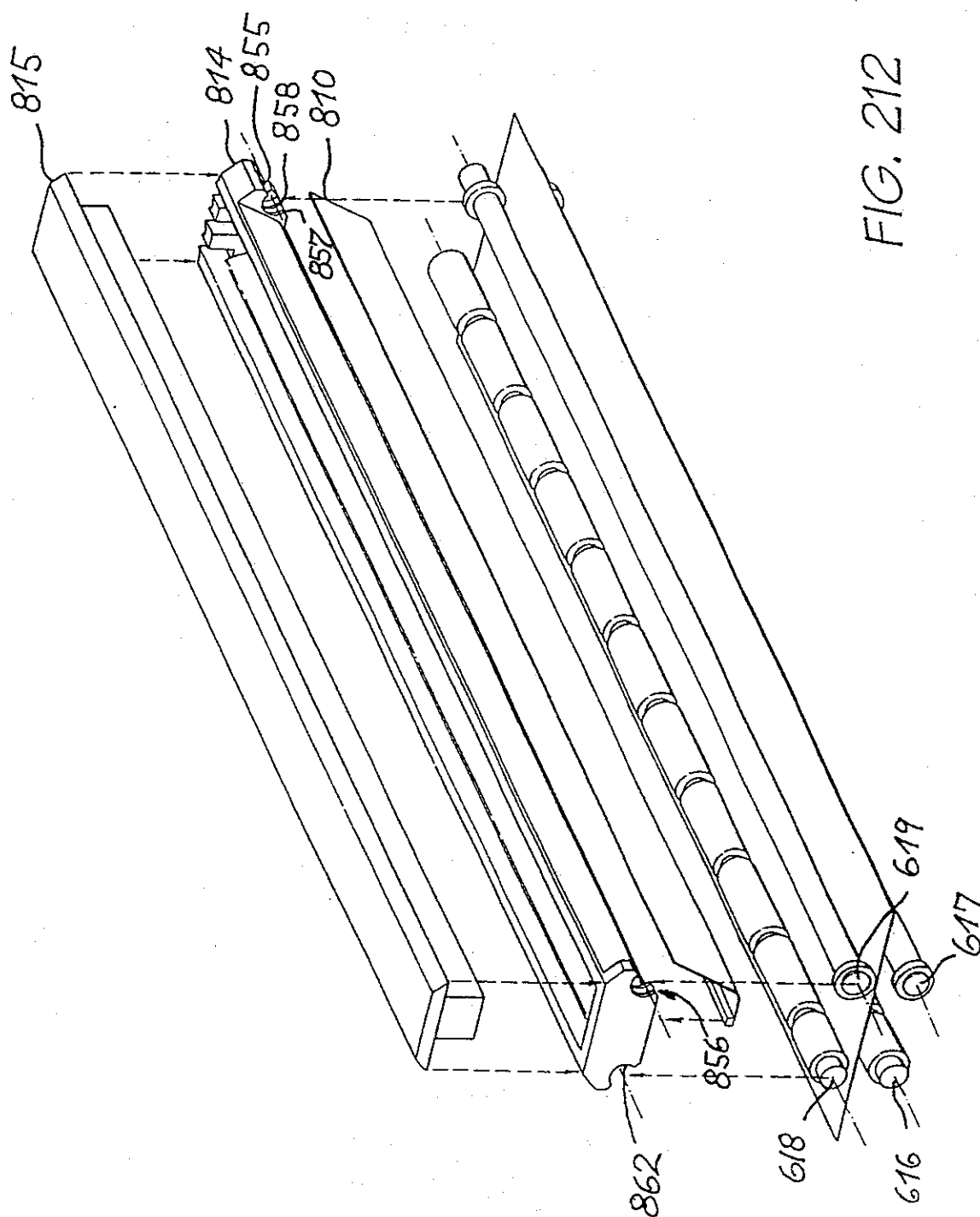
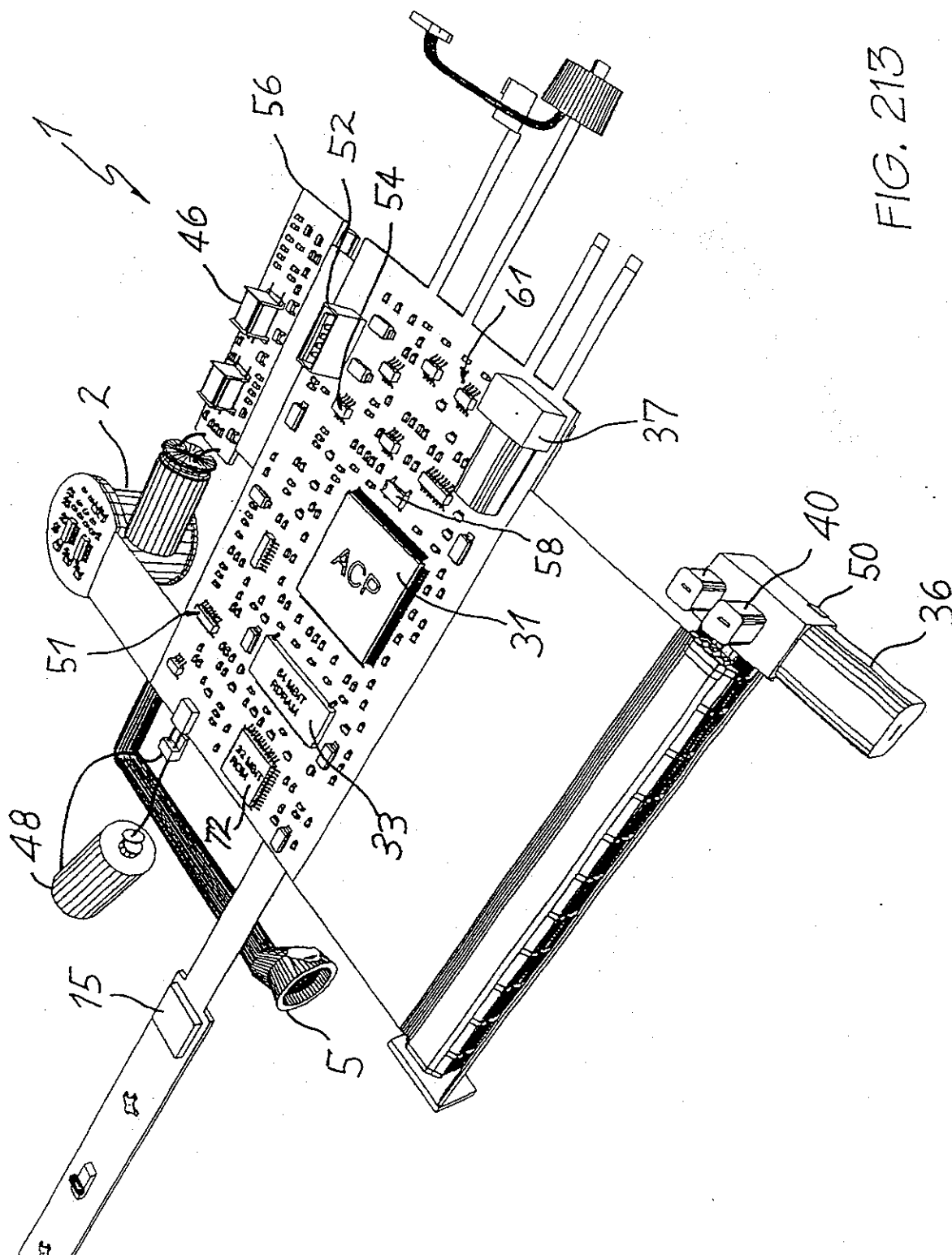
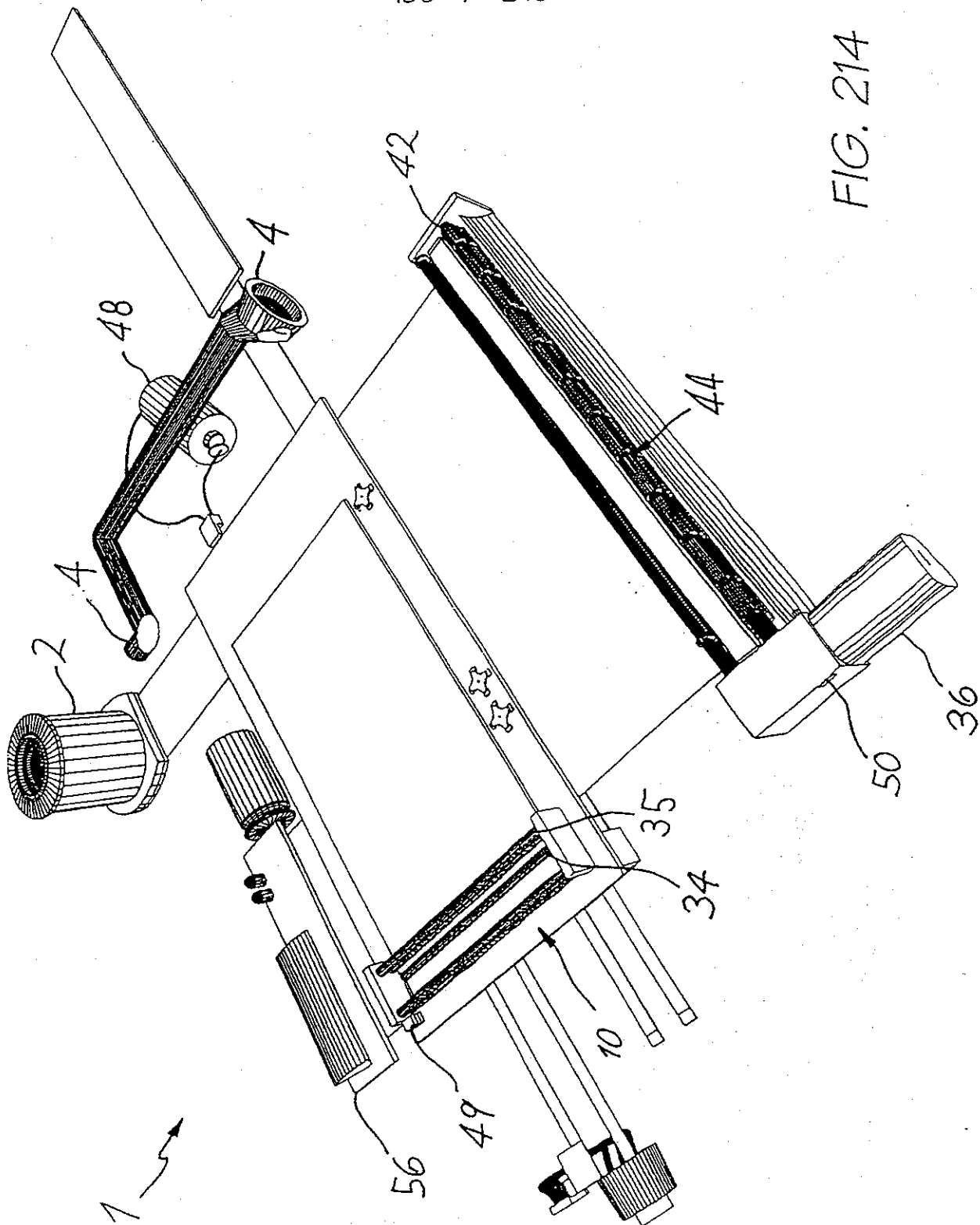


FIG. 212

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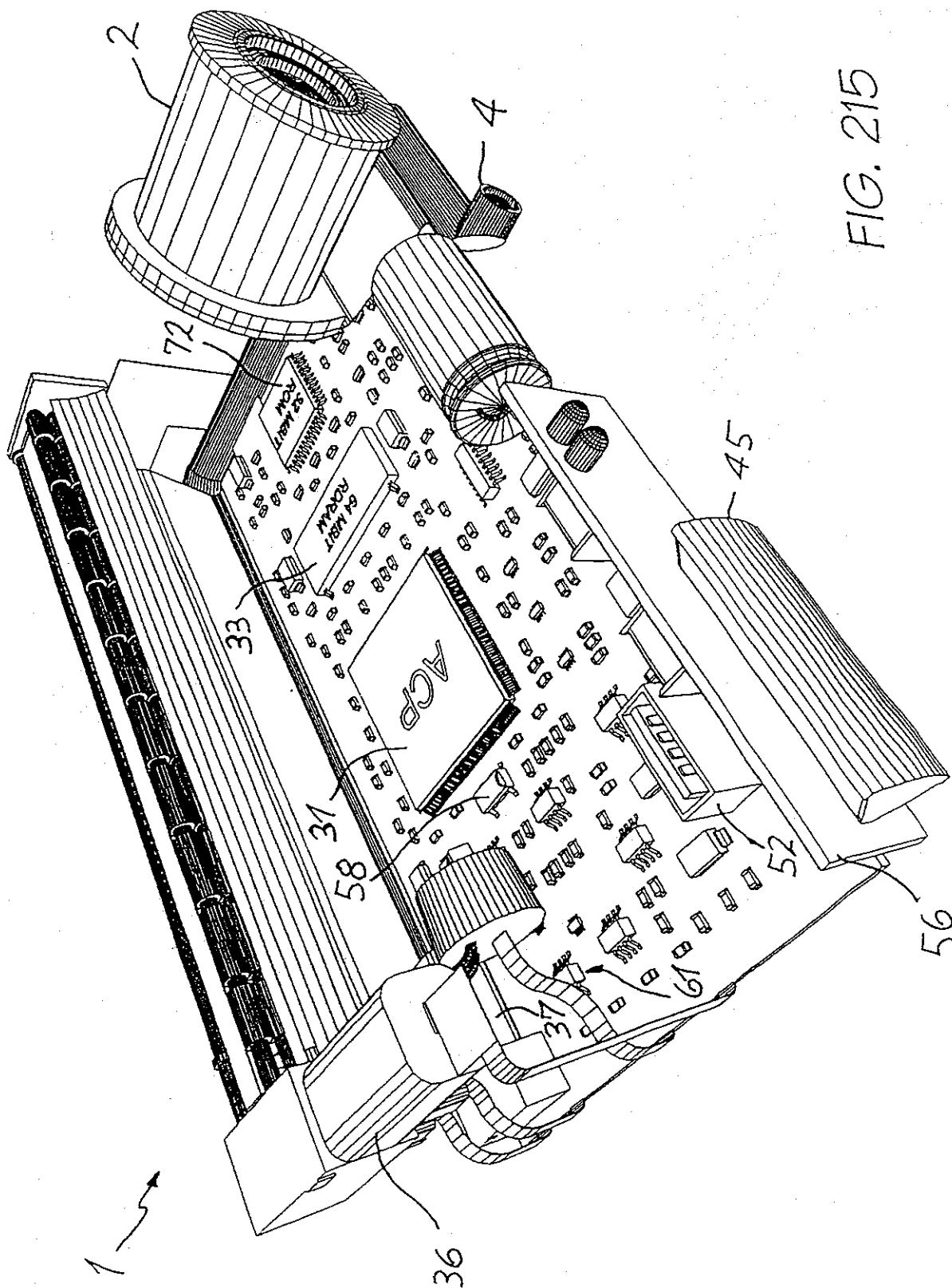
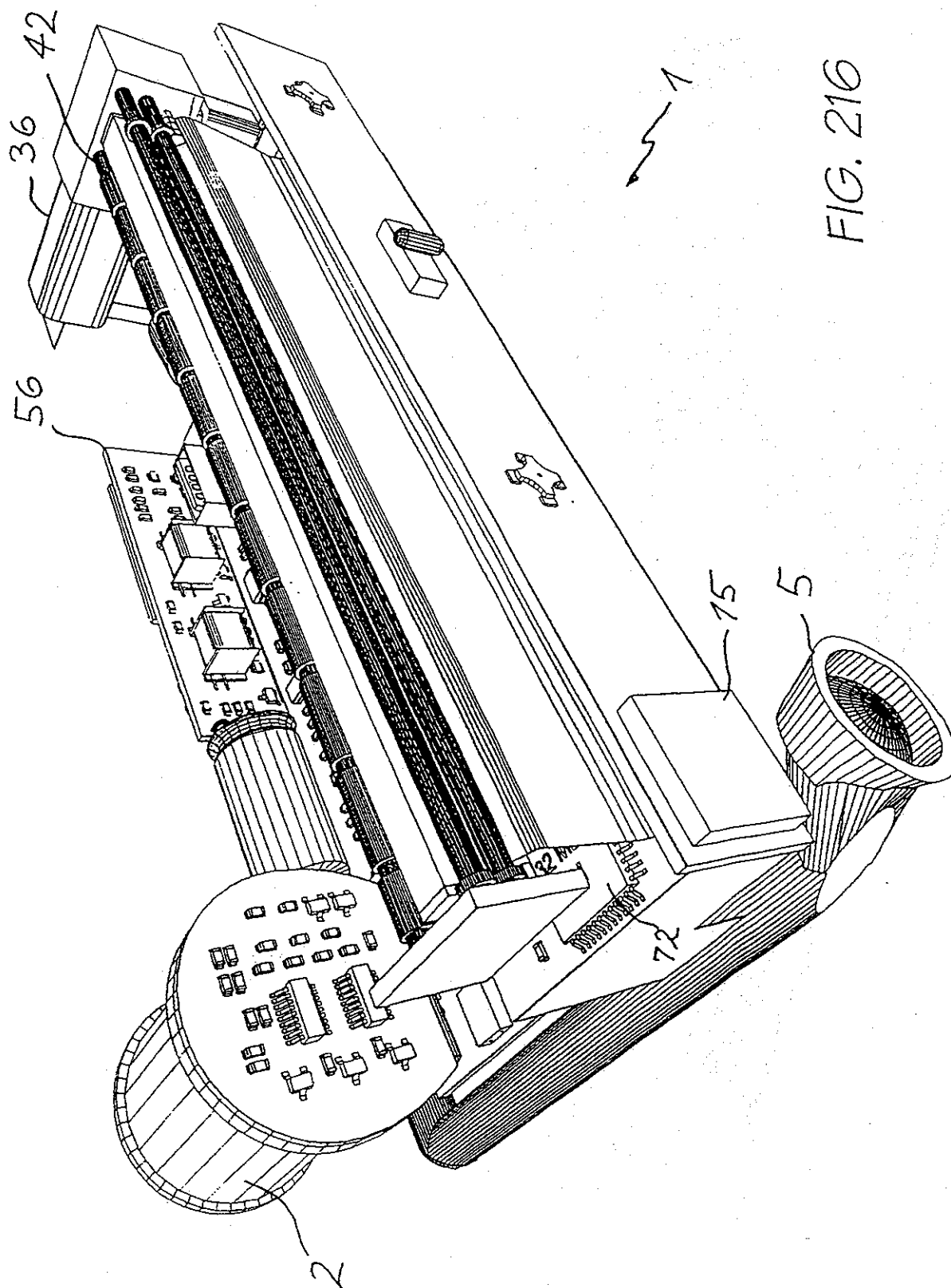
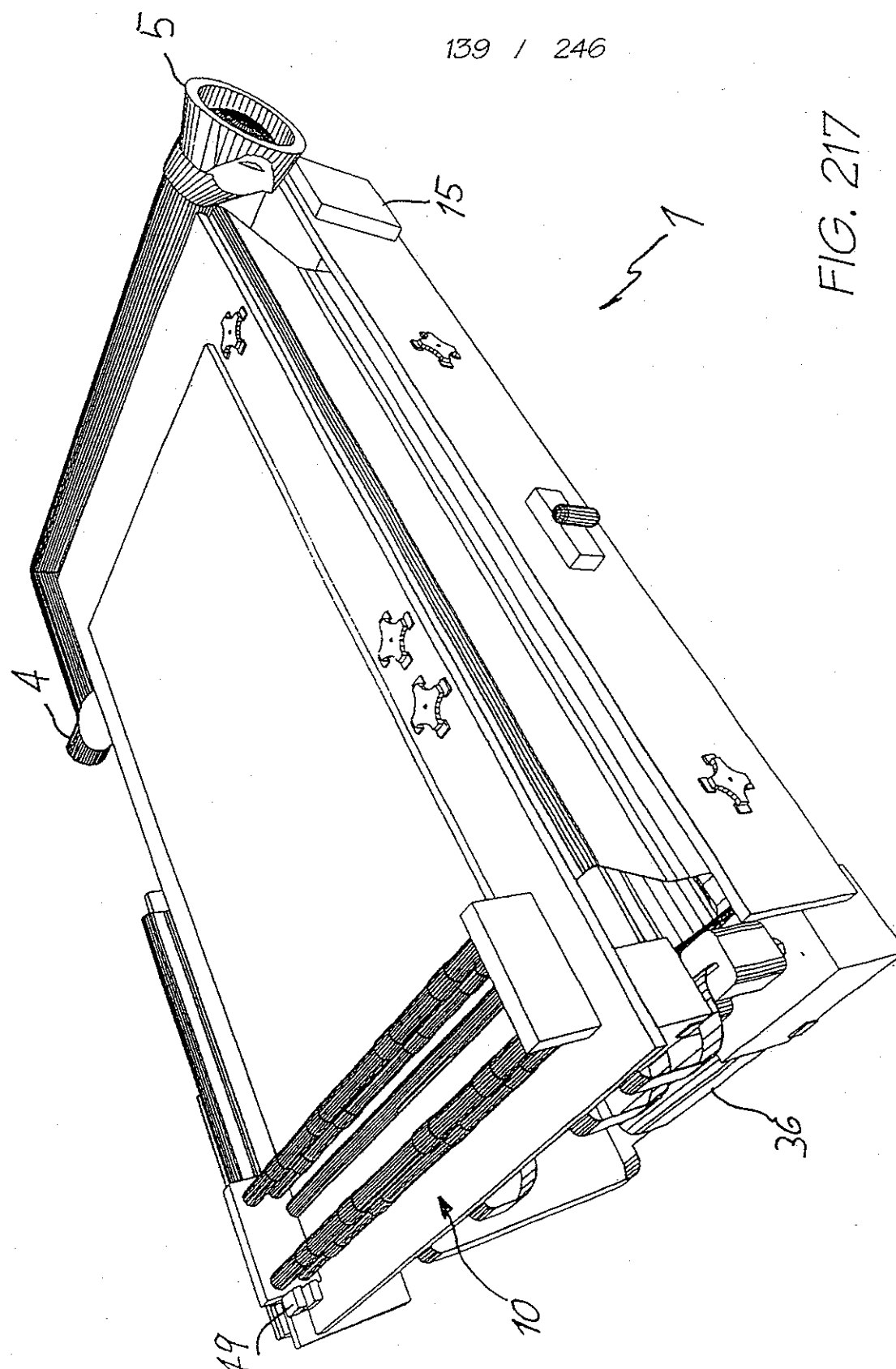


FIG. 215

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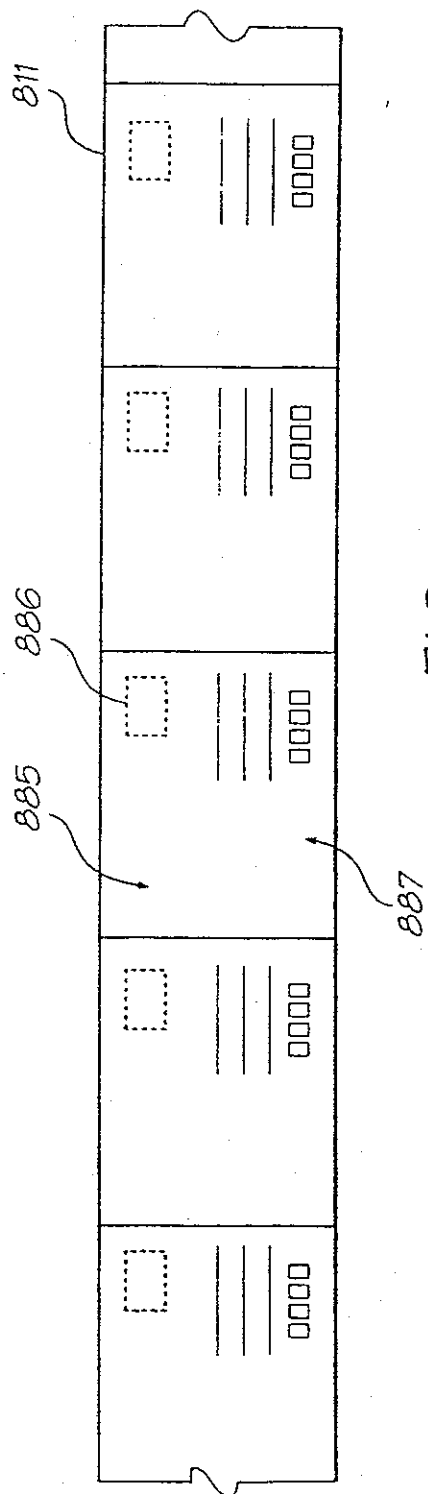


FIG. 218

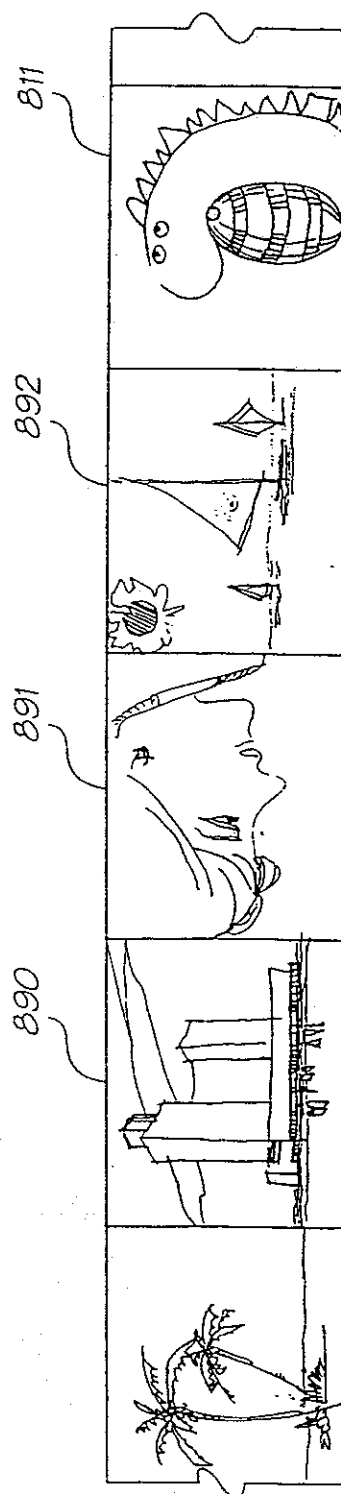


FIG. 219

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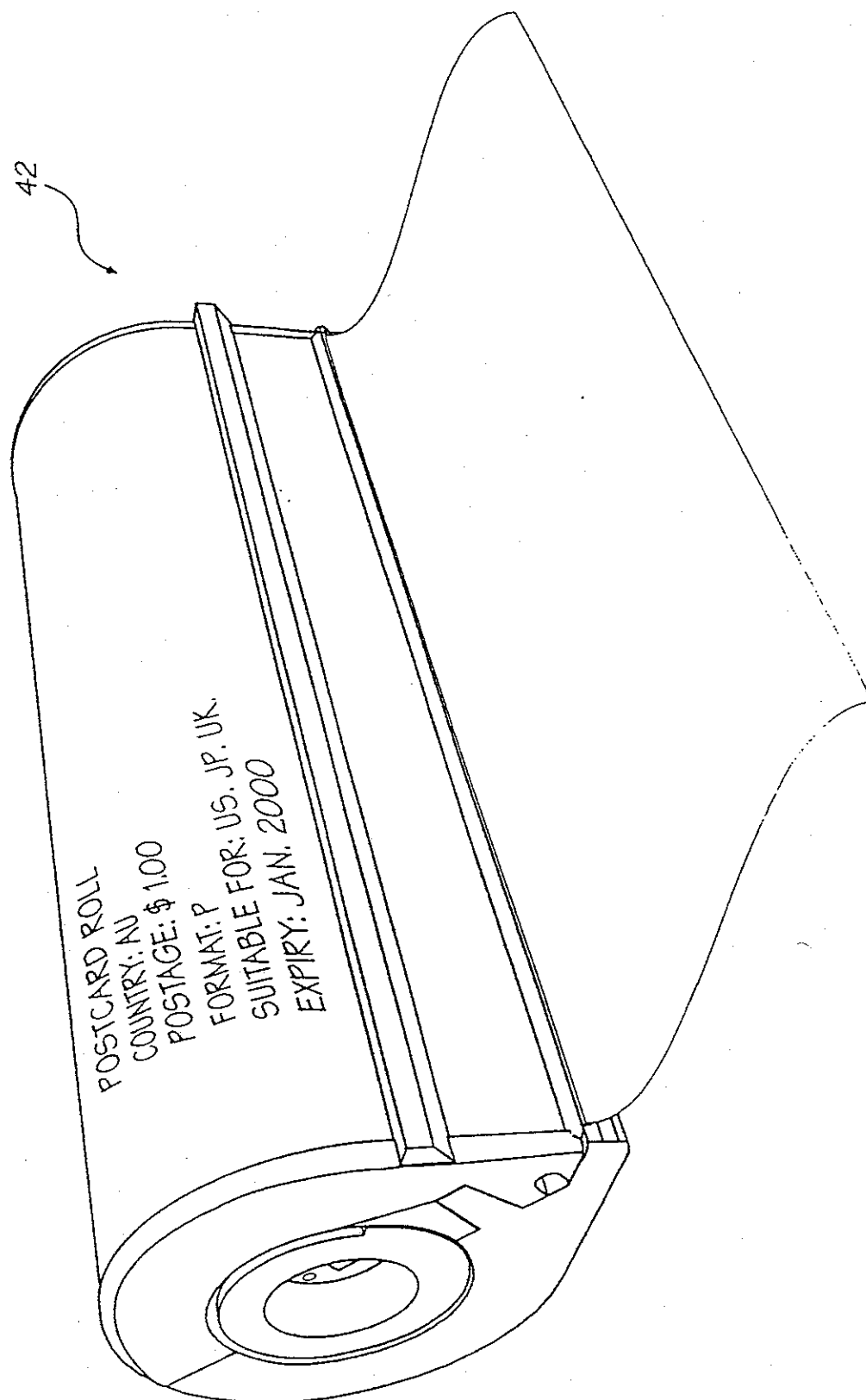


FIG. 220

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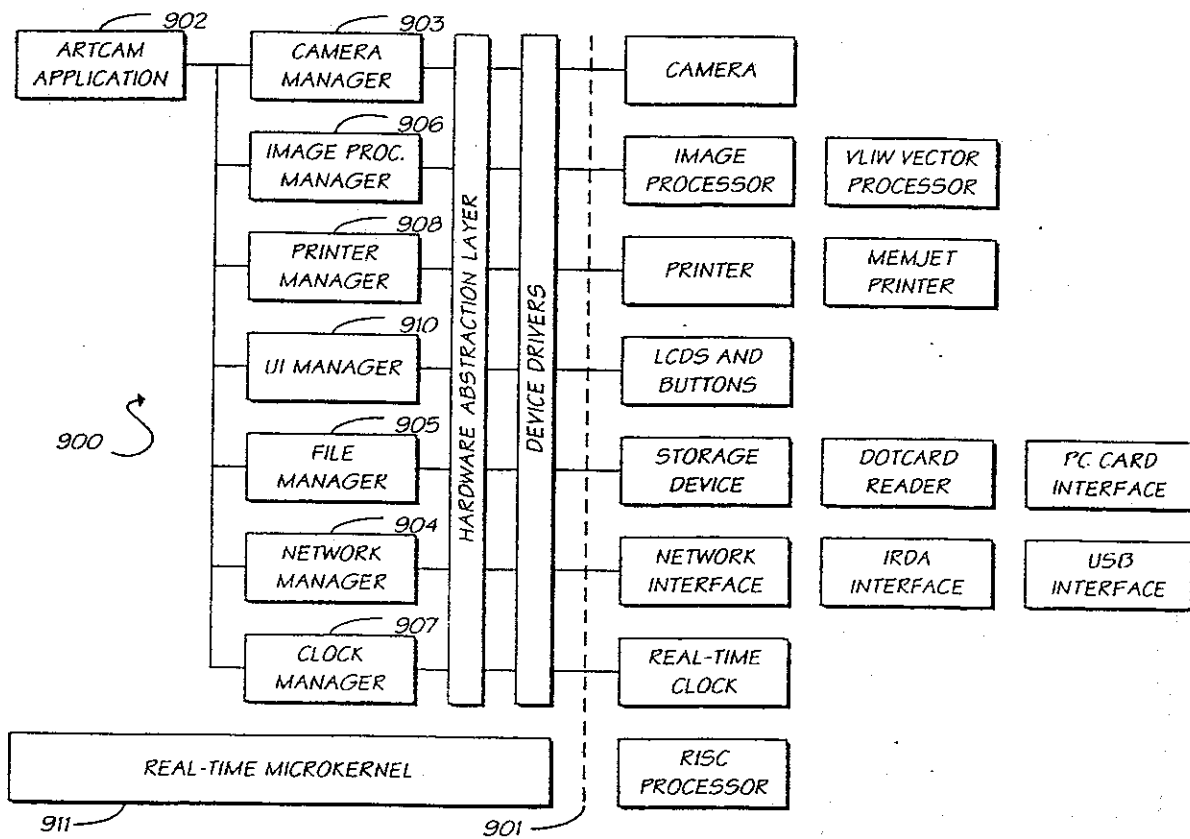
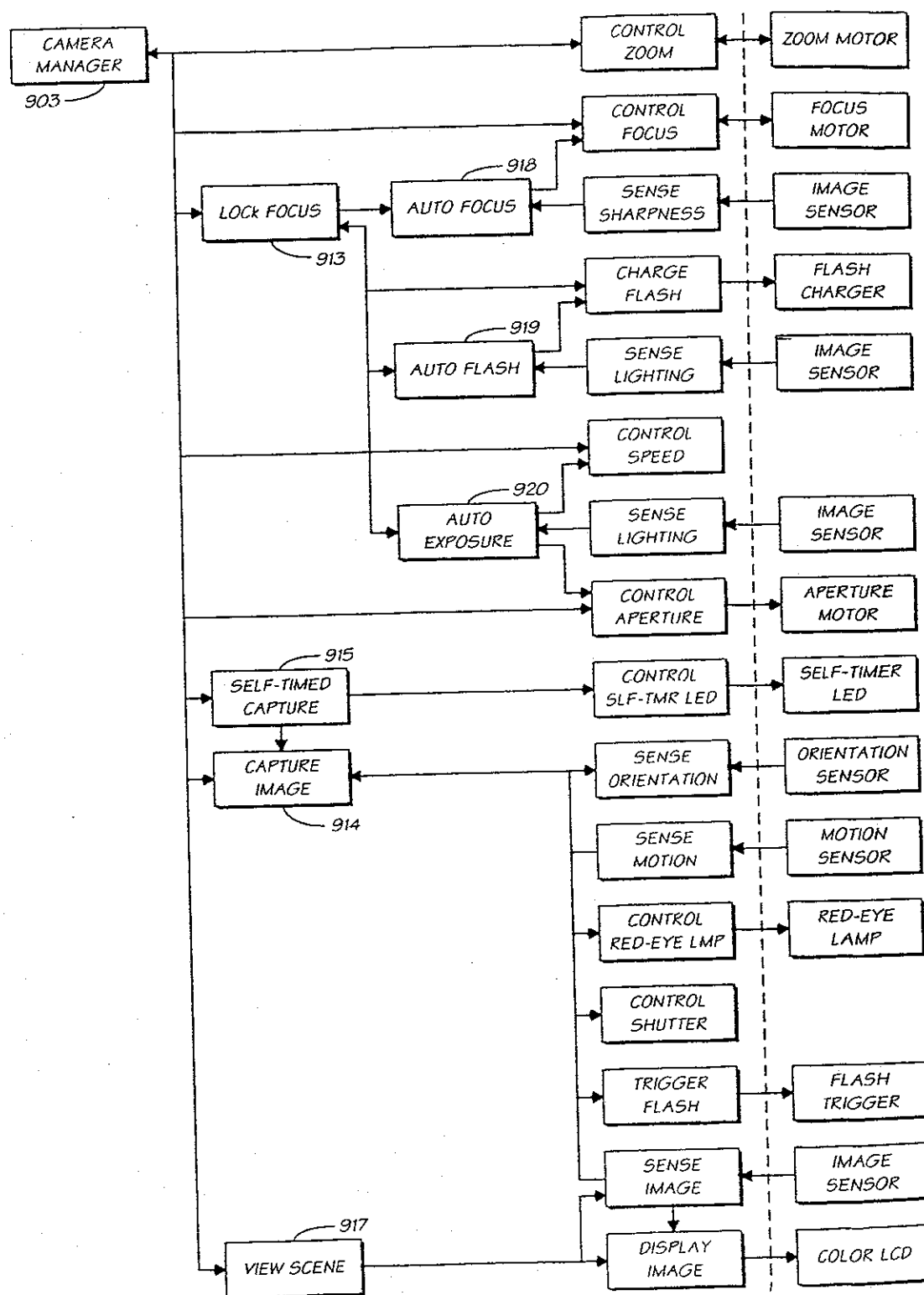


FIG. 221

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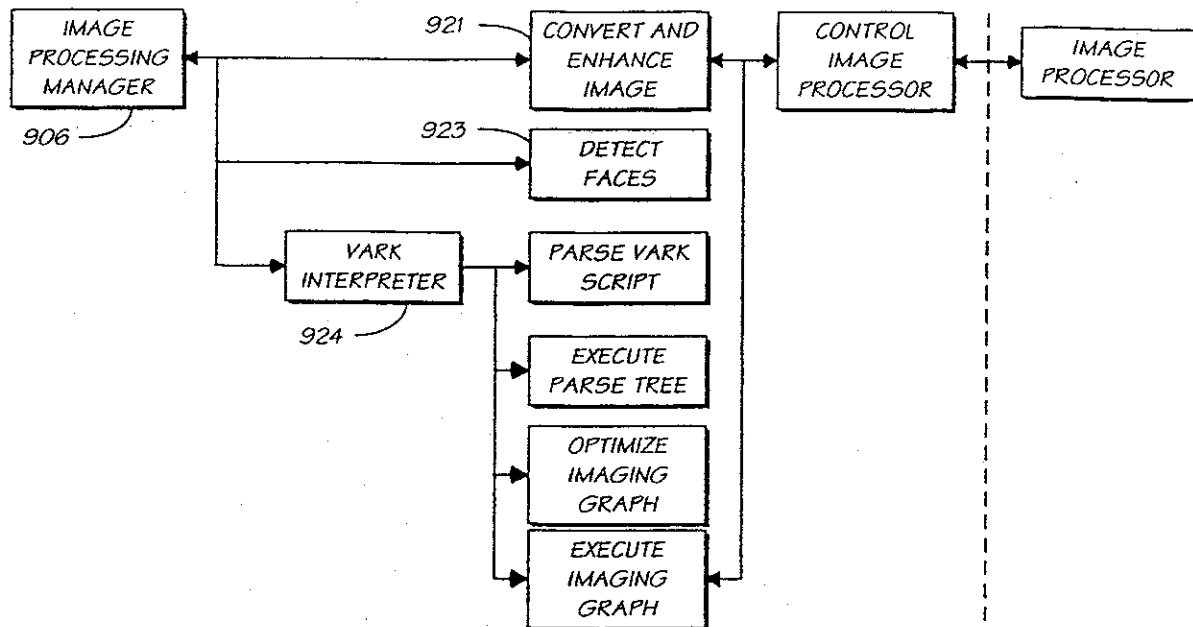


FIG. 223

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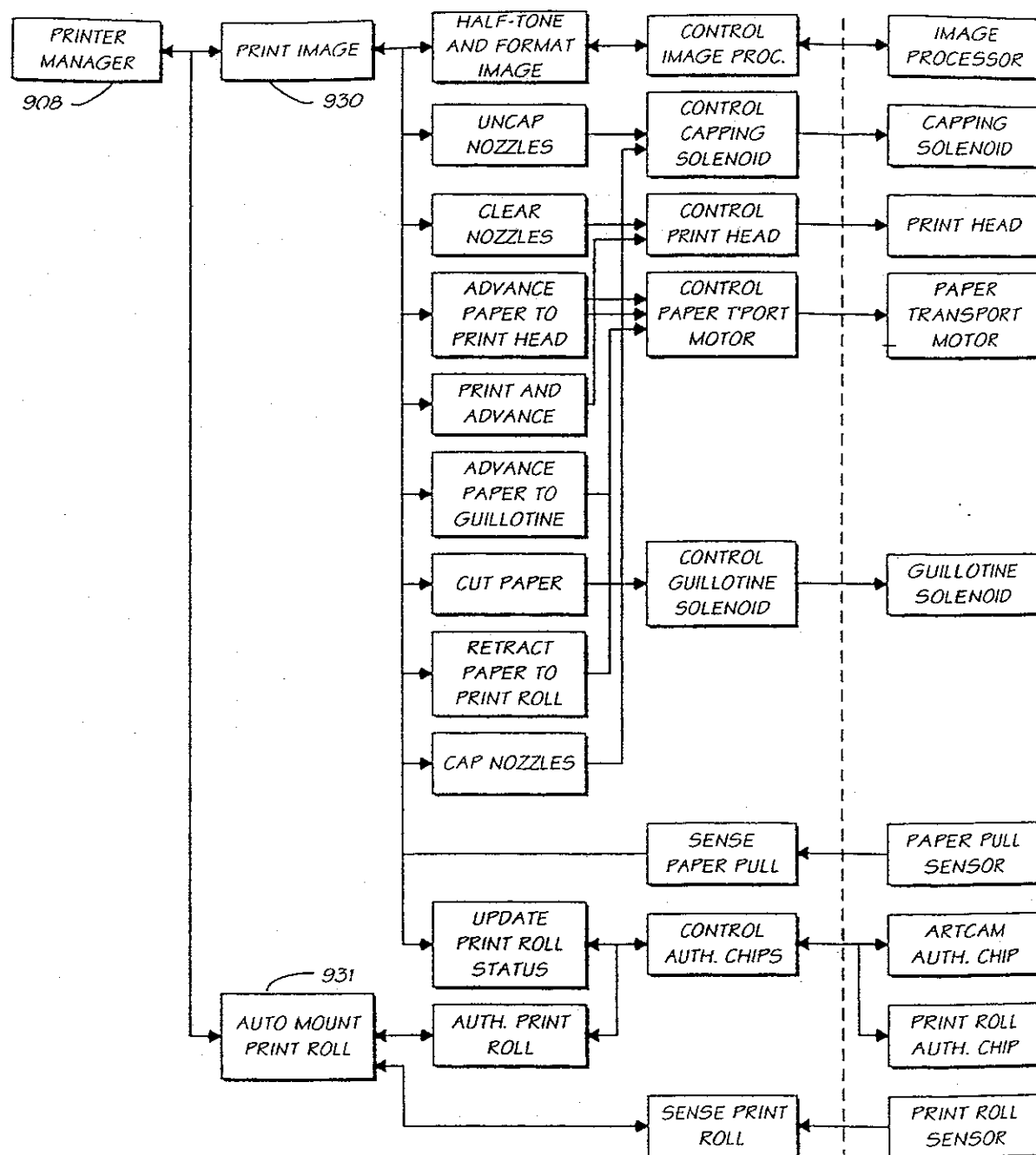


FIG. 224

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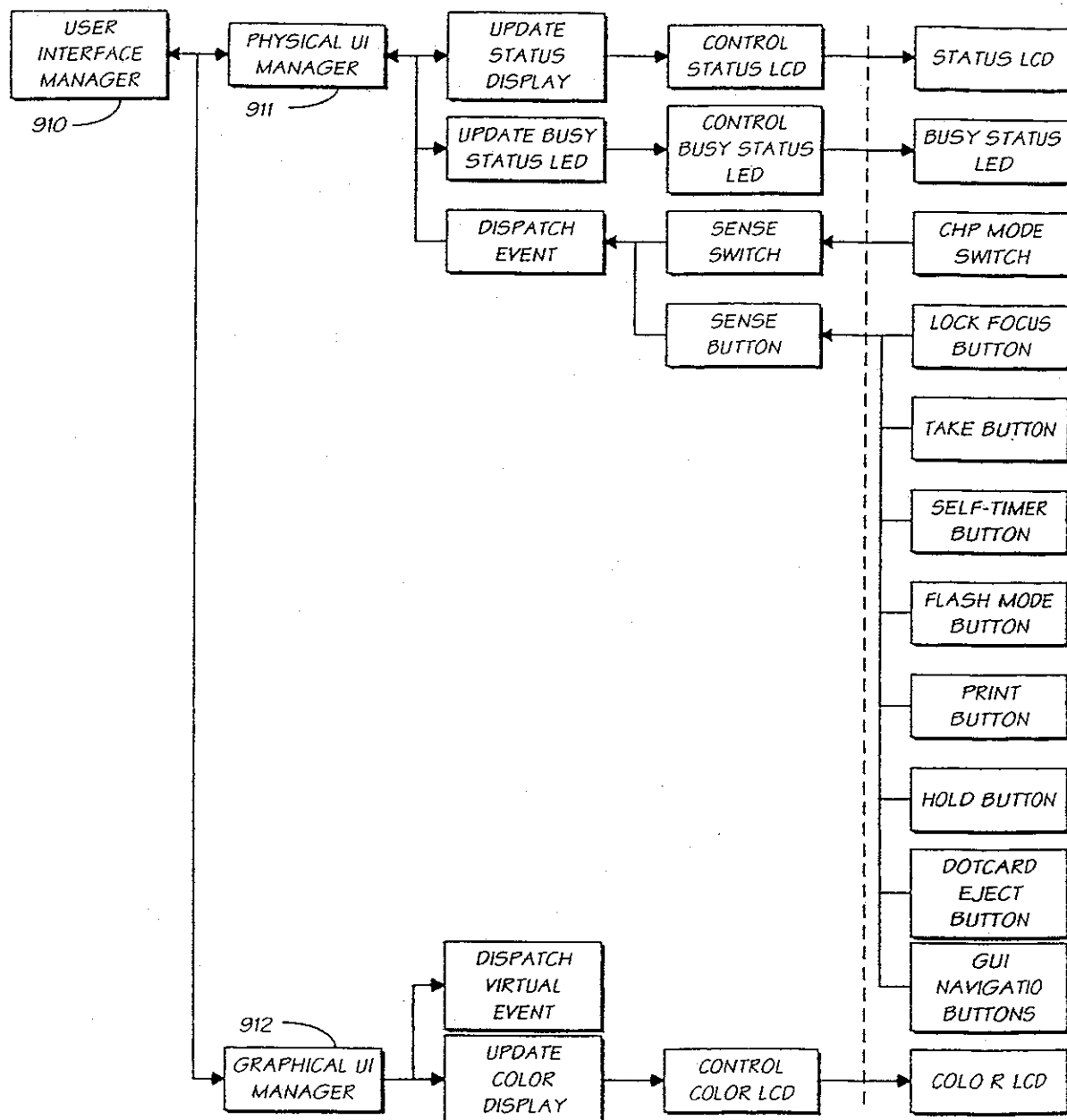


FIG. 225

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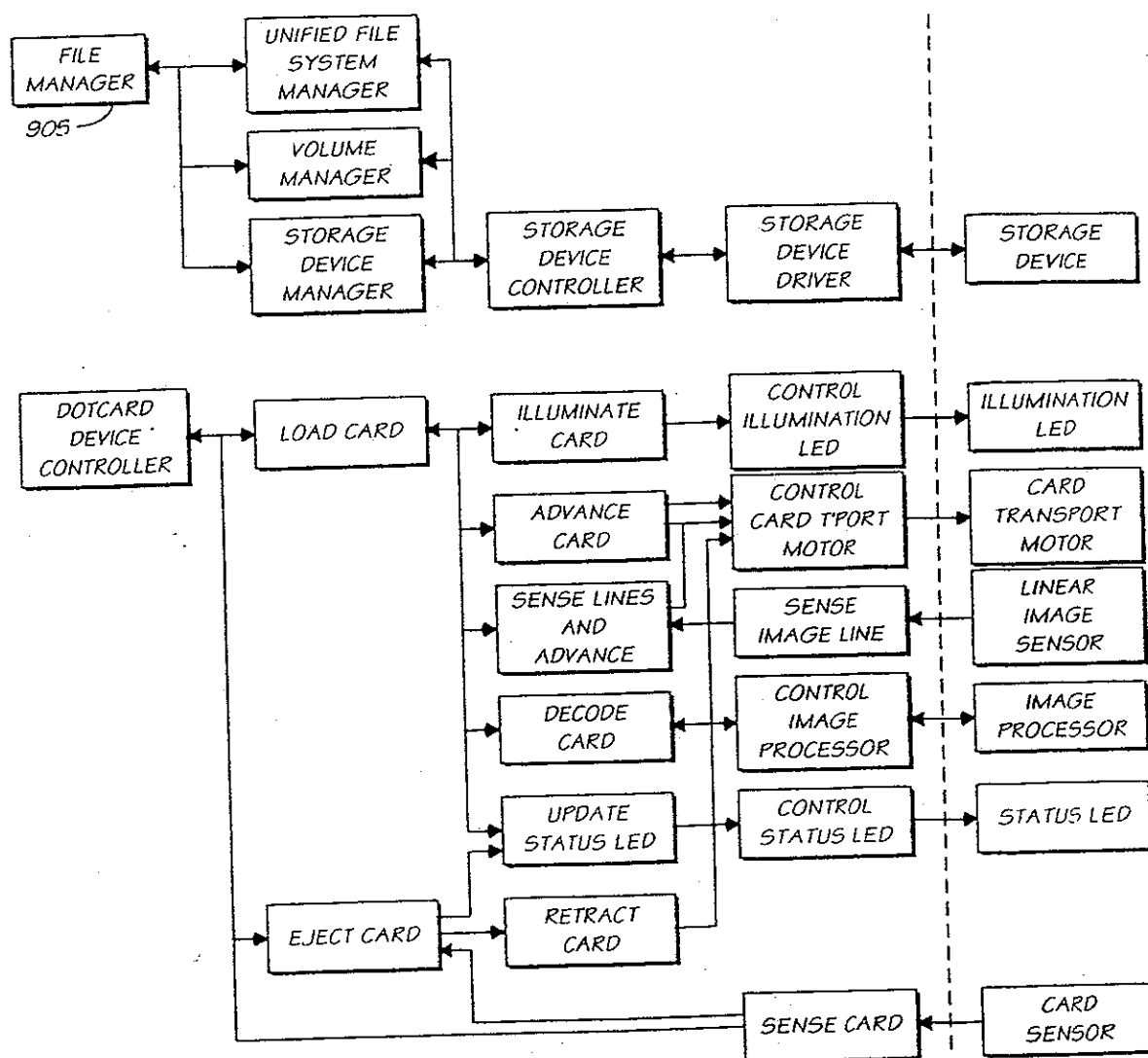


FIG. 226

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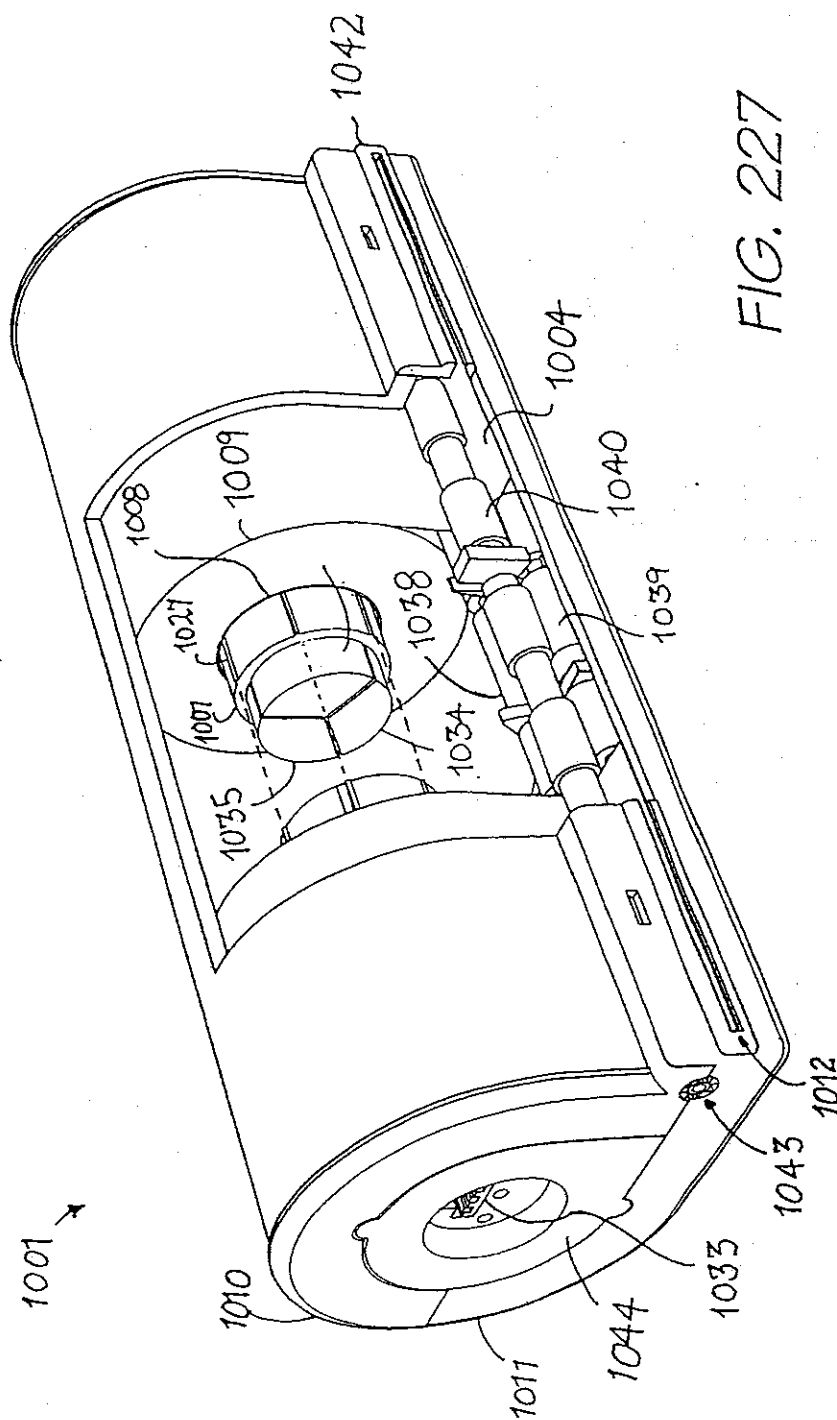
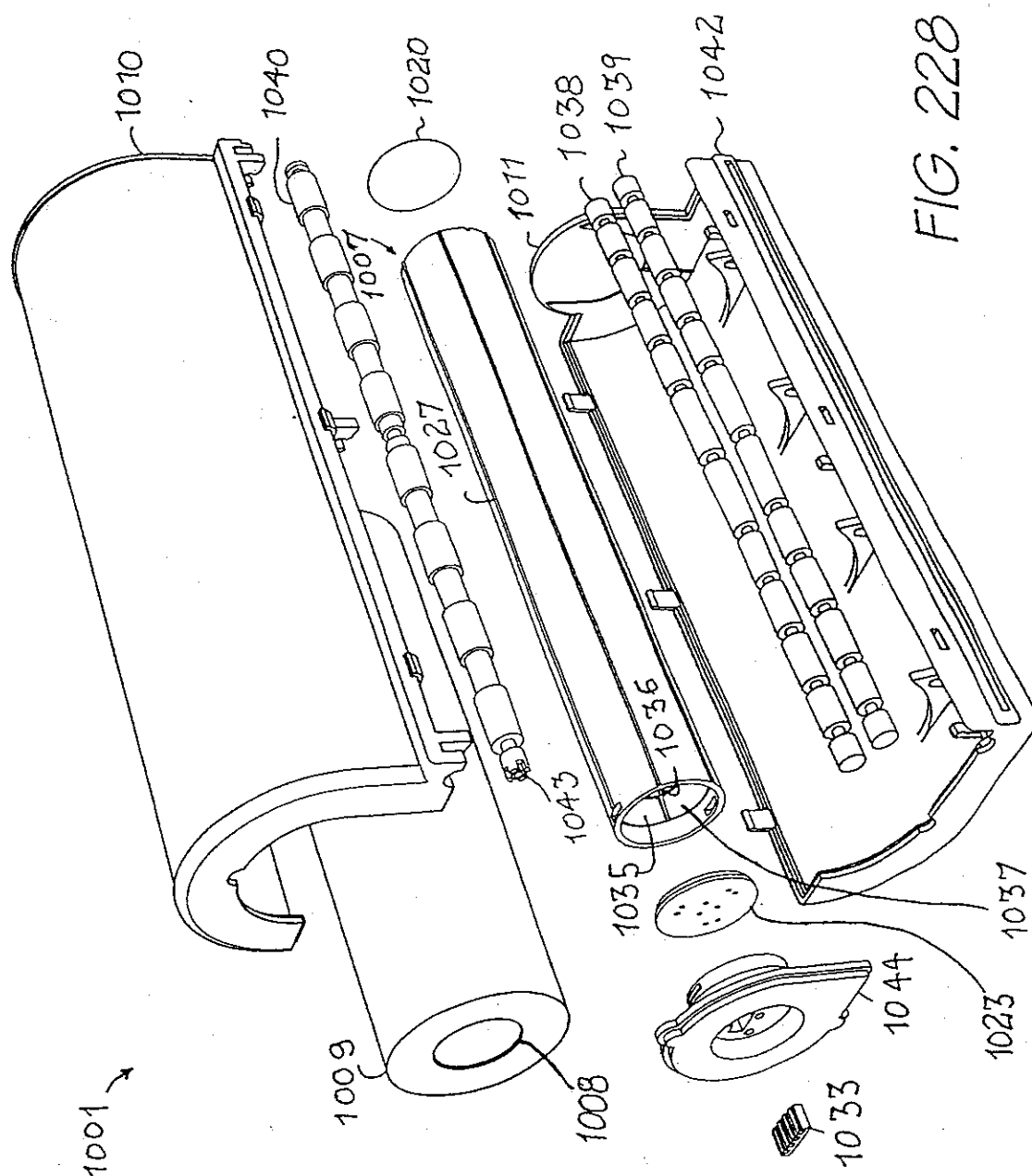
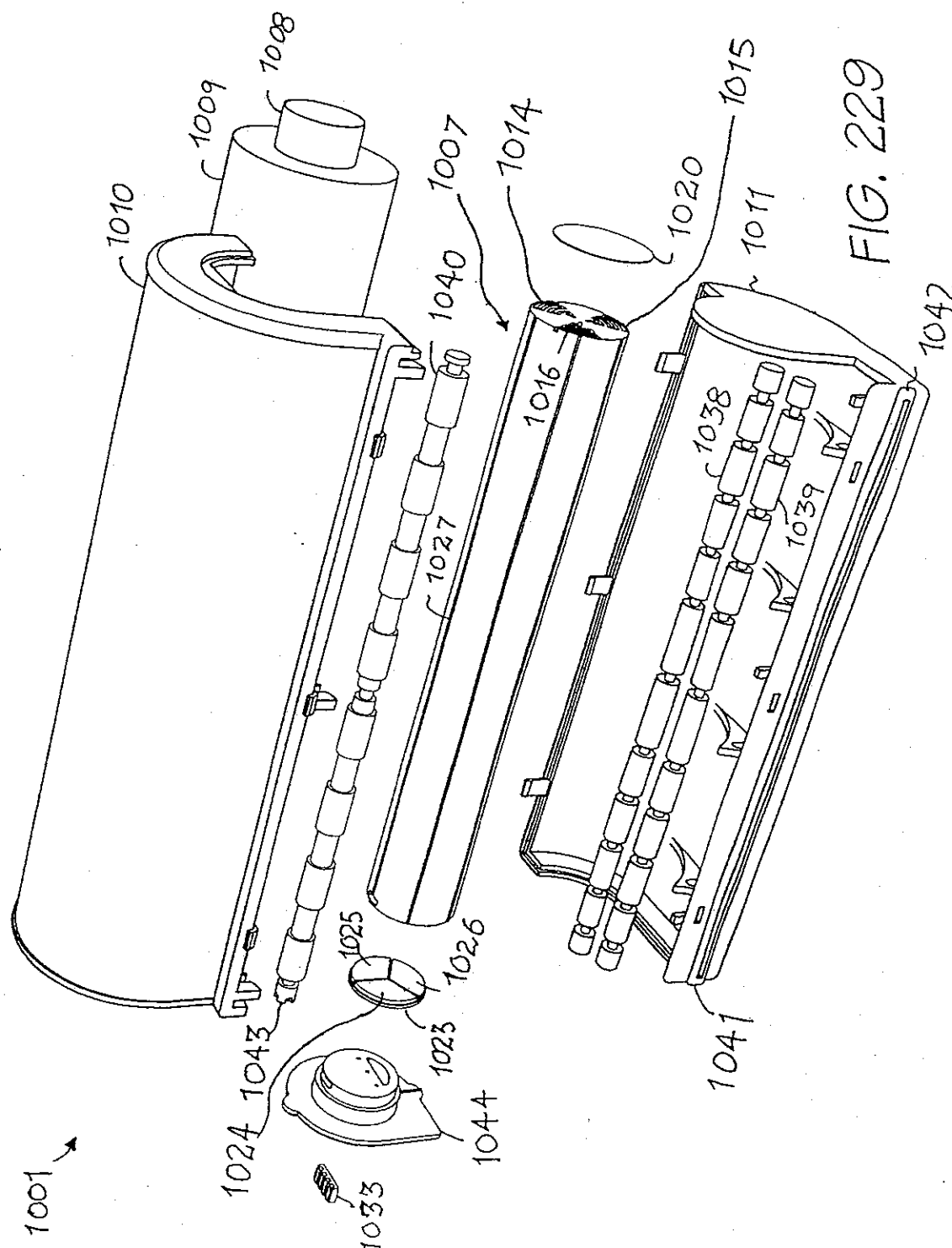


FIG. 227

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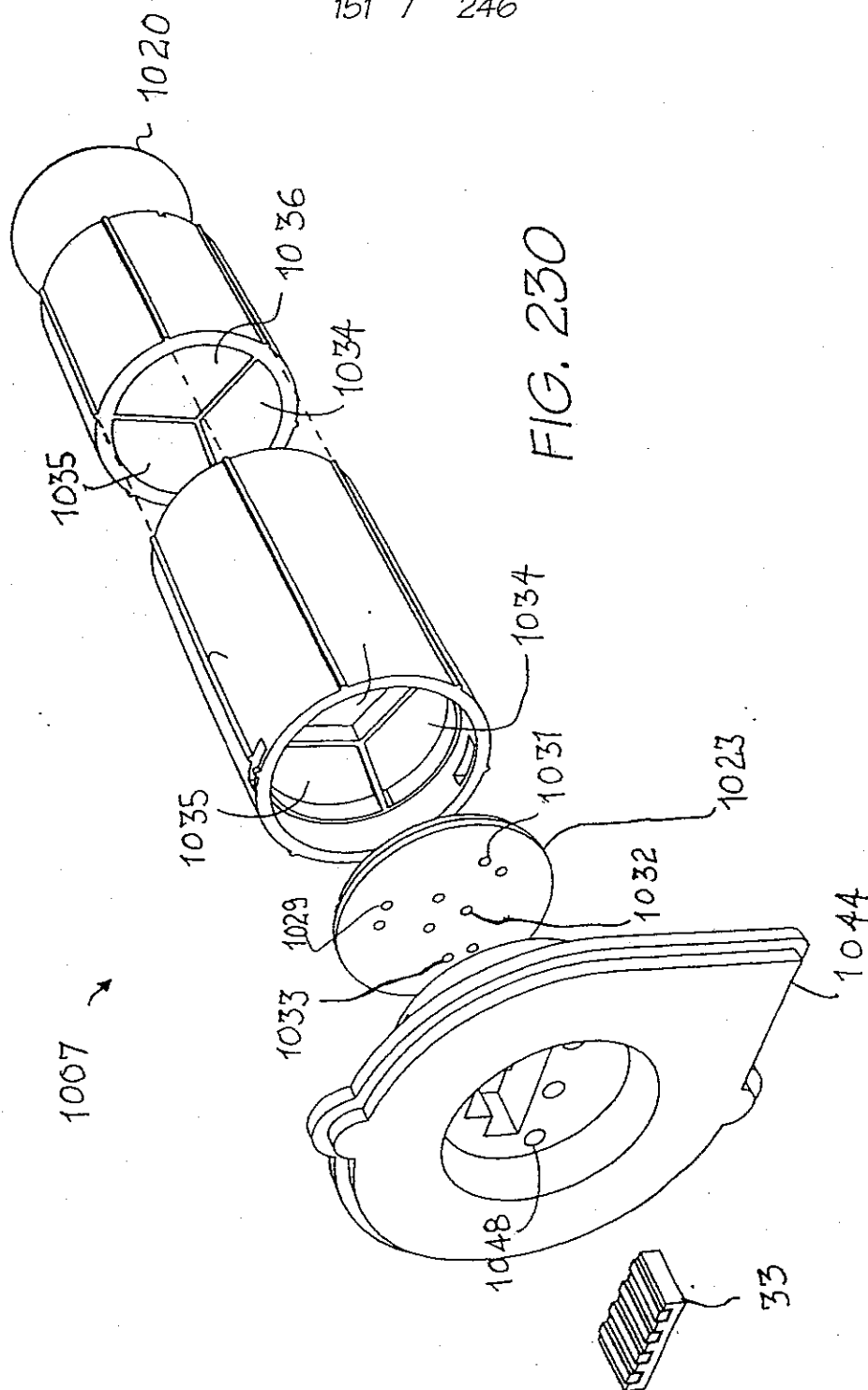


FIG. 230

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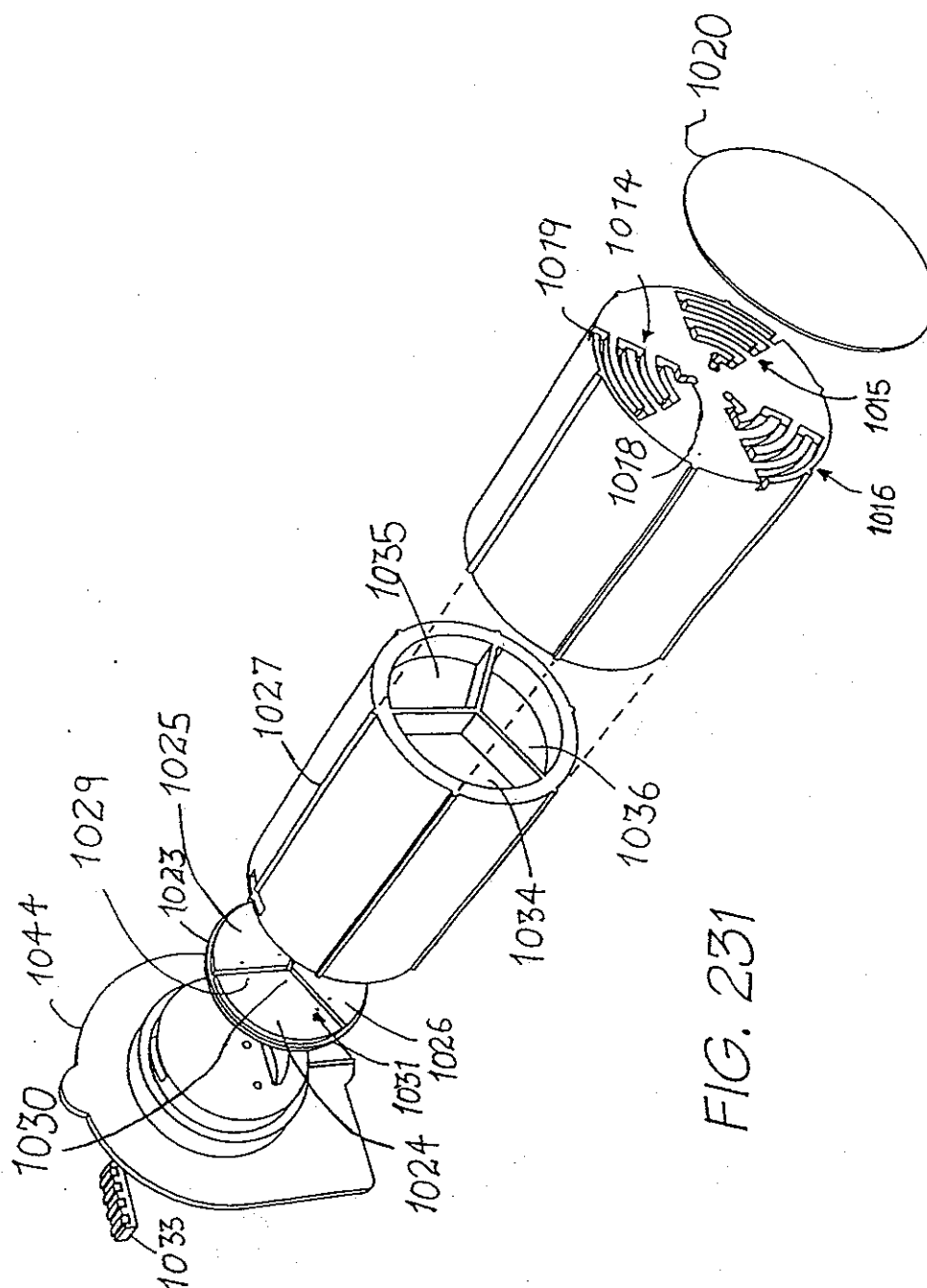


FIG. 231

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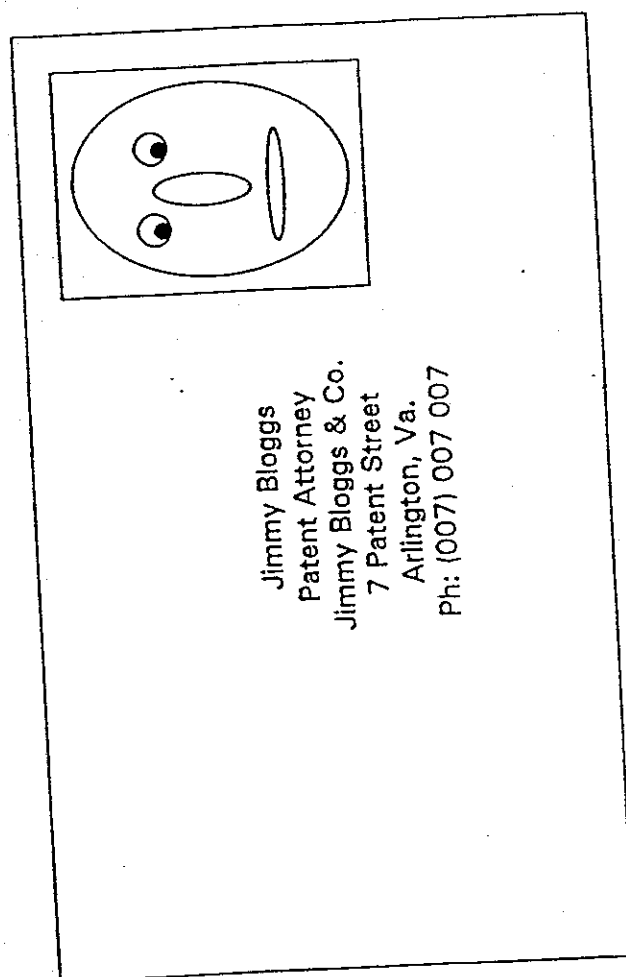


FIG. 232

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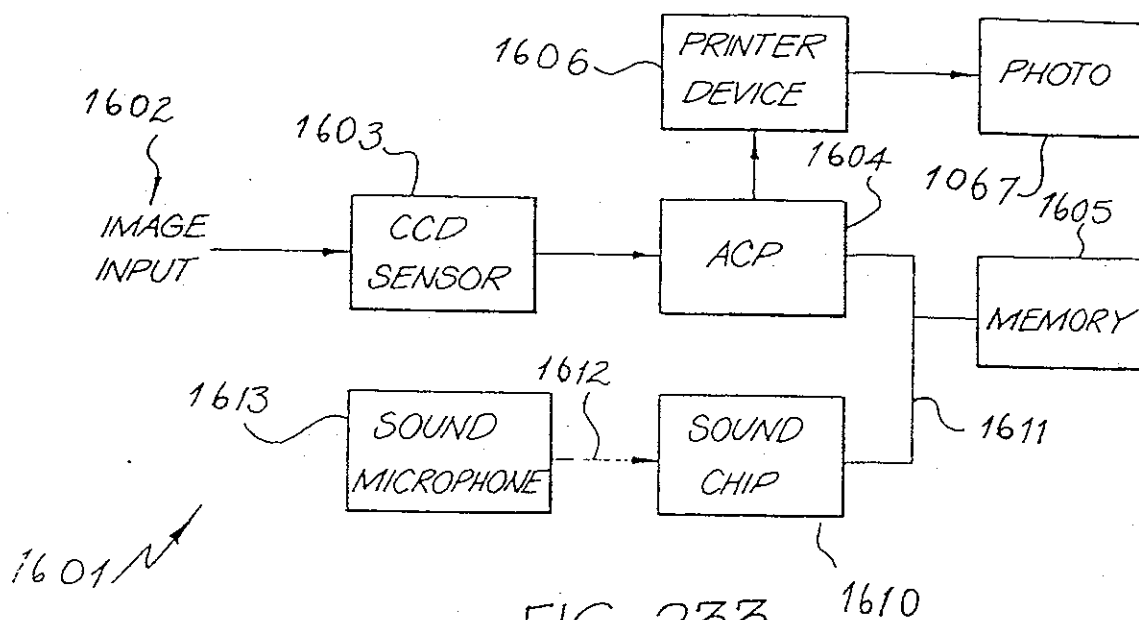


FIG. 233

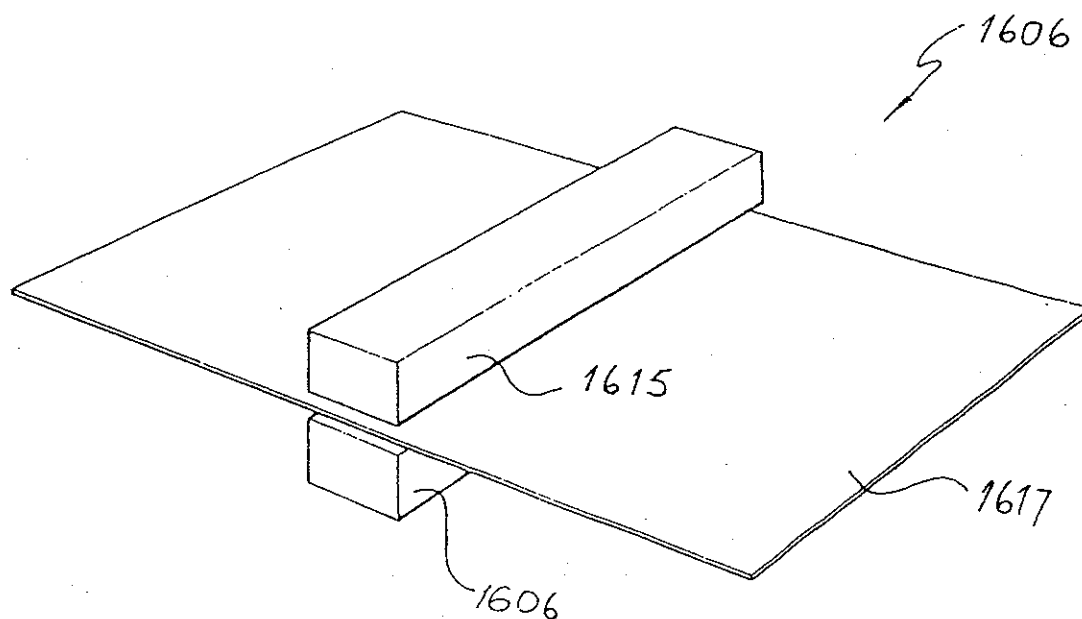


FIG. 234

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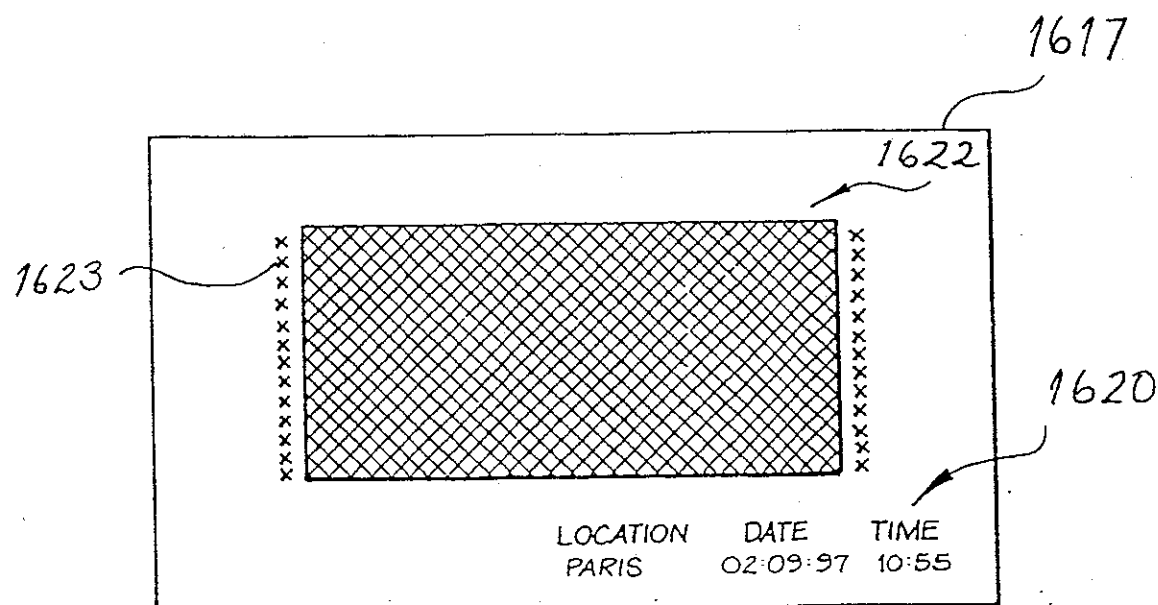


FIG. 235

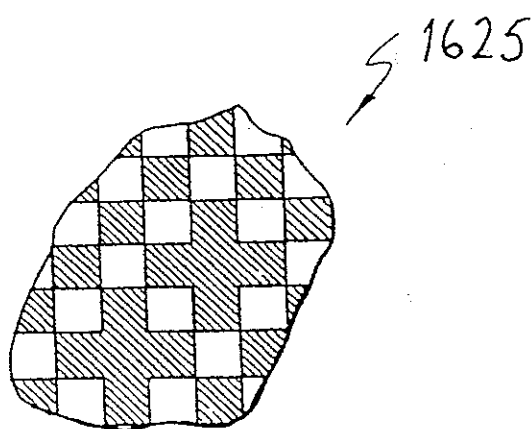
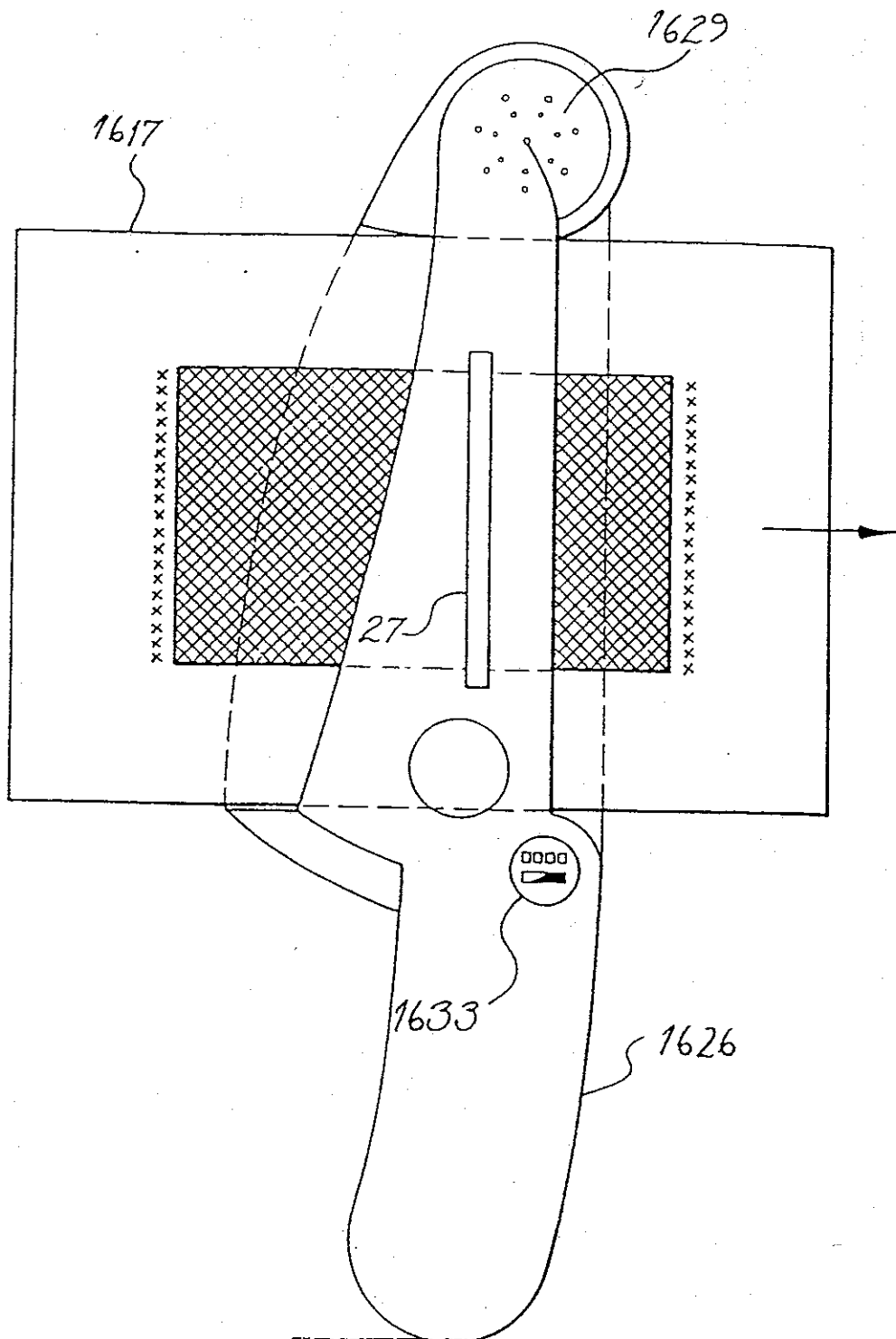


FIG. 236

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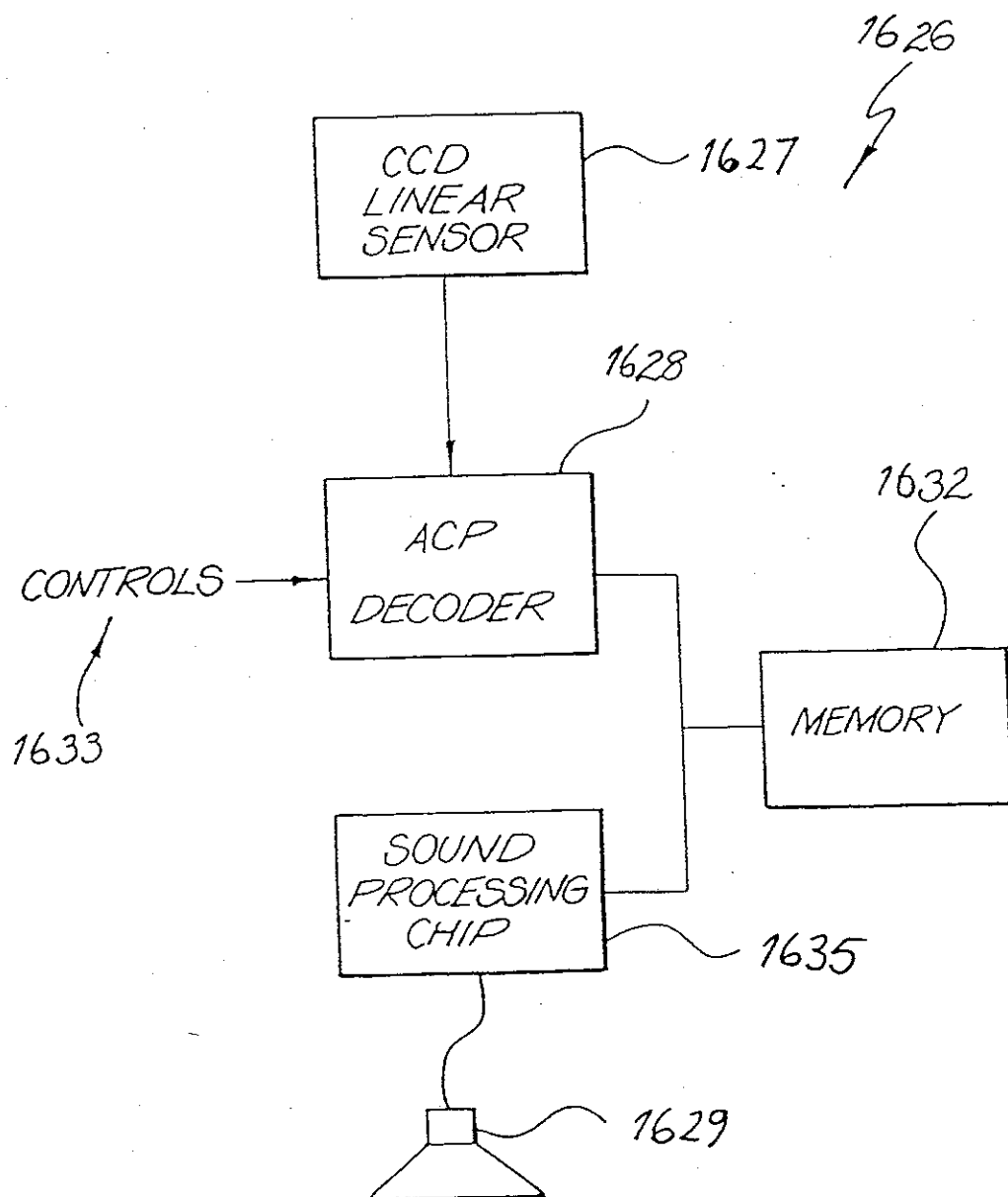


FIG. 238

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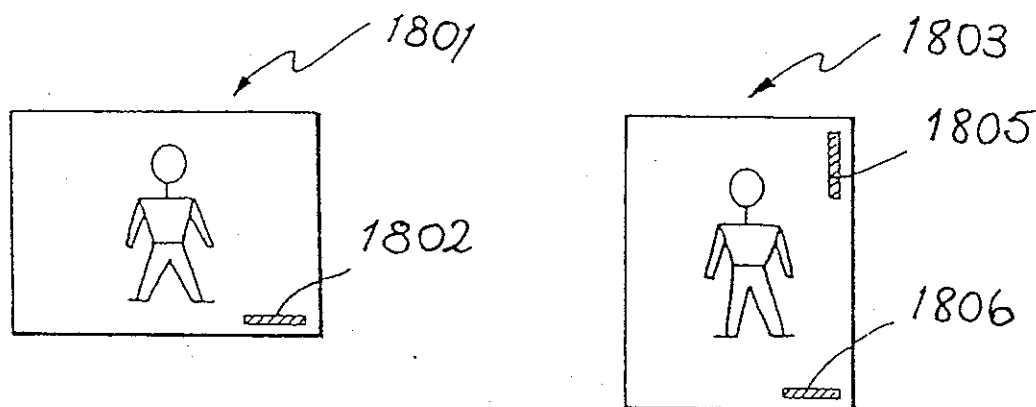


FIG. 239

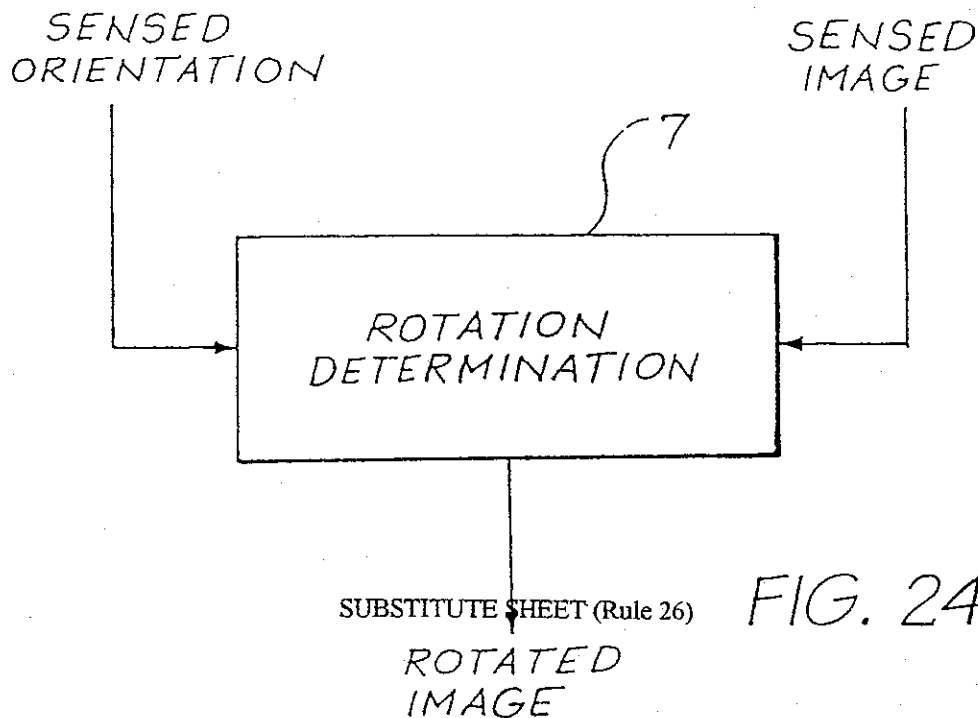


FIG. 240

SUBSTITUTE SHEET (Rule 26)

SUBSTITUTE SHEET (Rule 26)

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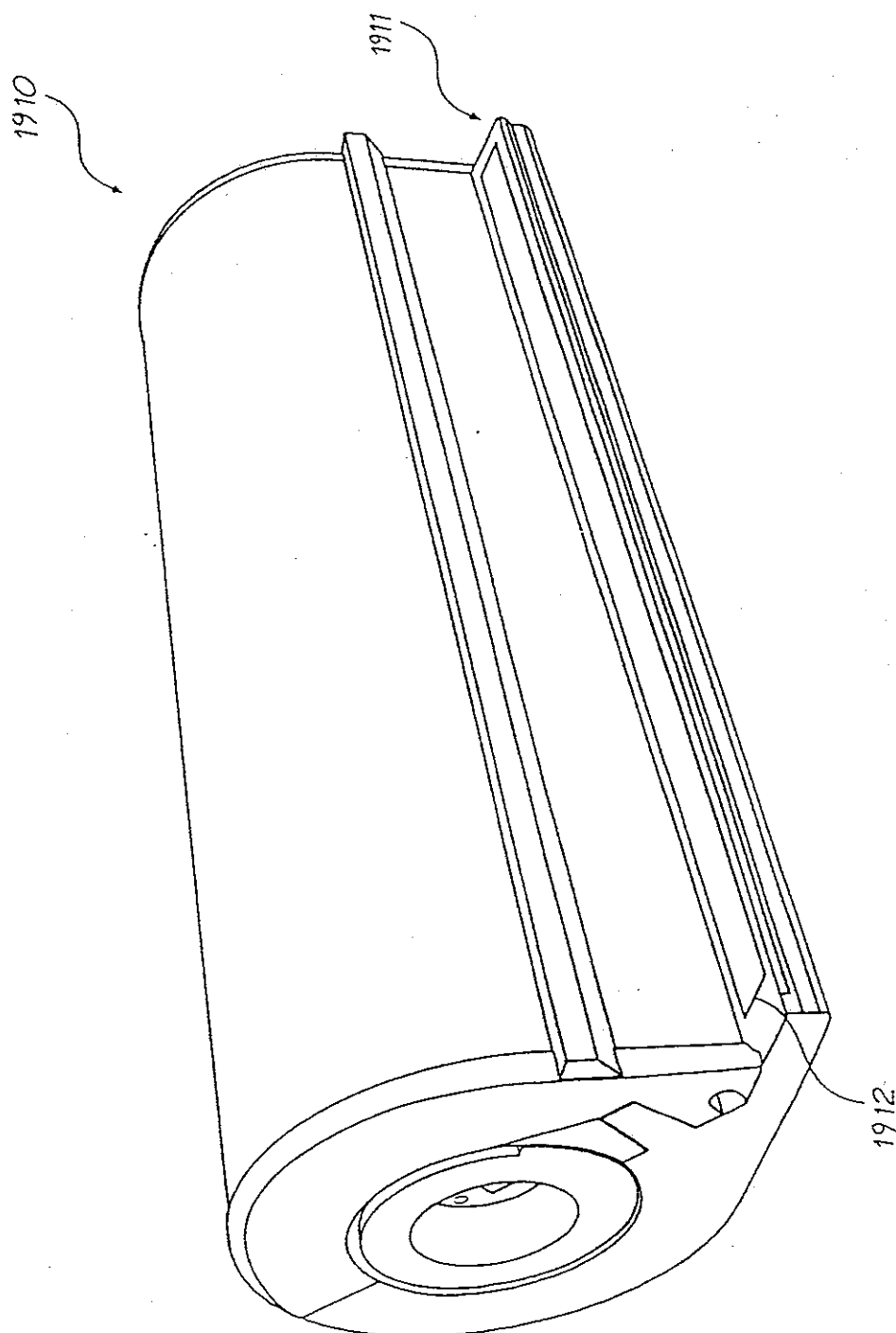


FIG. 241

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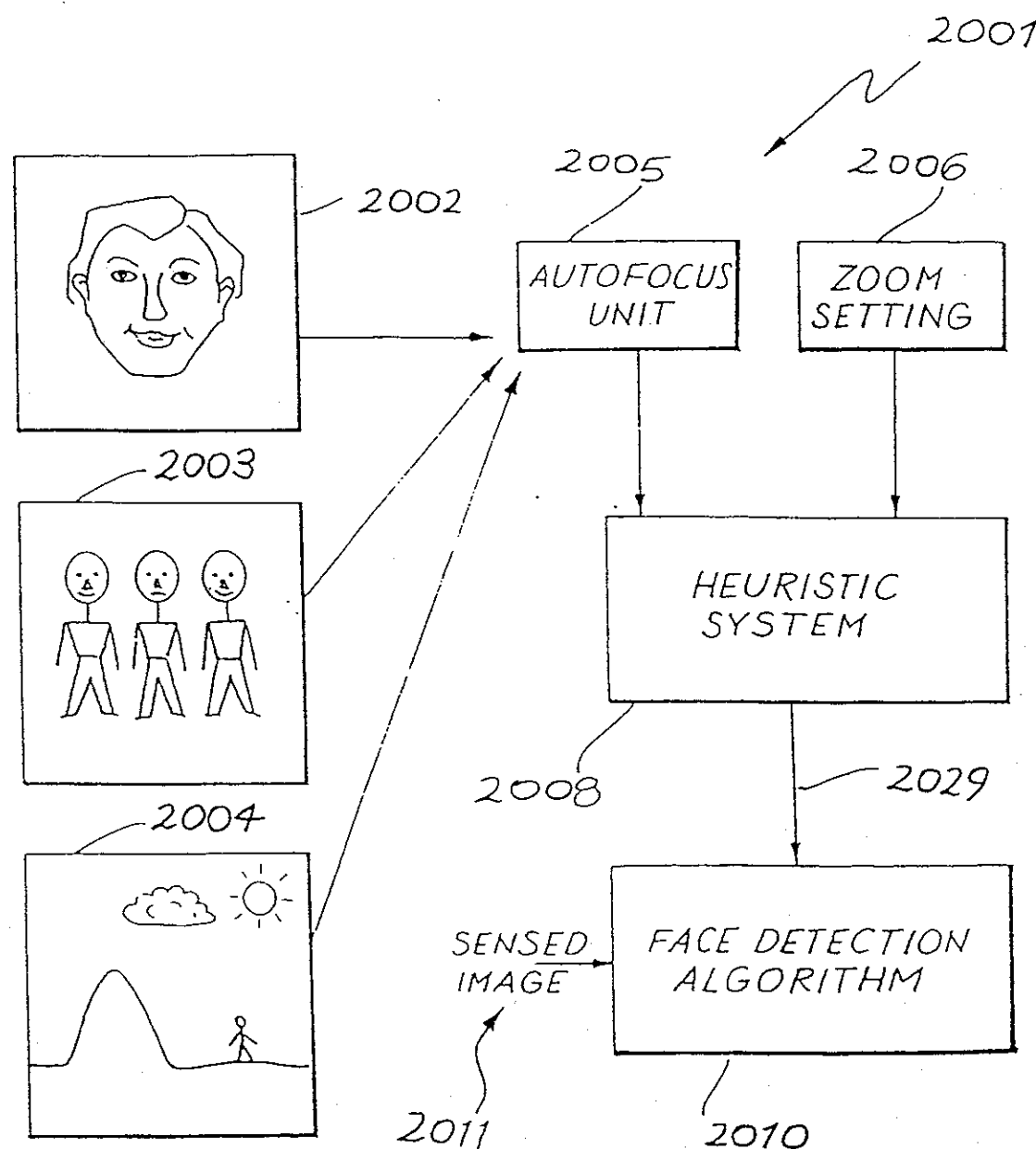


FIG. 242

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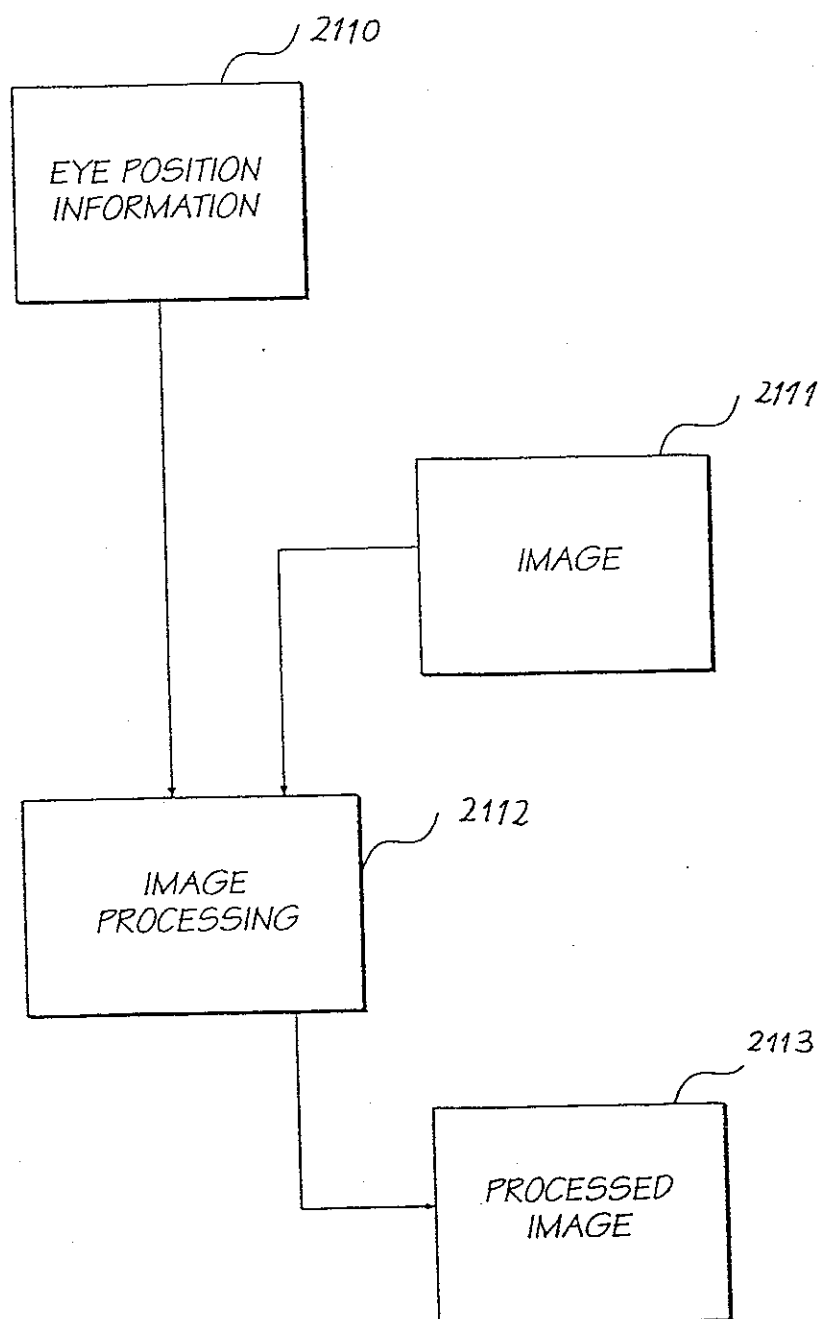


FIG. 243

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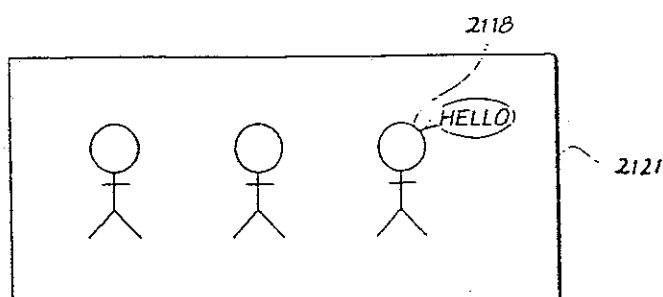
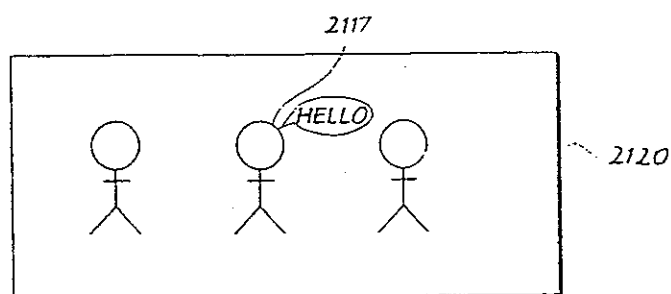
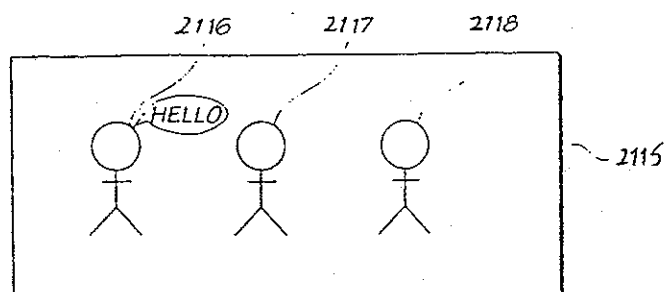


FIG. 244

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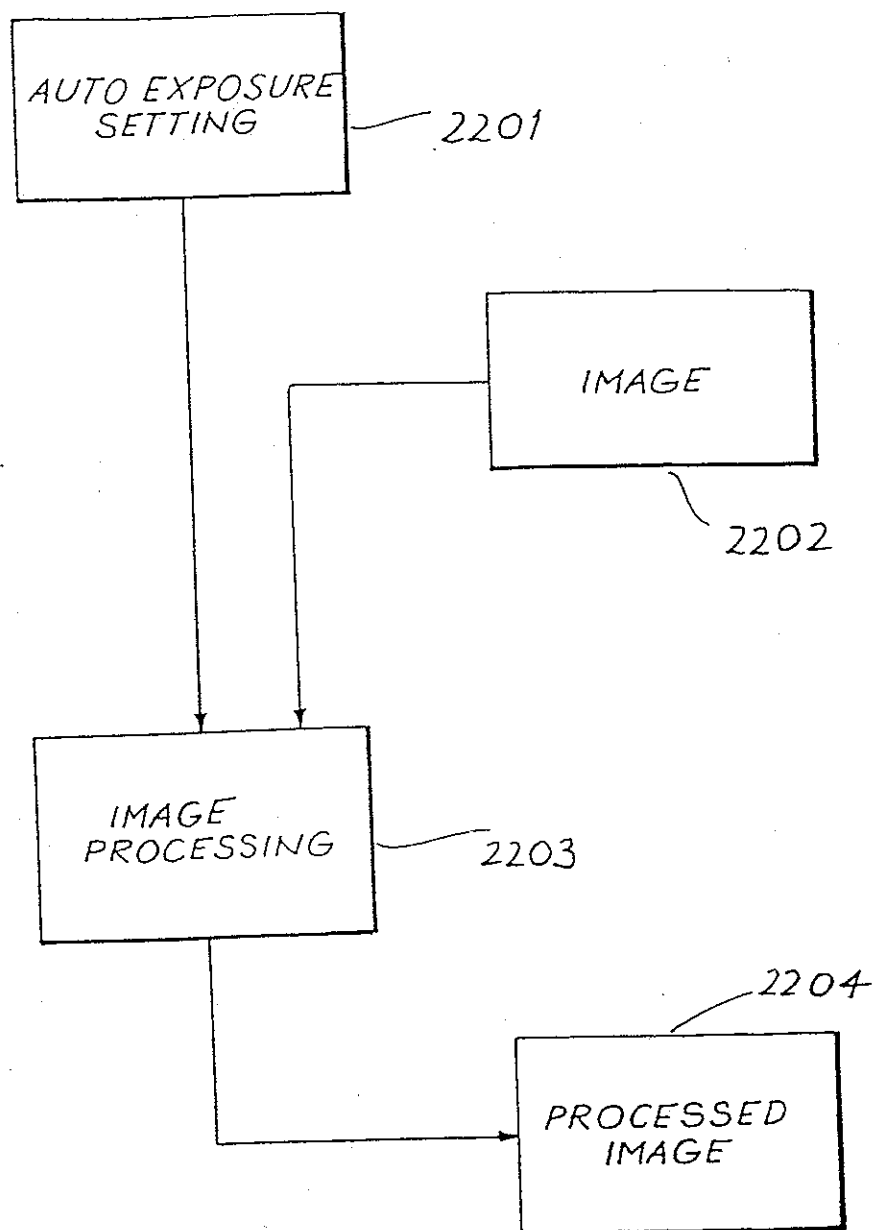


FIG. 245

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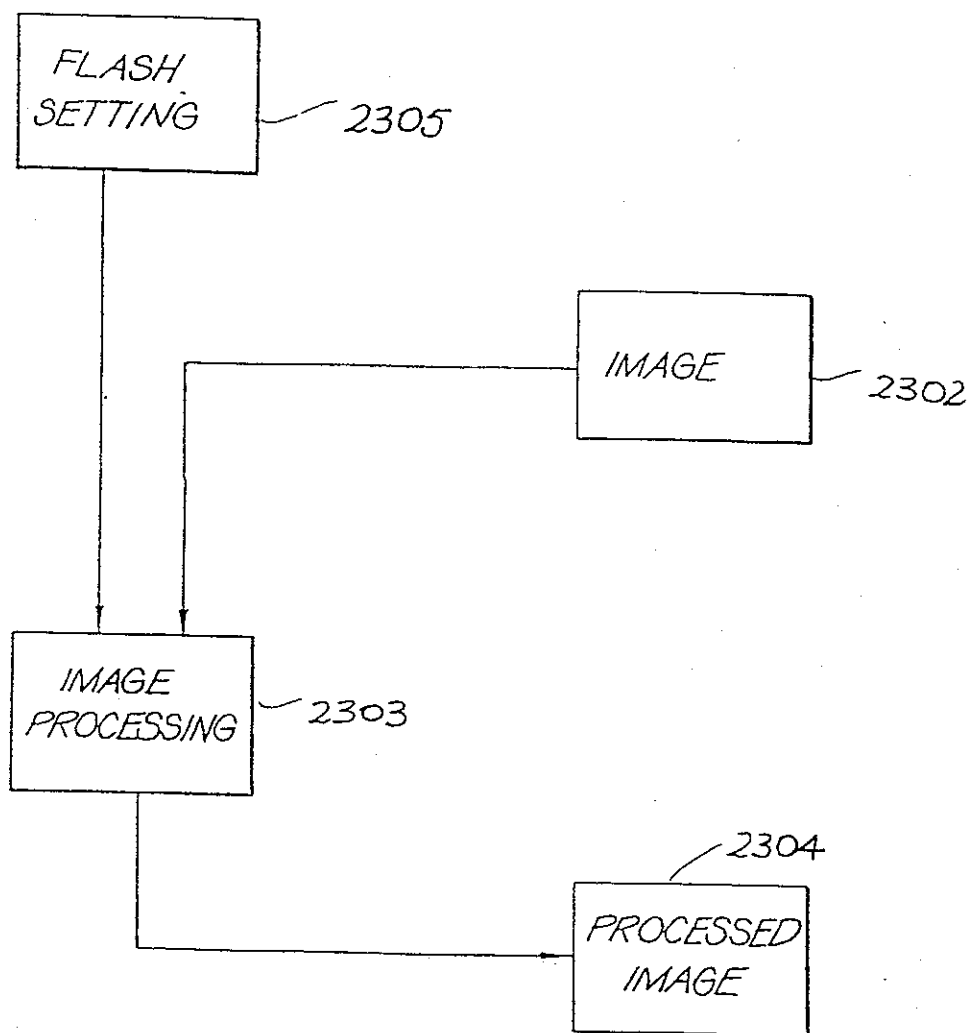
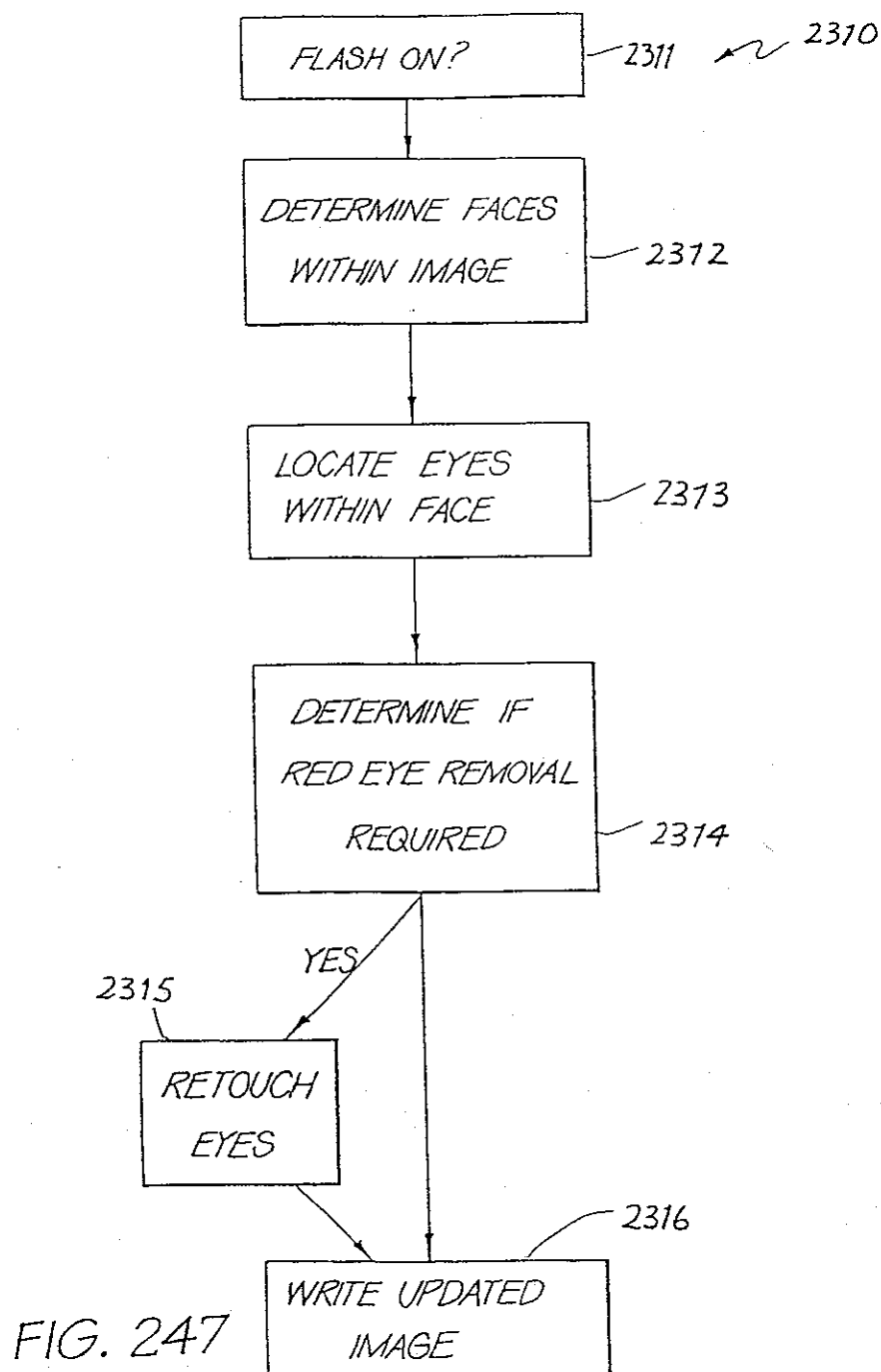


FIG. 246

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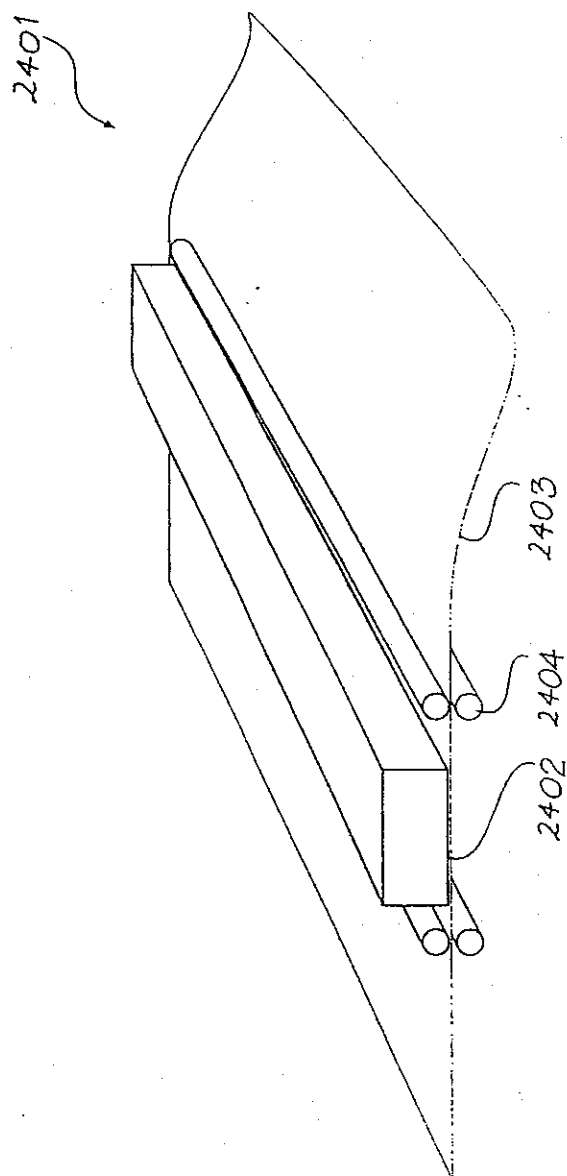


FIG. 248

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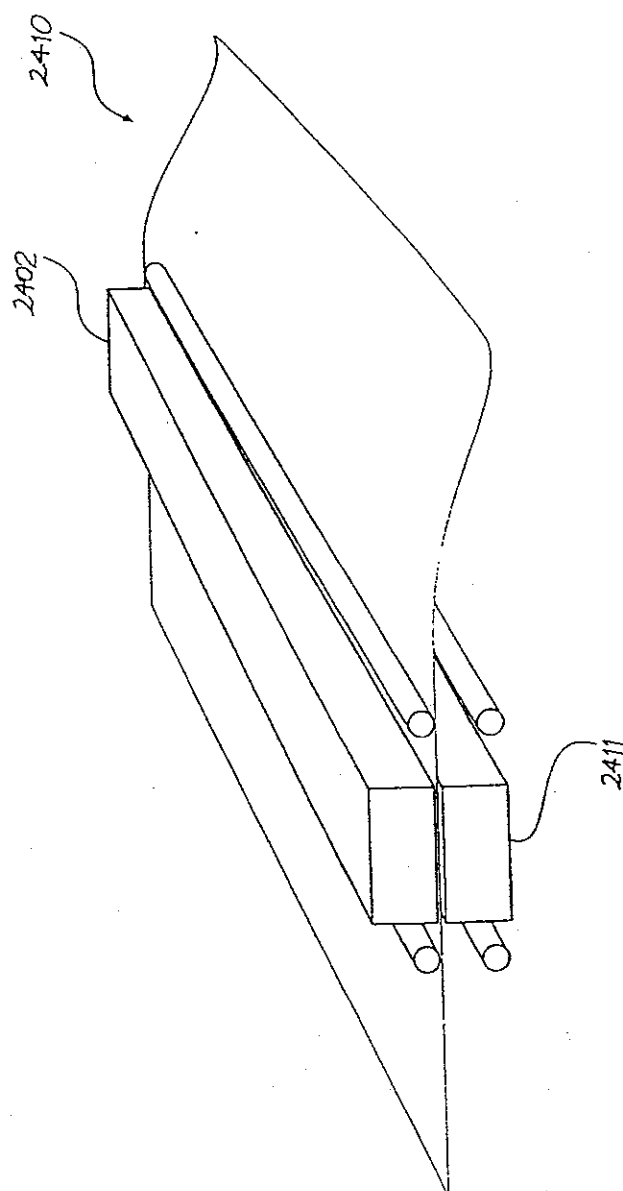


FIG. 249

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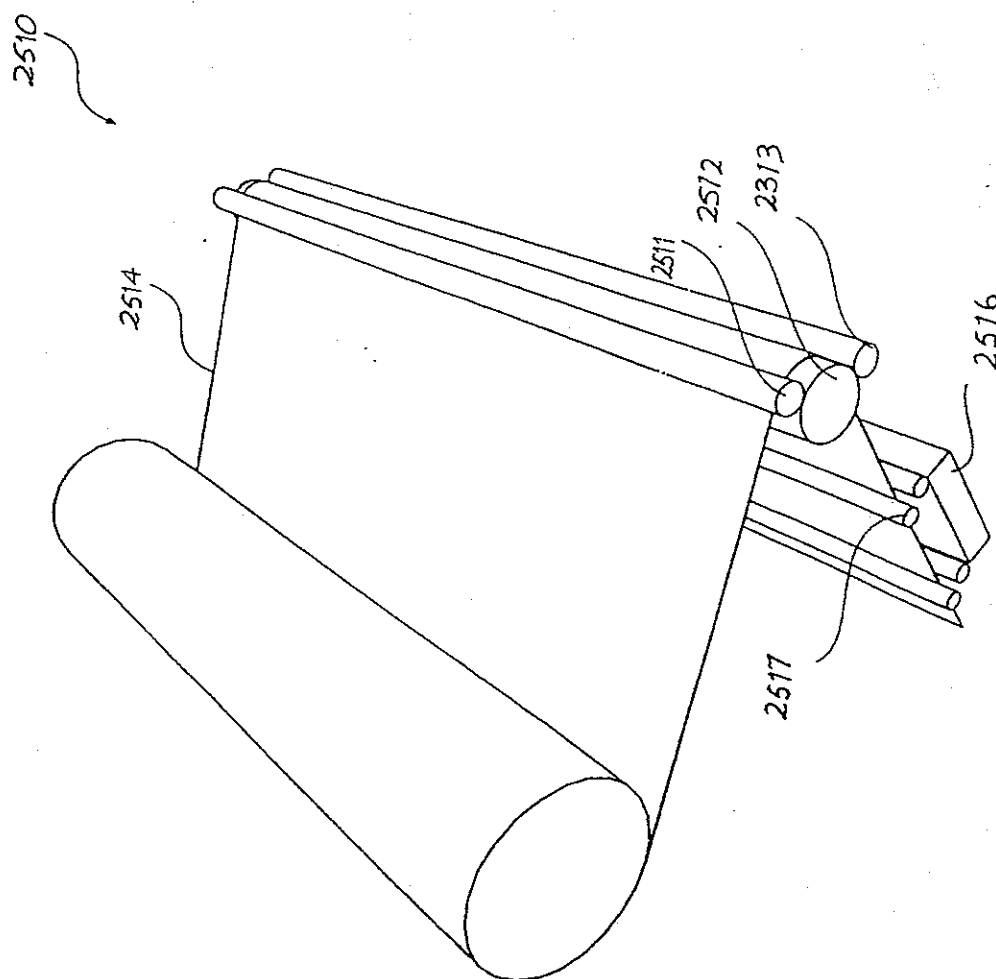


FIG. 250

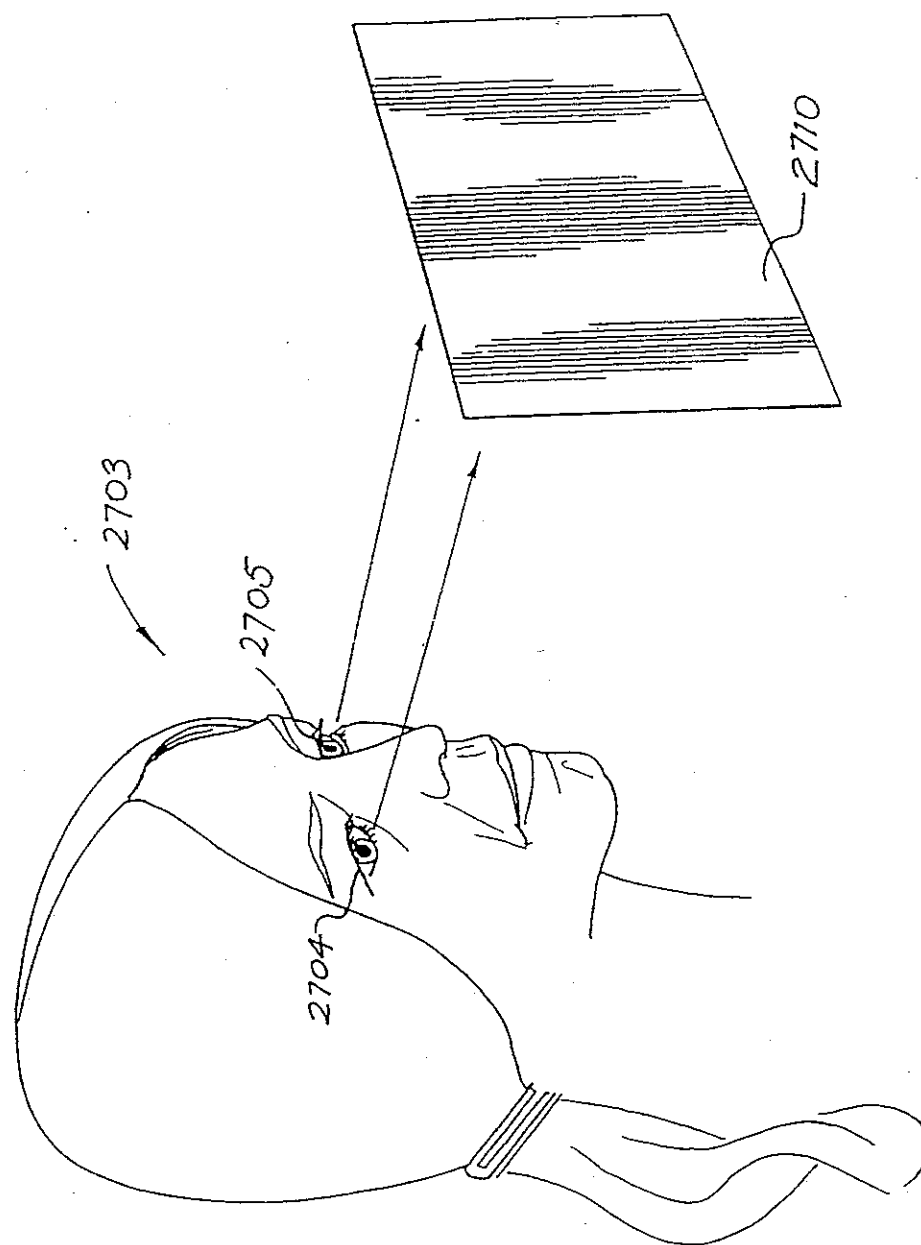


FIG. 251

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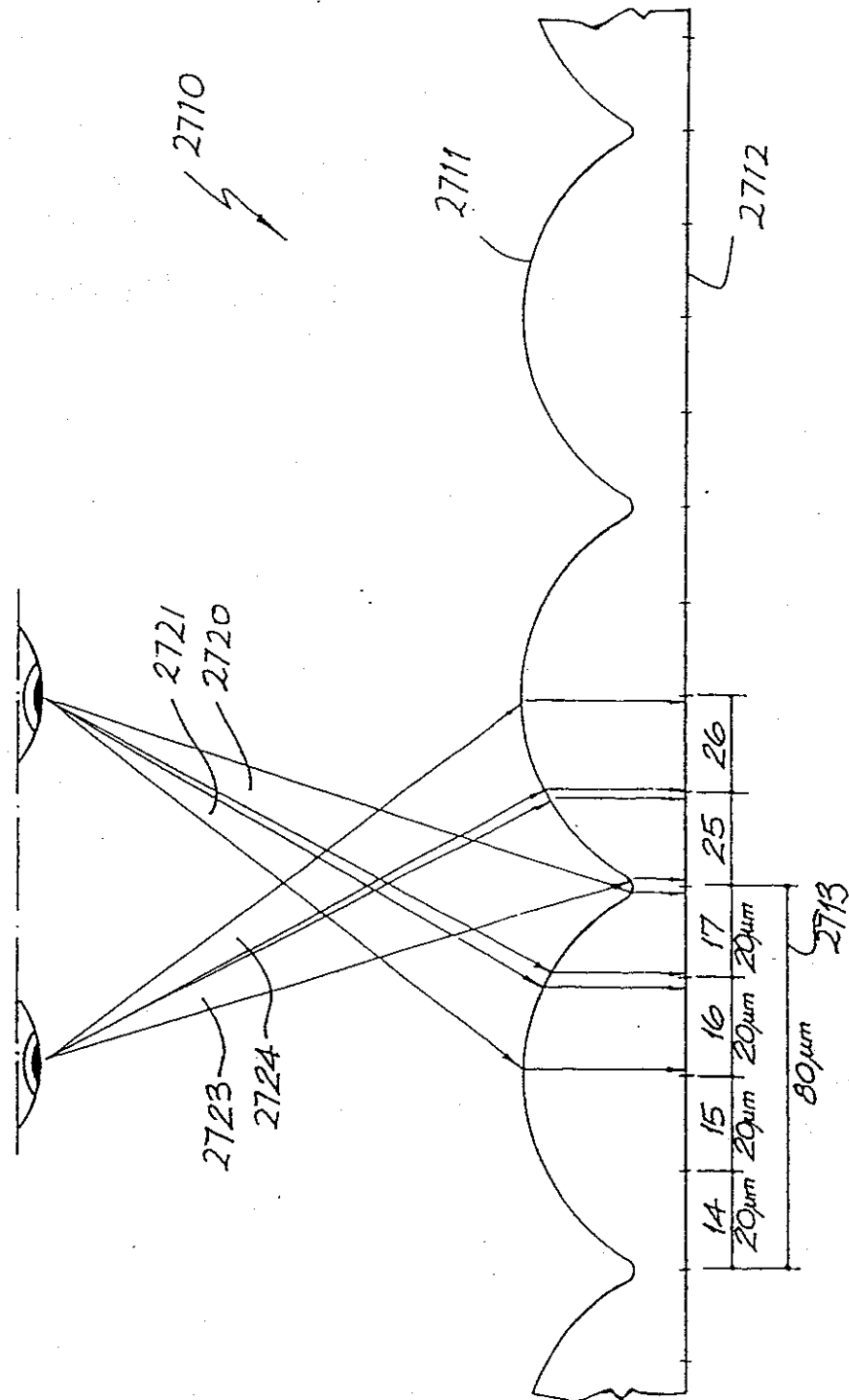
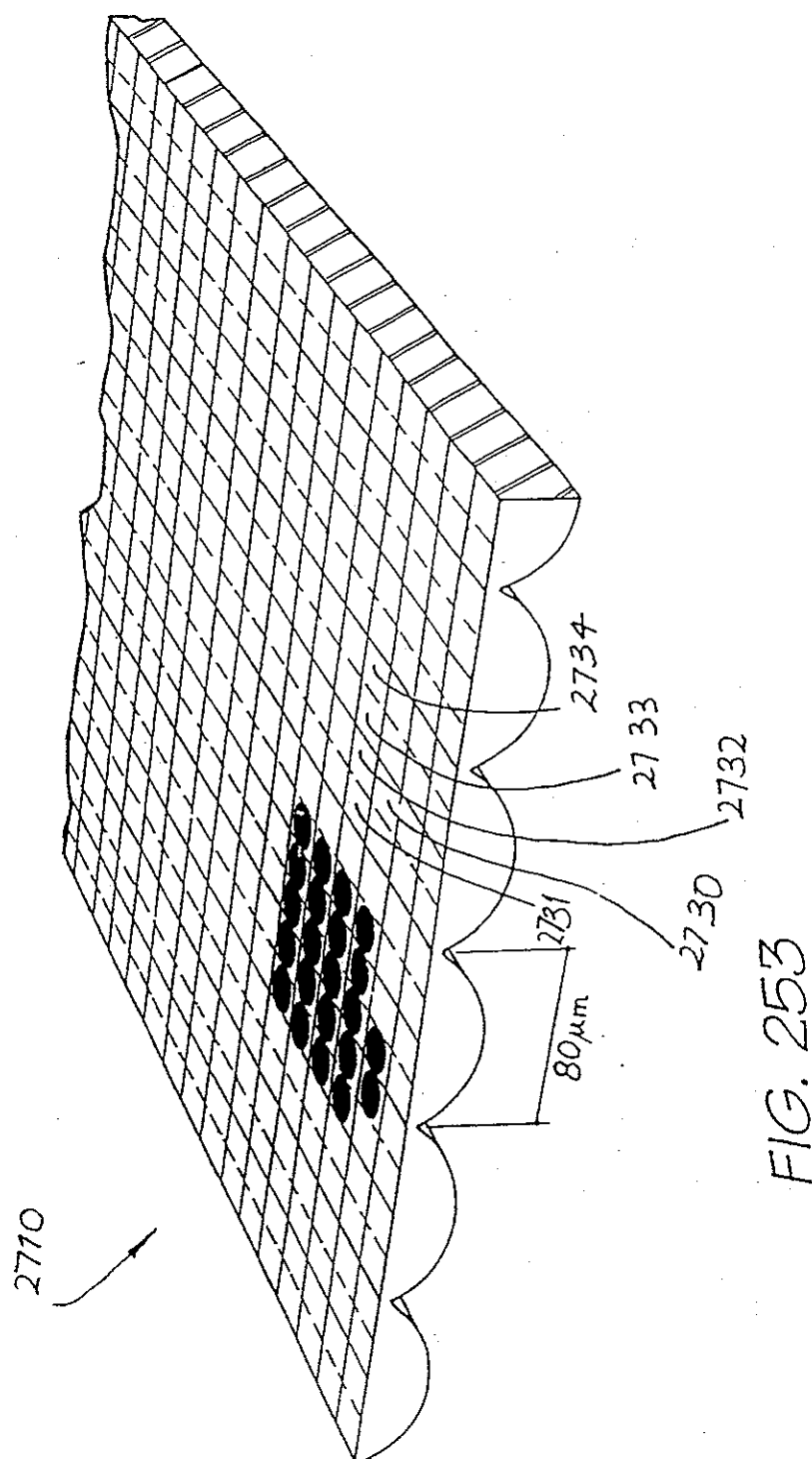


FIG. 252

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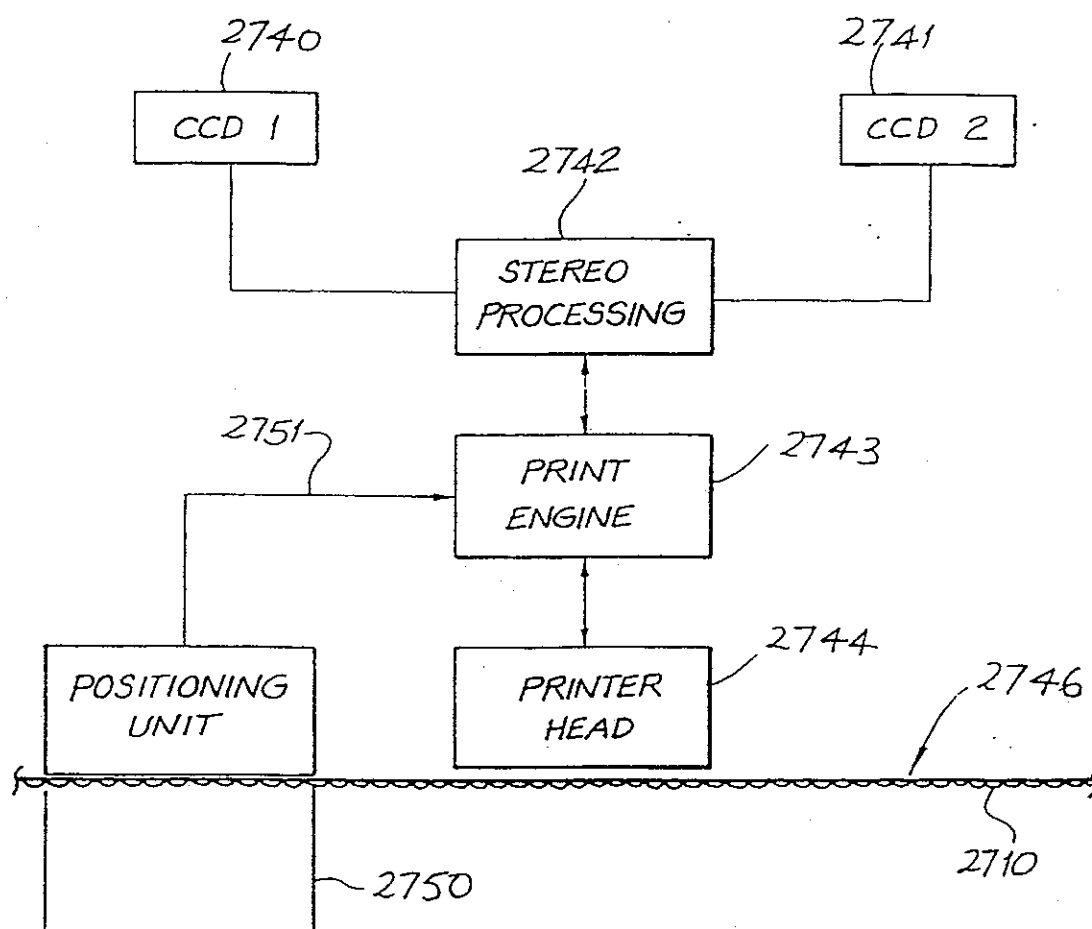


FIG. 254

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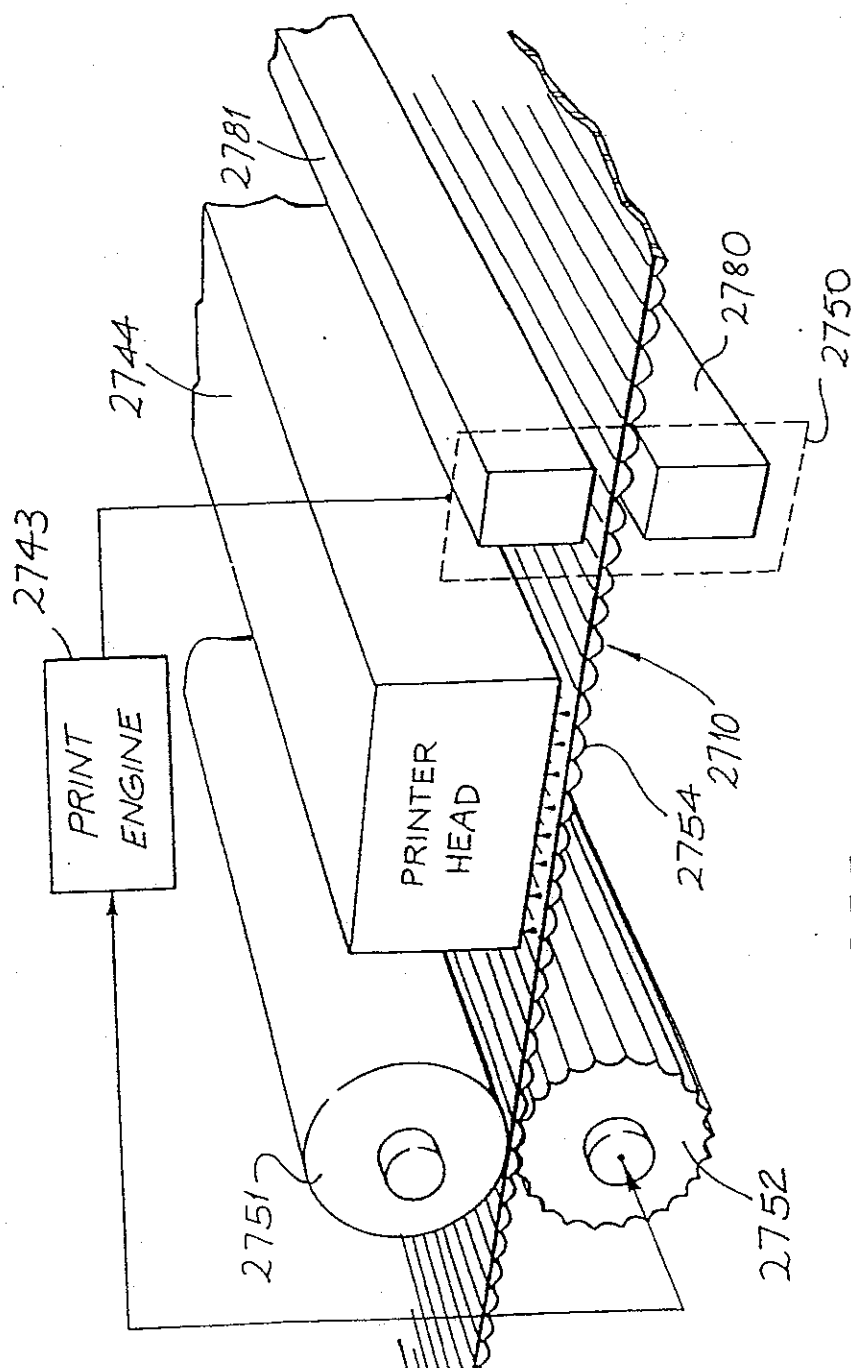


FIG. 255

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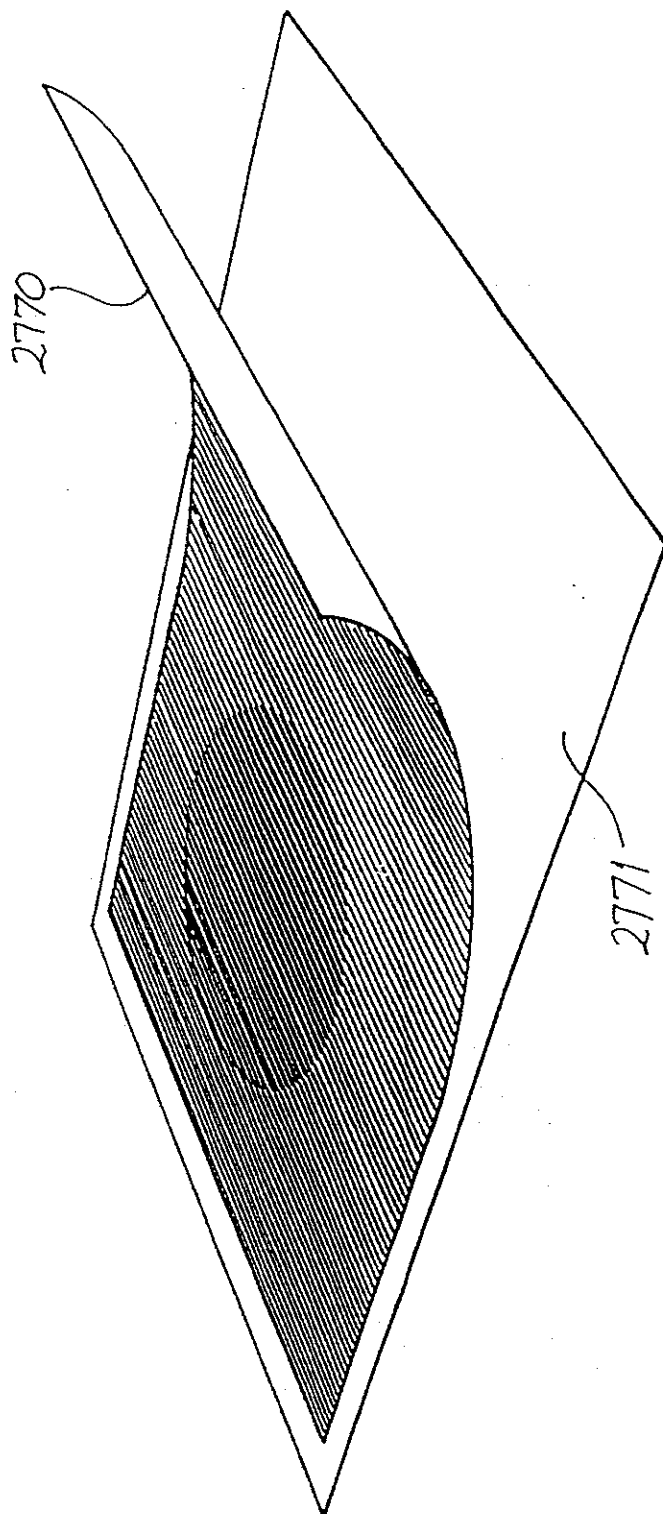


FIG. 257

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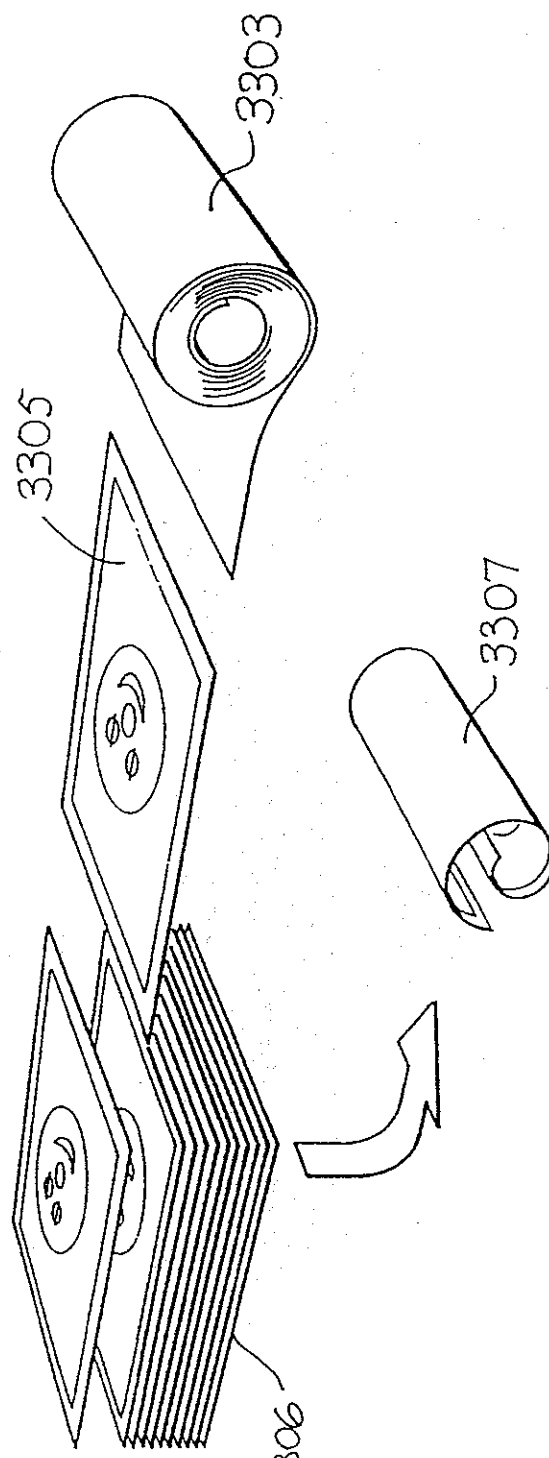


FIG. 258

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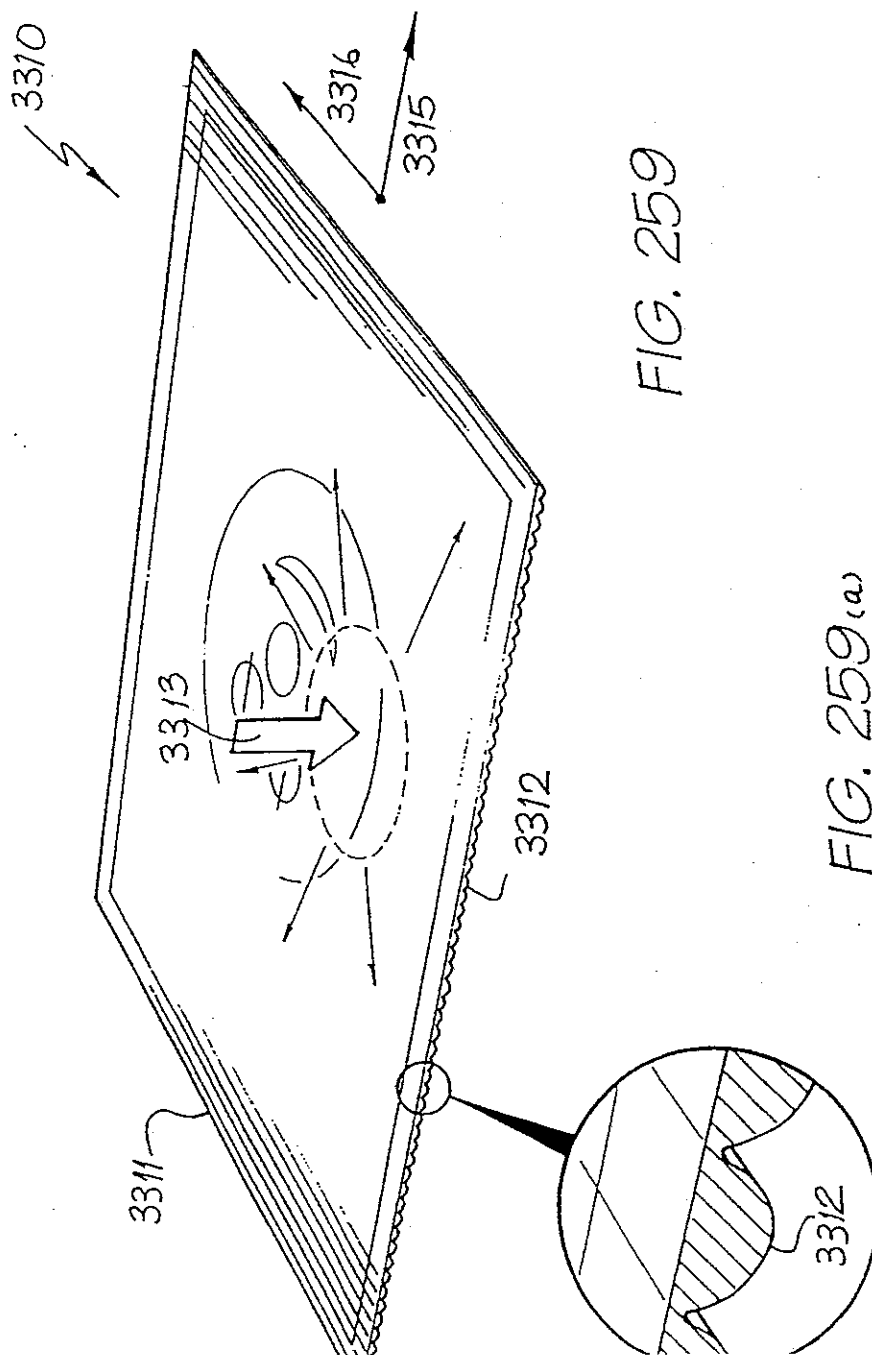


FIG. 259

FIG. 259(a)

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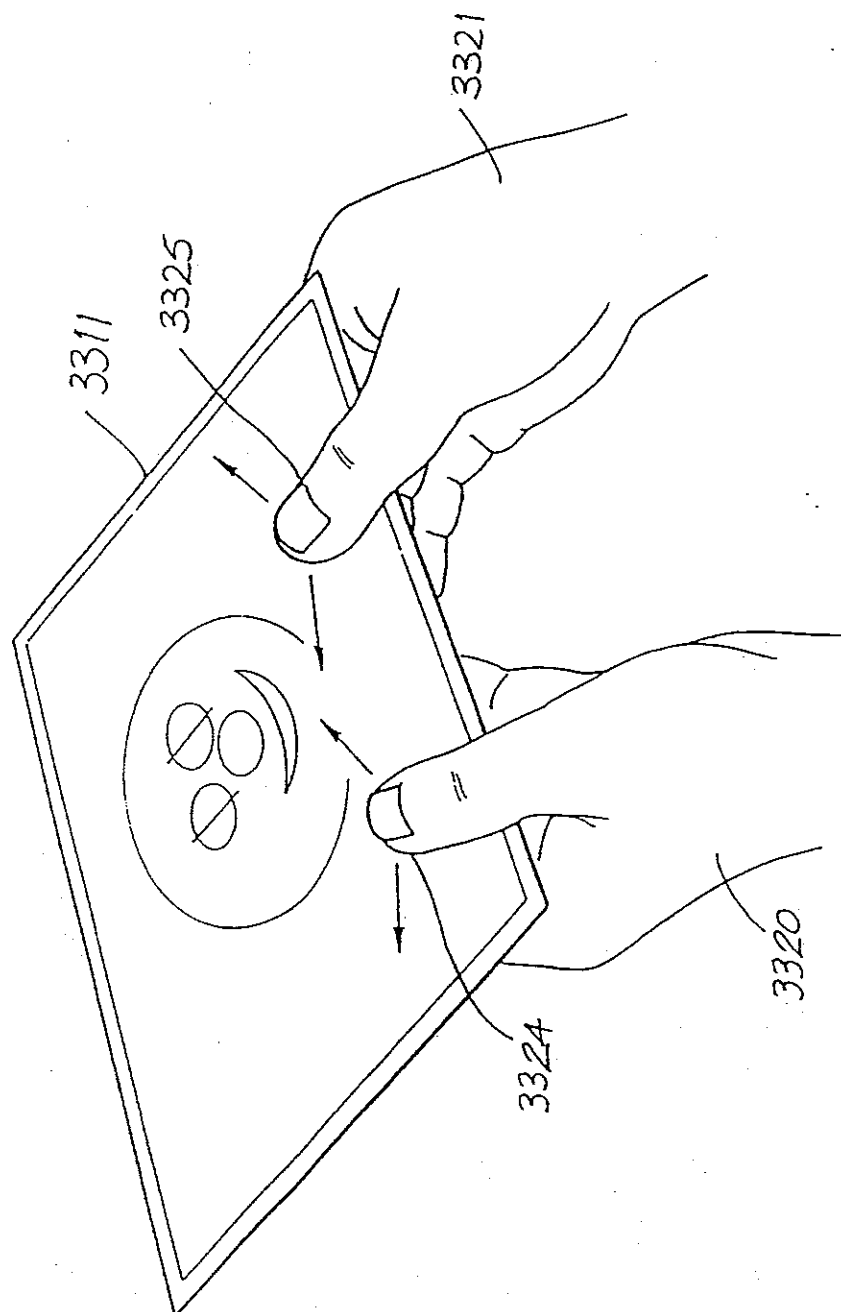
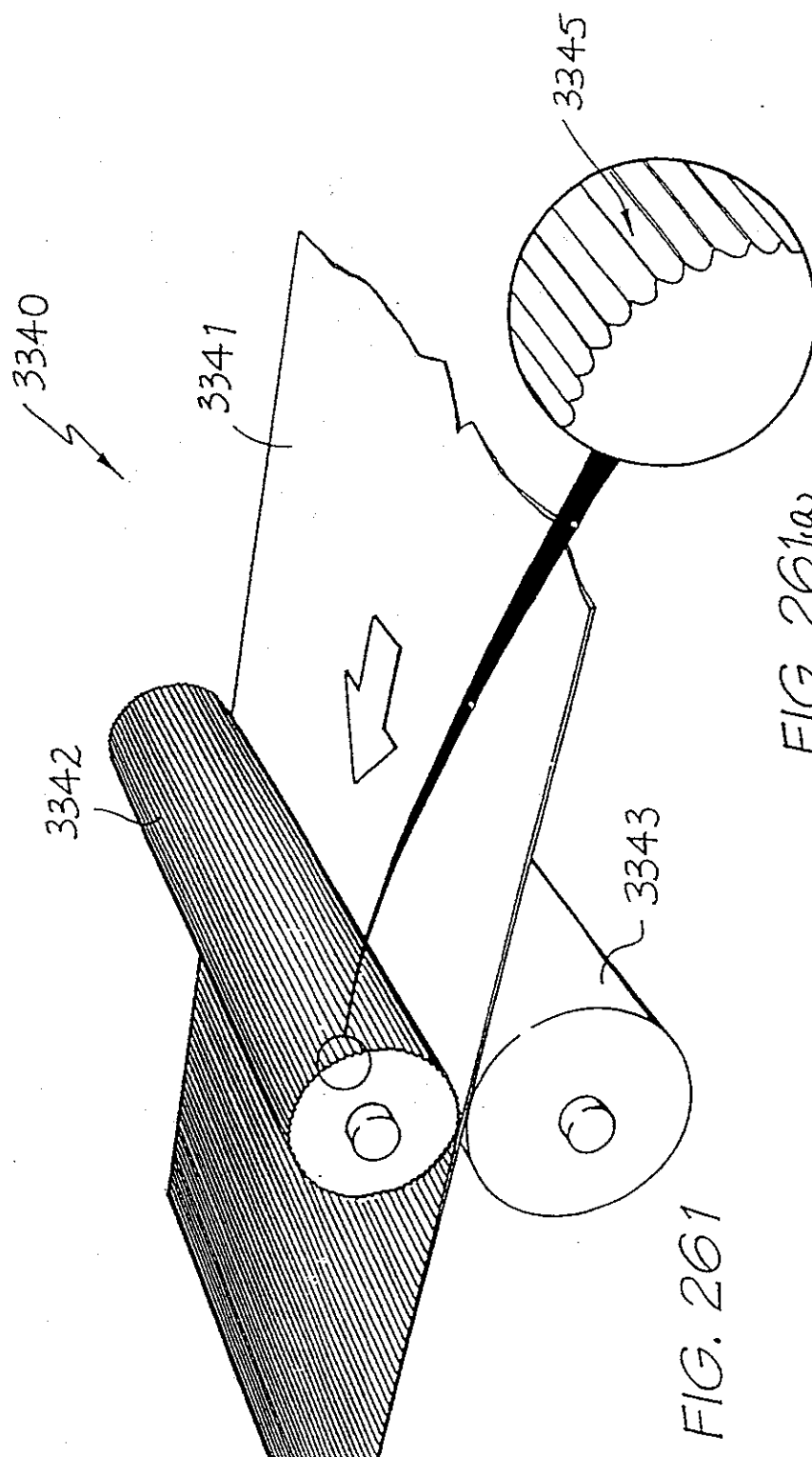


FIG. 260

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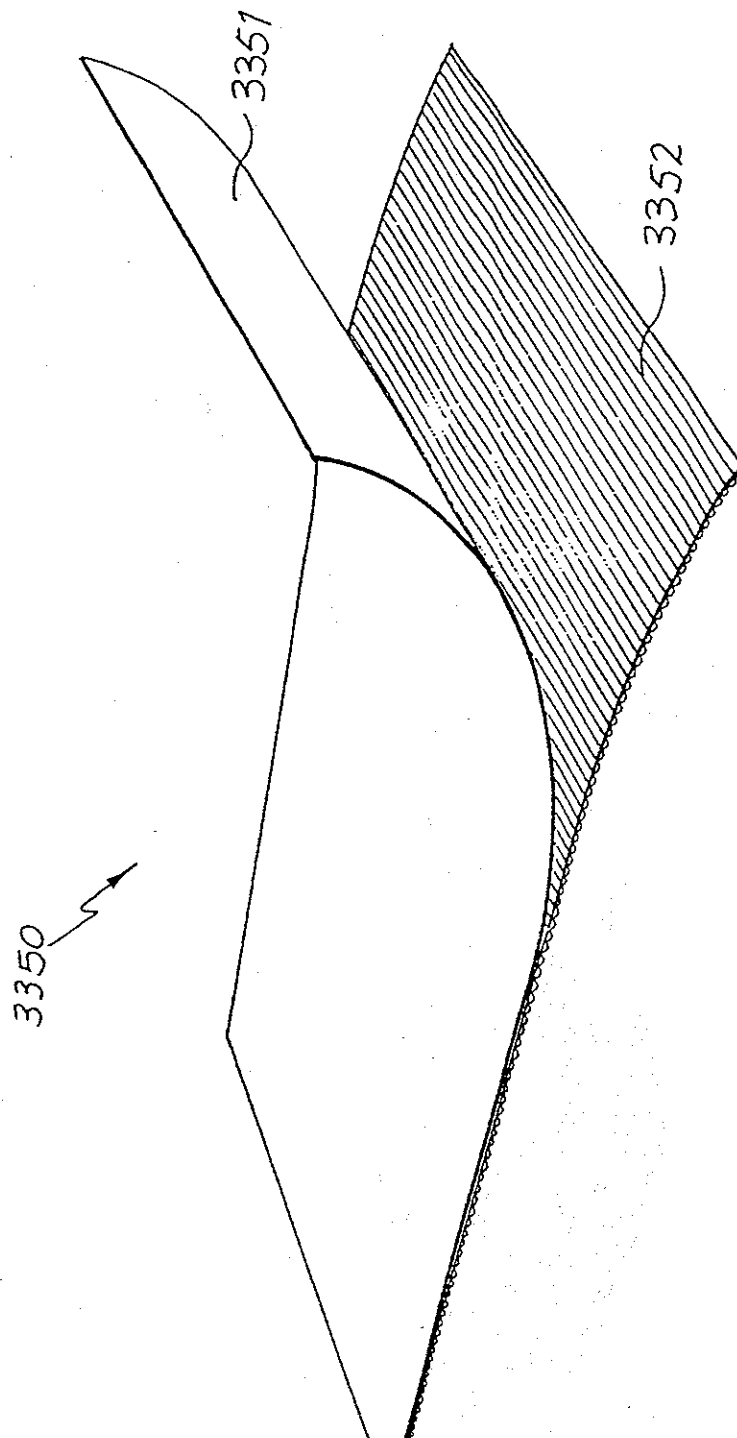
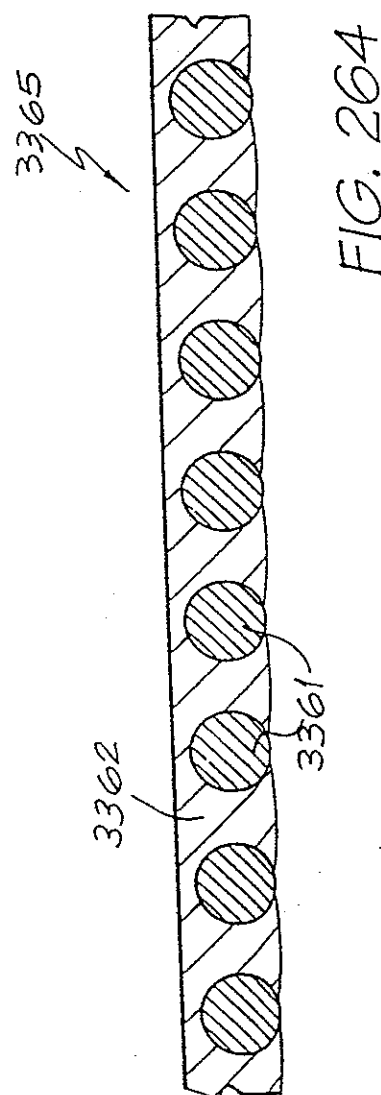
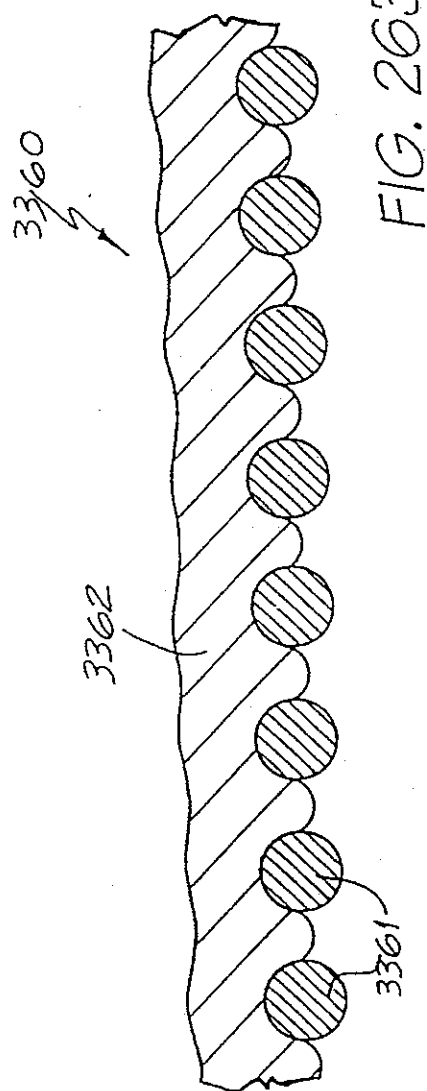


FIG. 262

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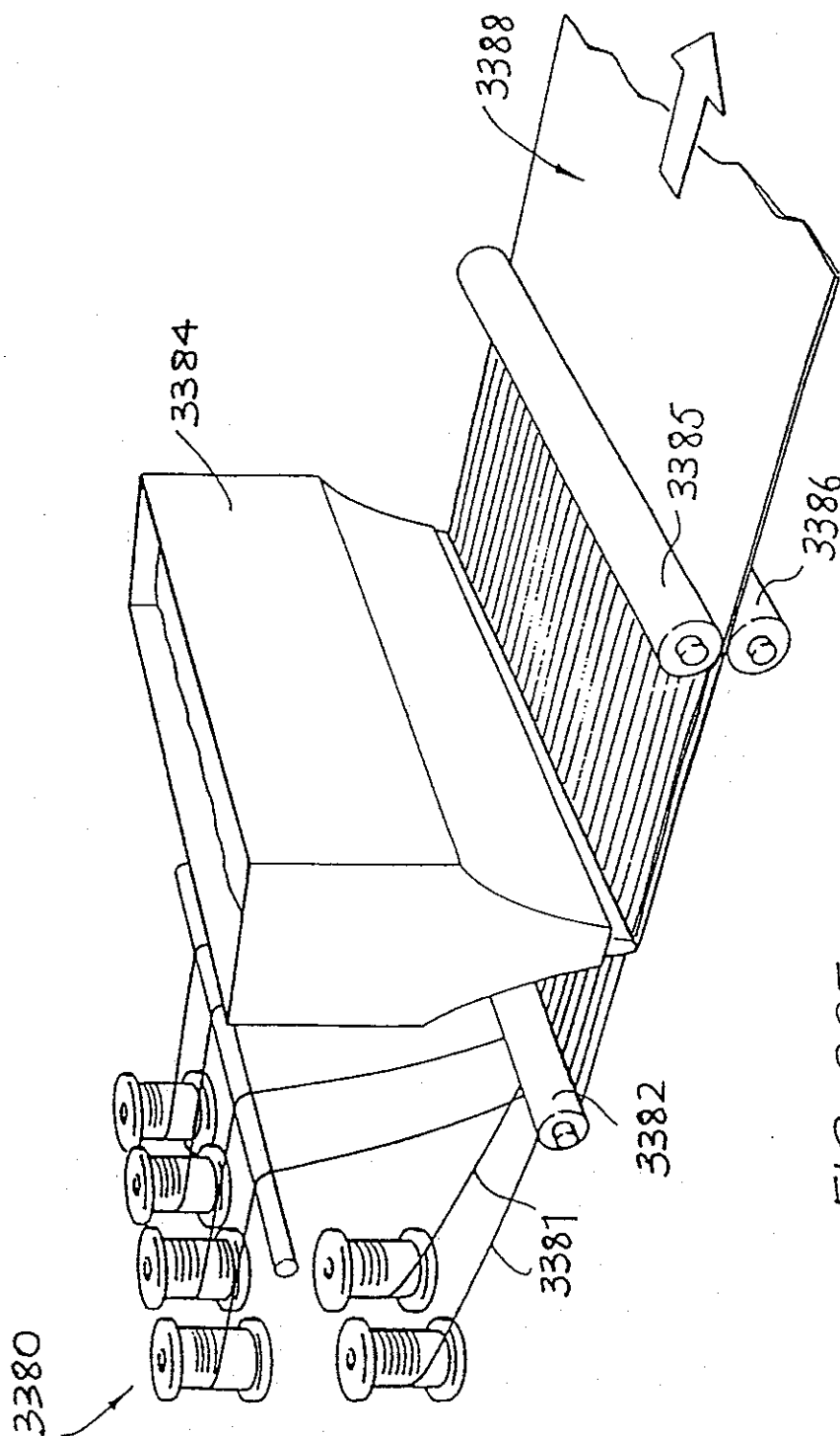


FIG. 265

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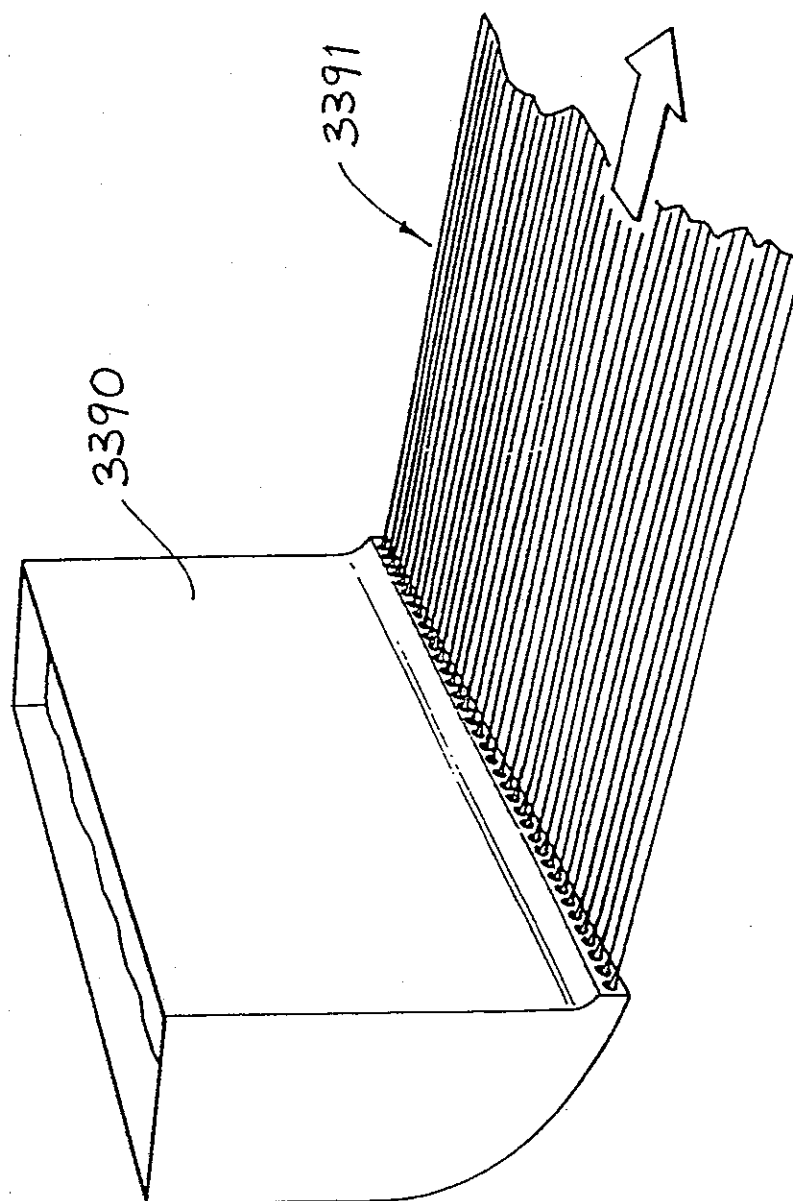
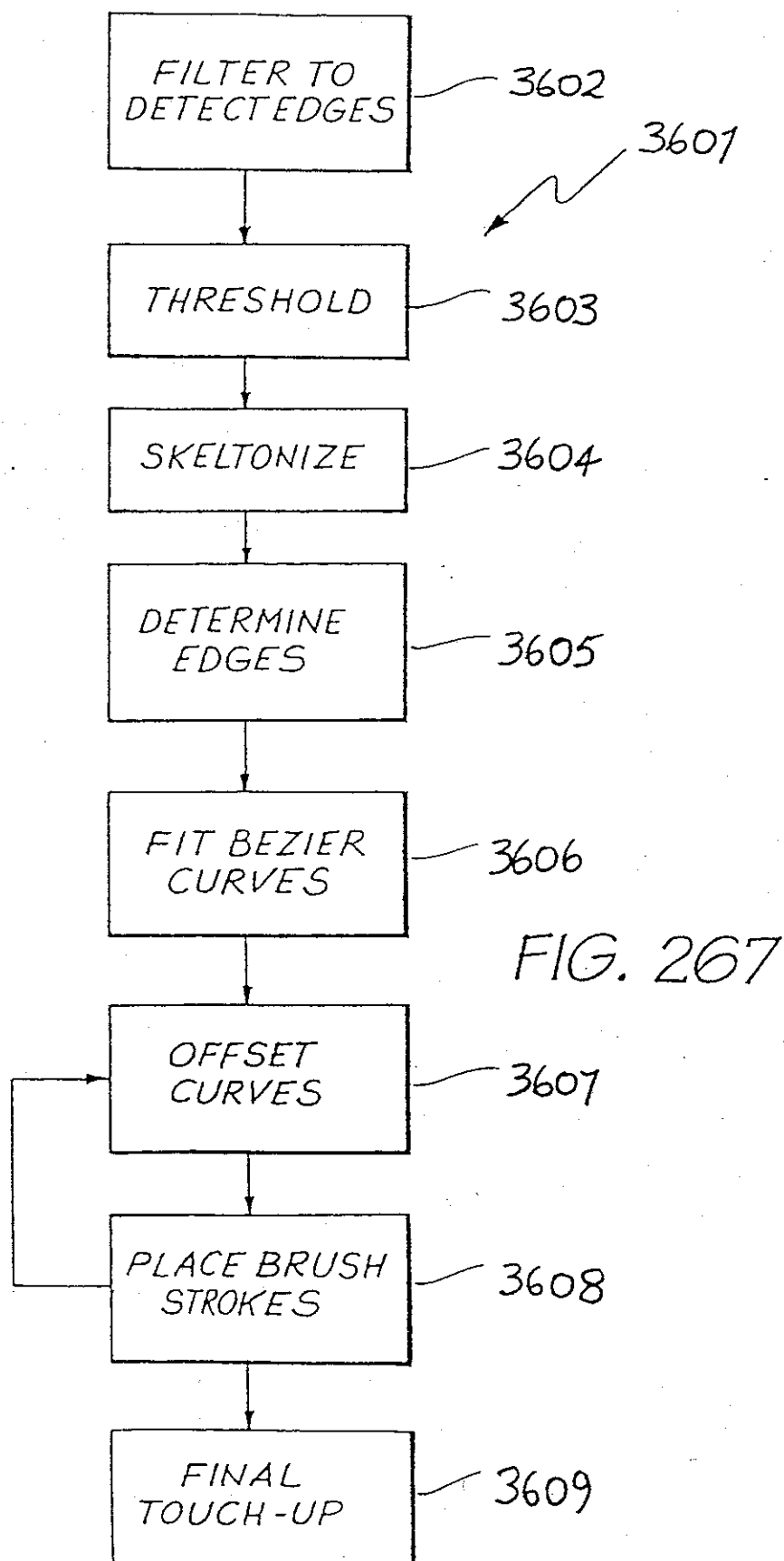


FIG. 260

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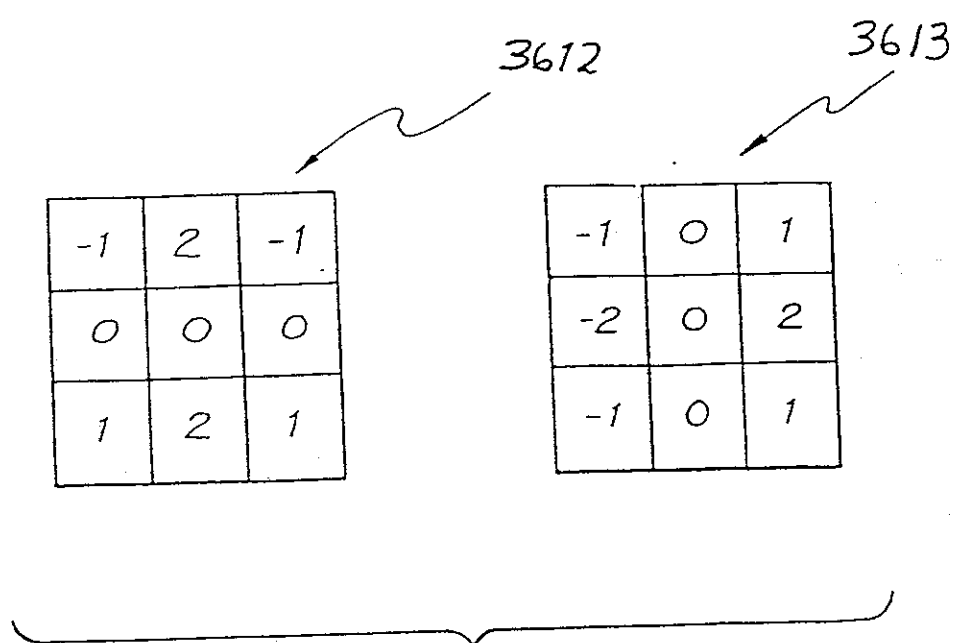


FIG. 268

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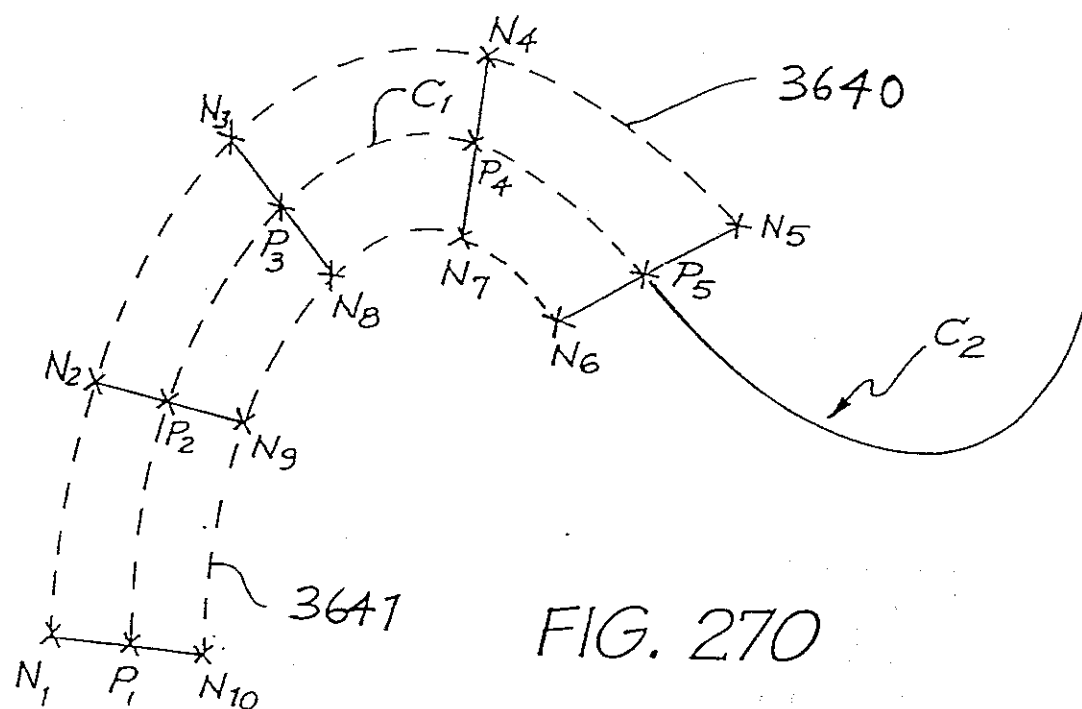


FIG. 270

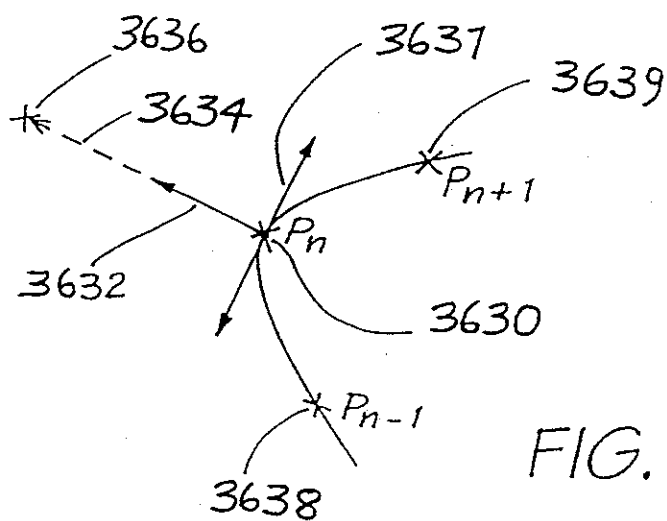


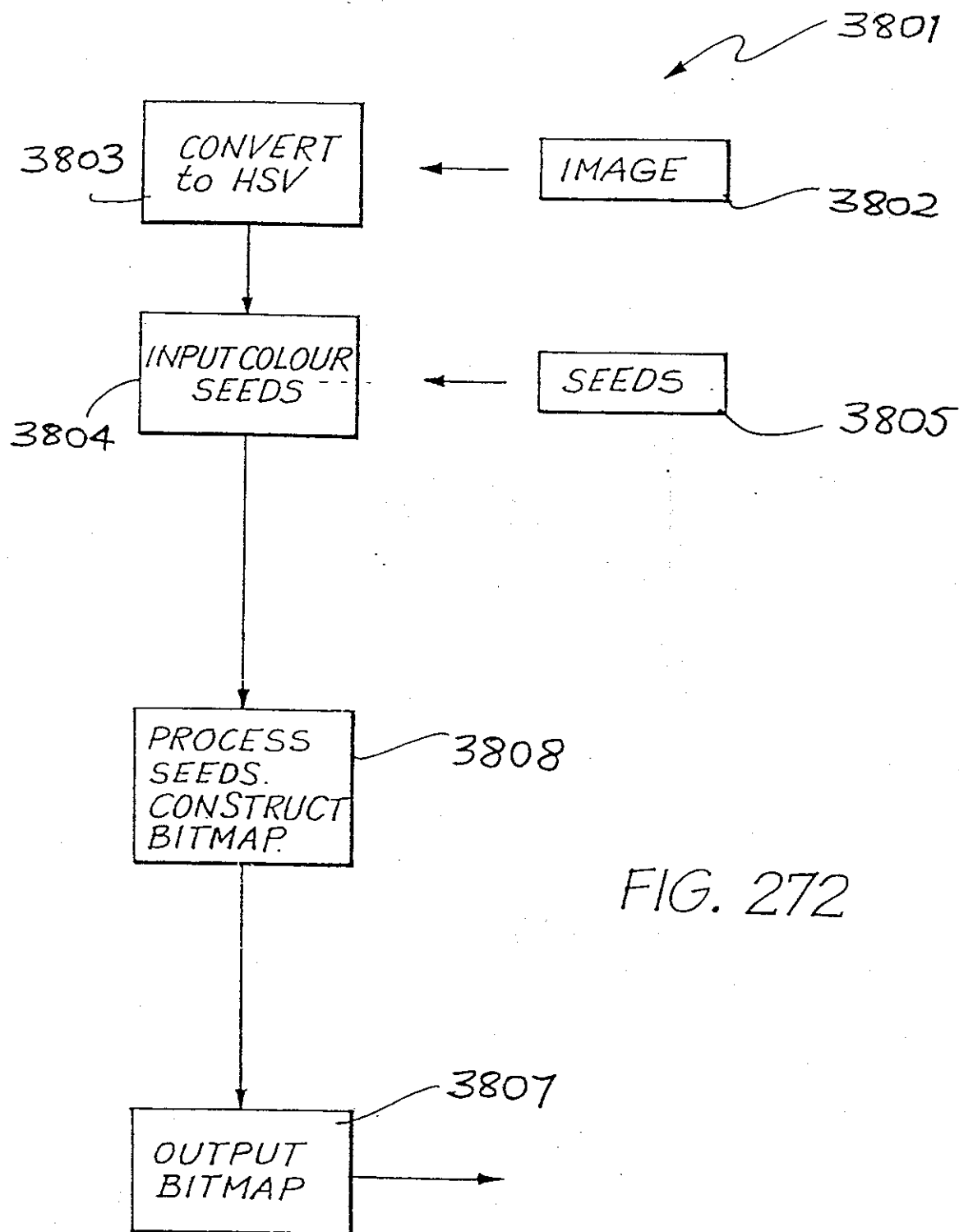
FIG. 269

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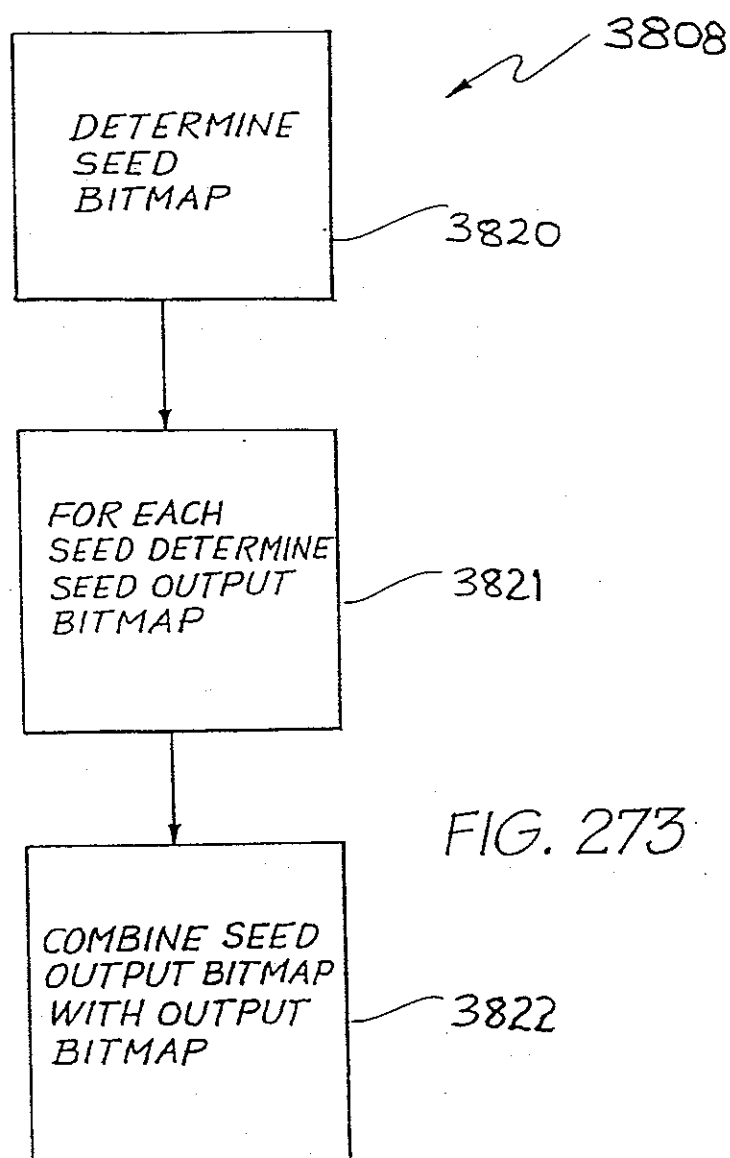


FIG. 271

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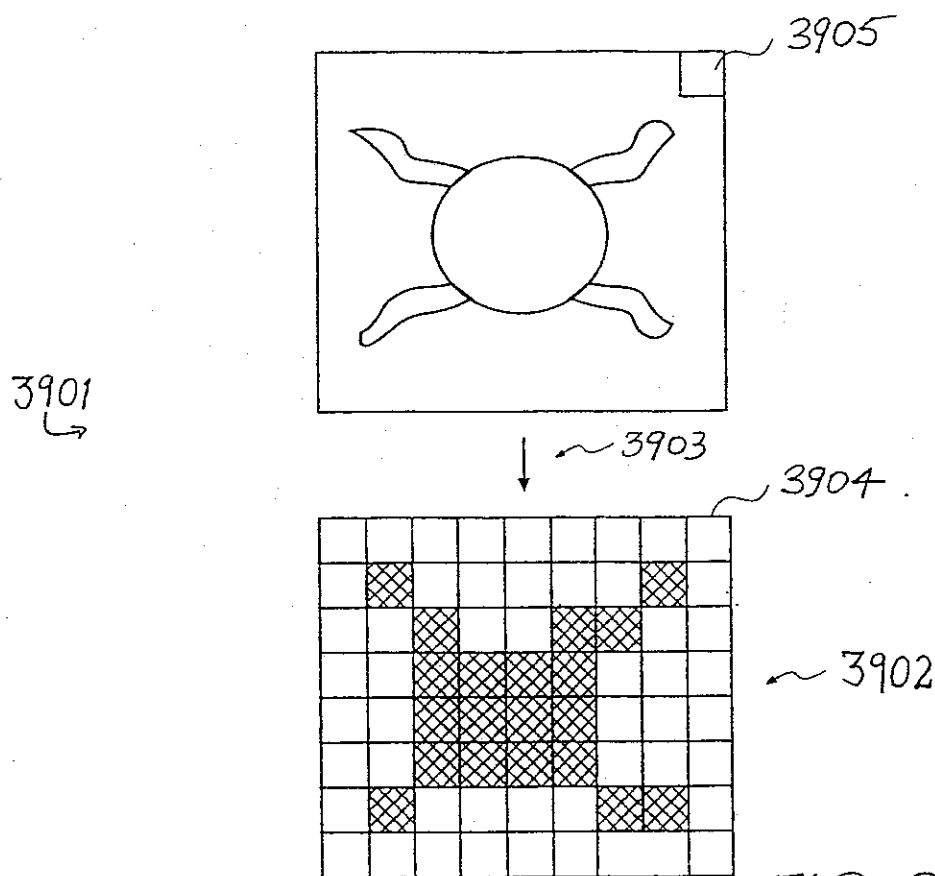


FIG. 274

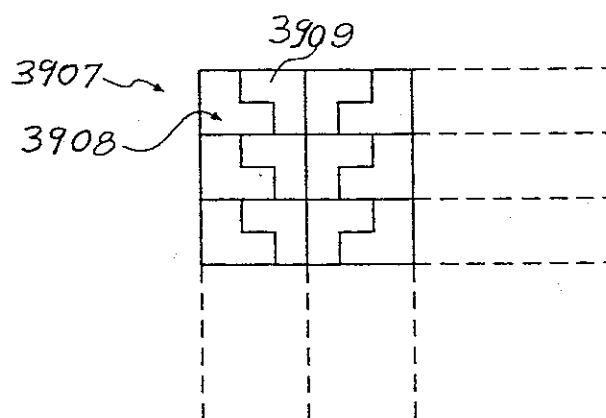
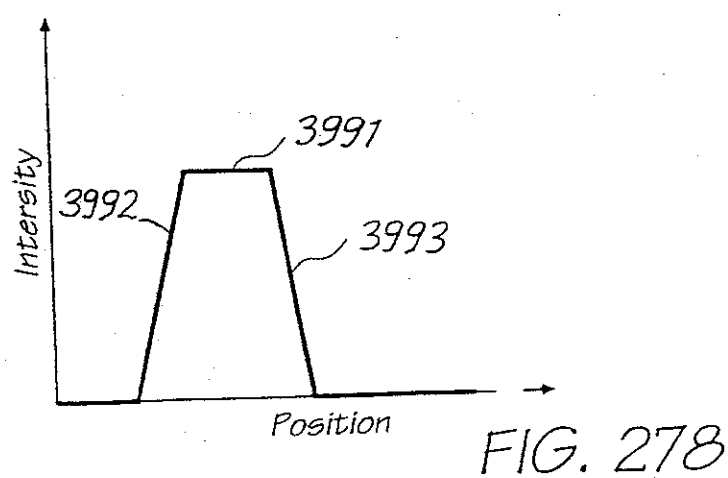
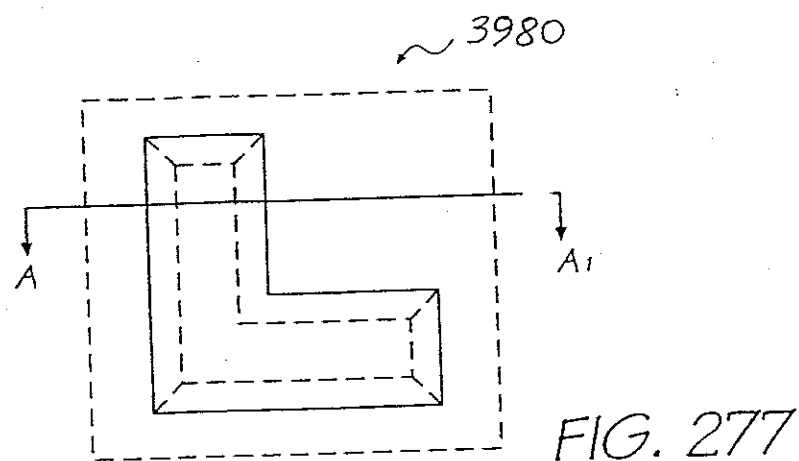
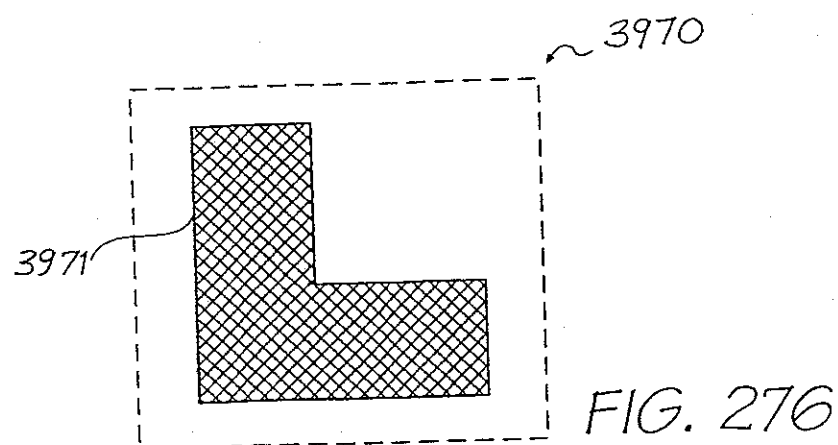


FIG. 275

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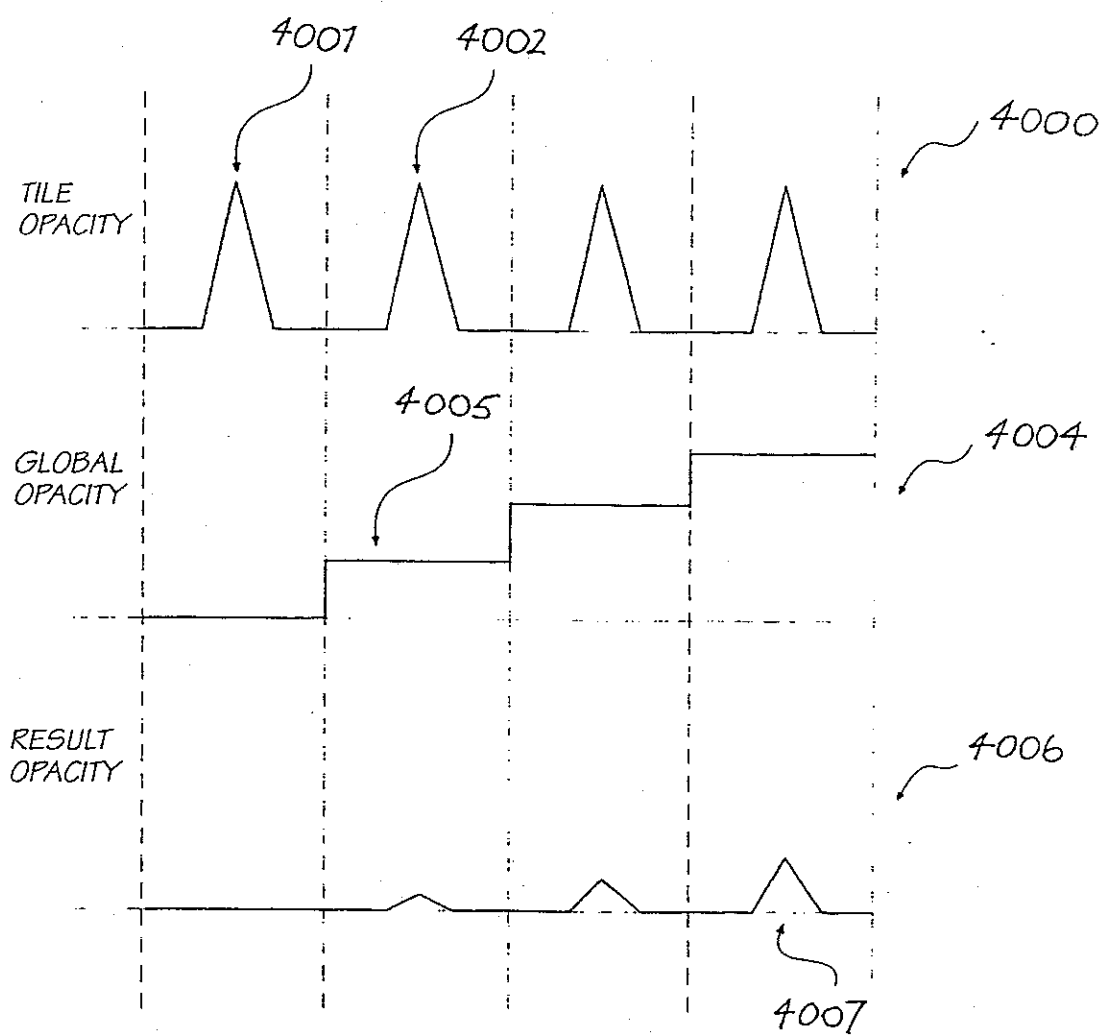
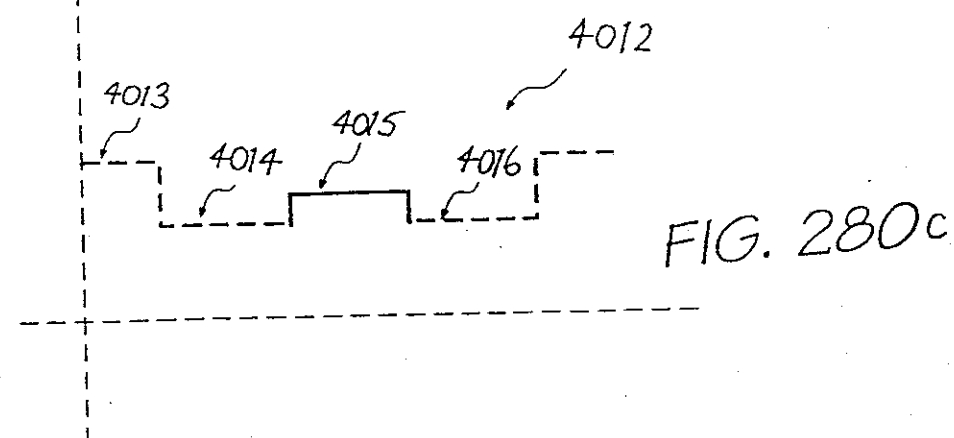
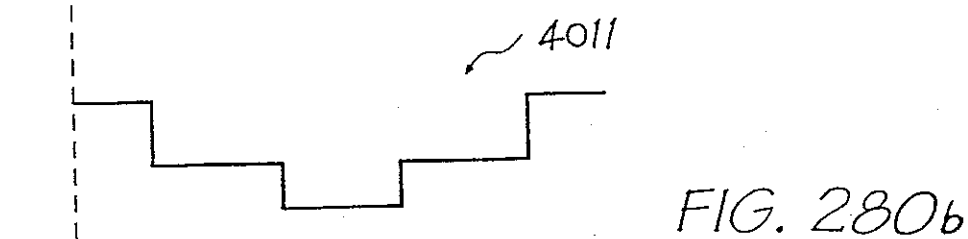
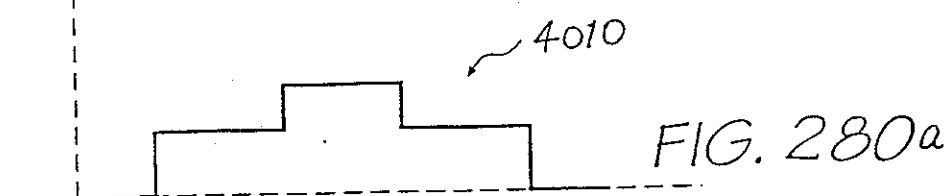


FIG. 279

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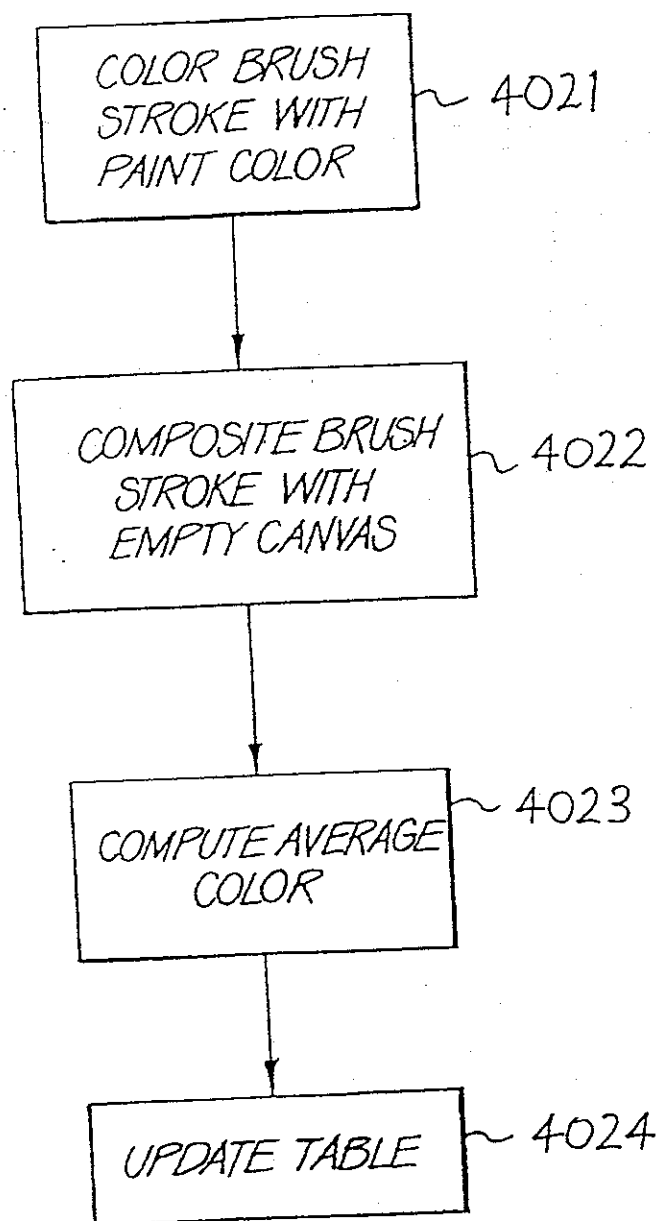


FIG. 282

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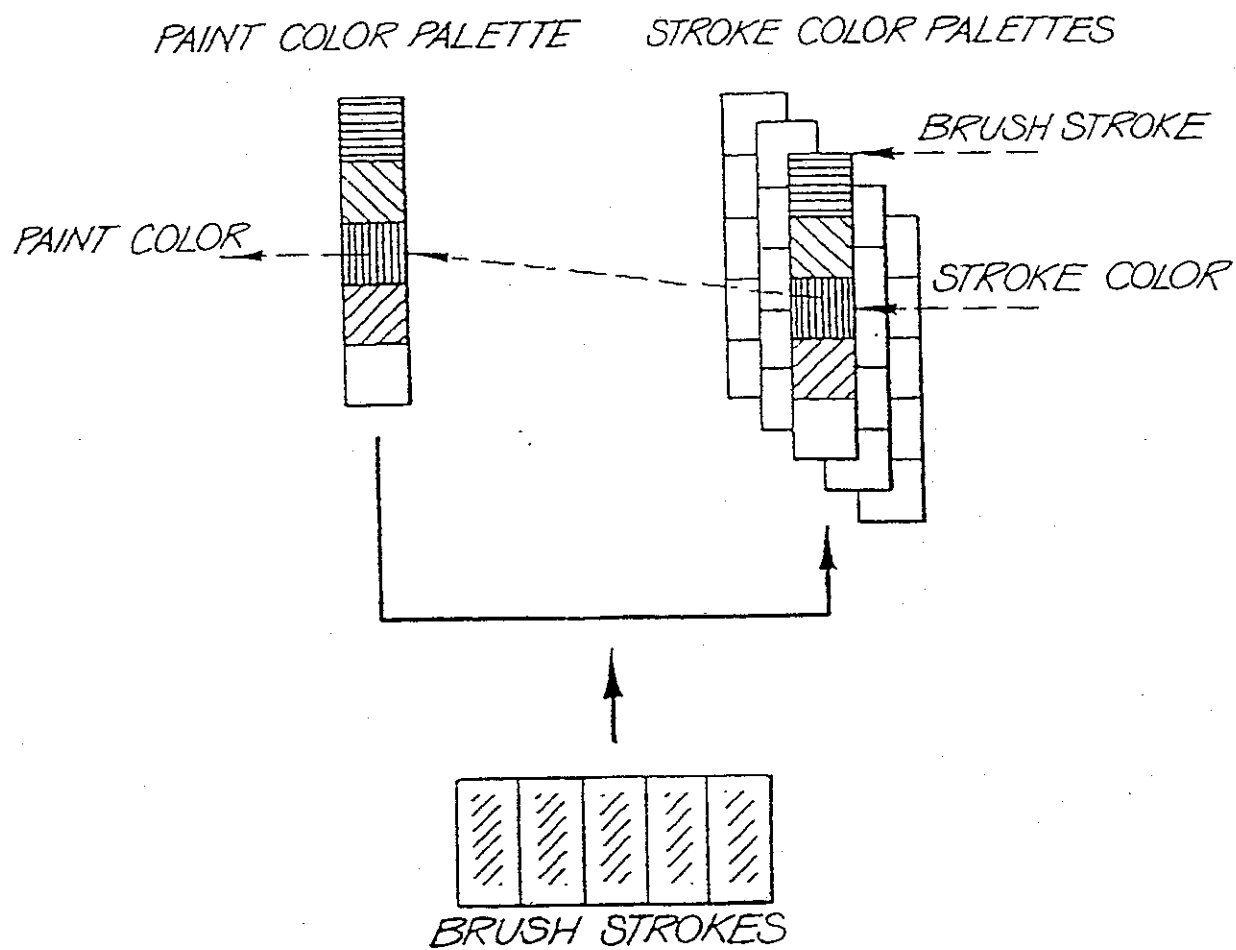


FIG. 283

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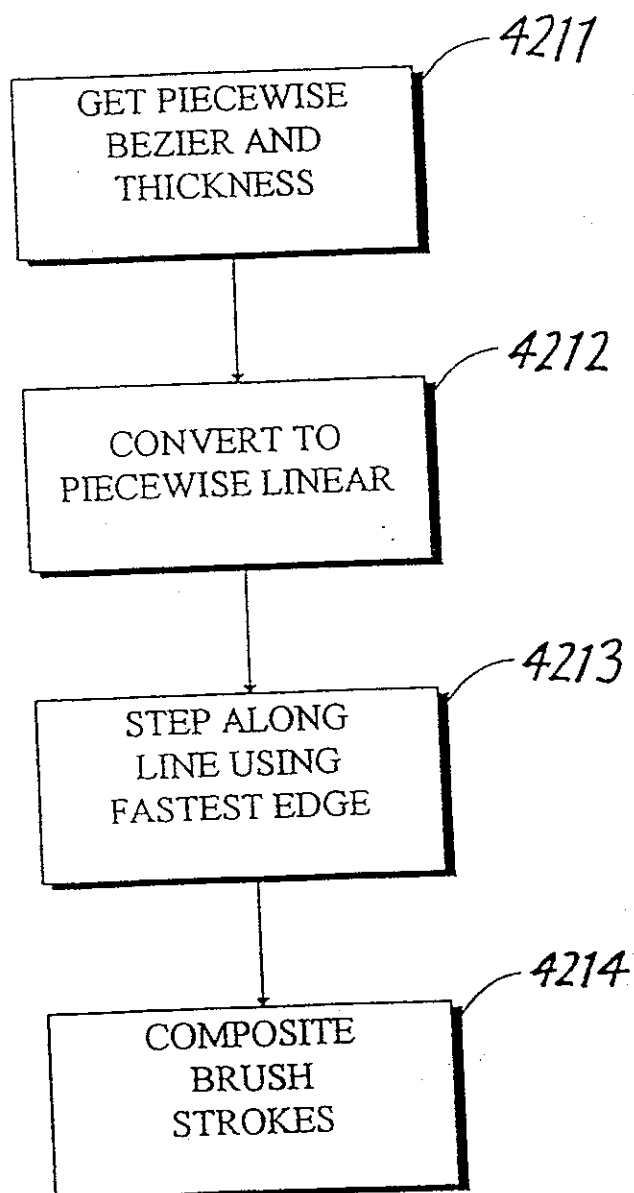


FIG. 284

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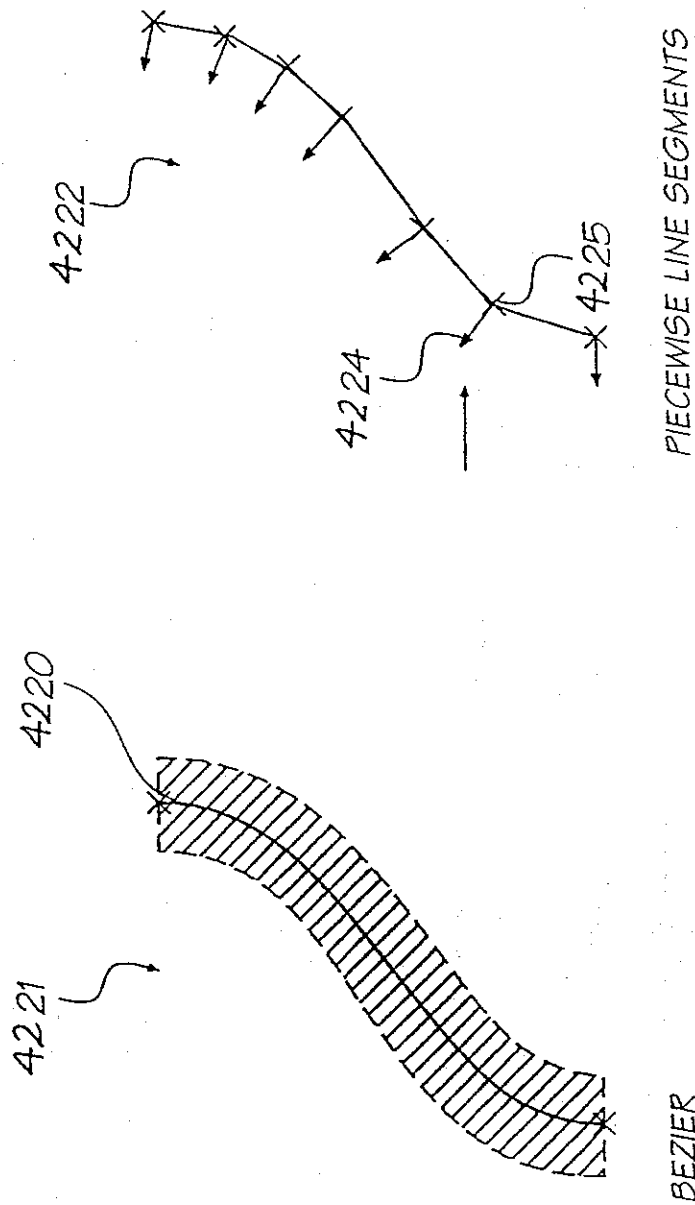


FIG. 285

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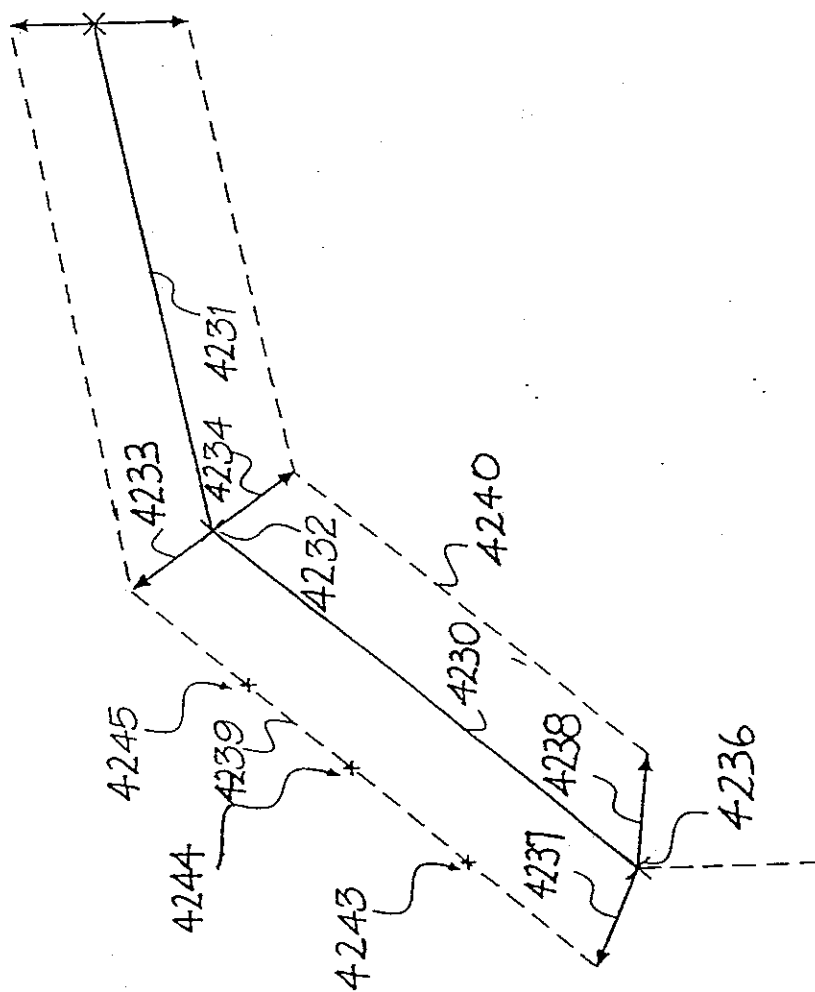


FIG. 286

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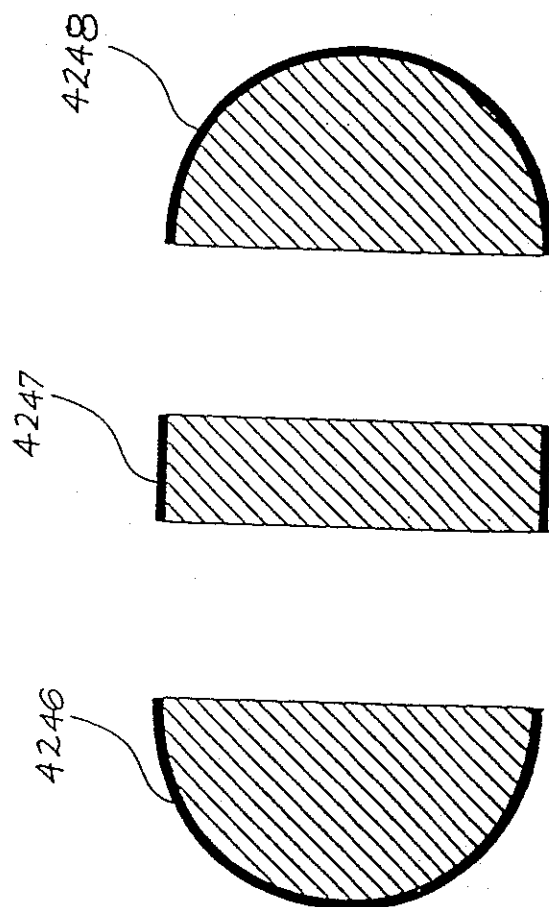


FIG. 287

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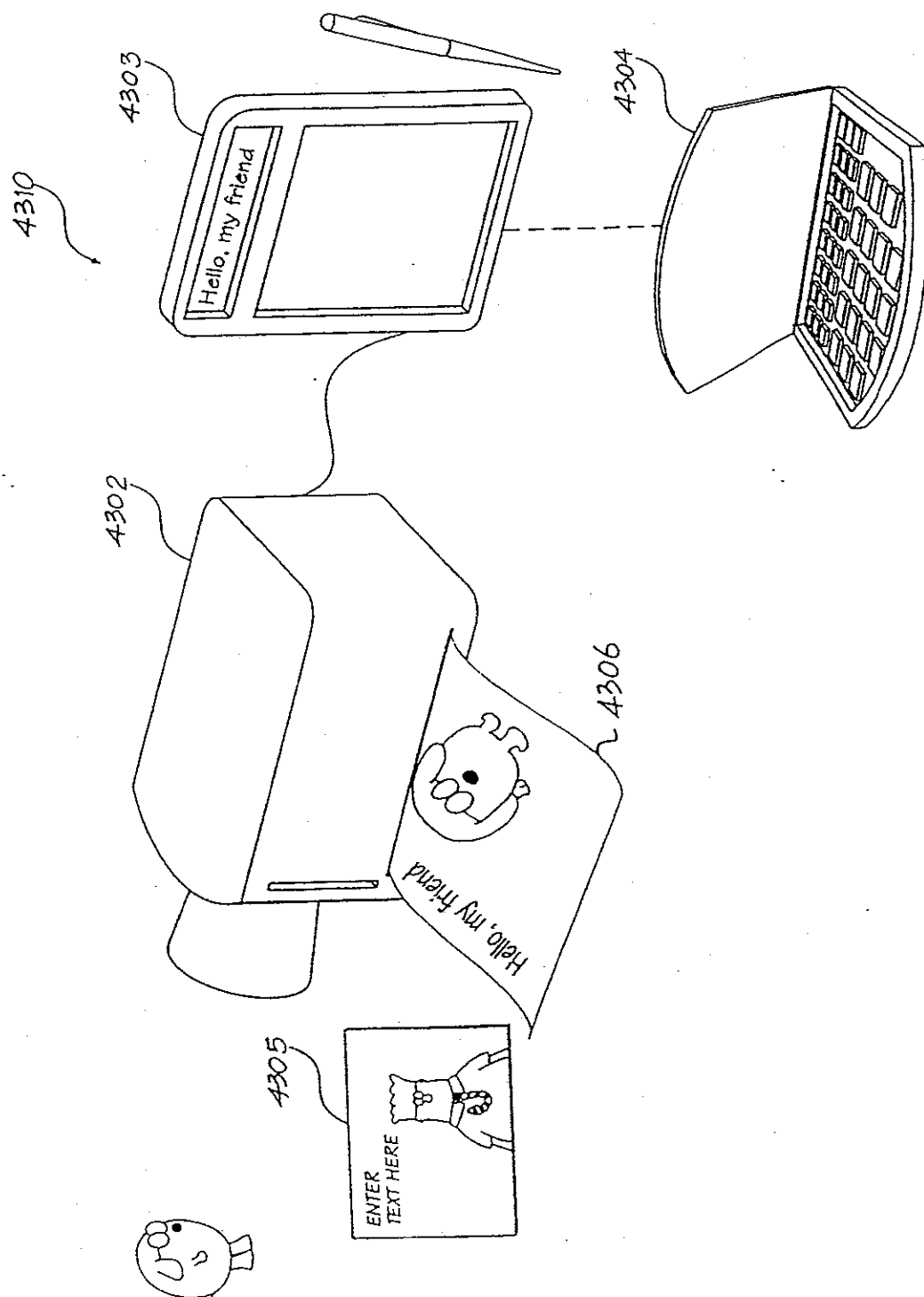


FIG. 288

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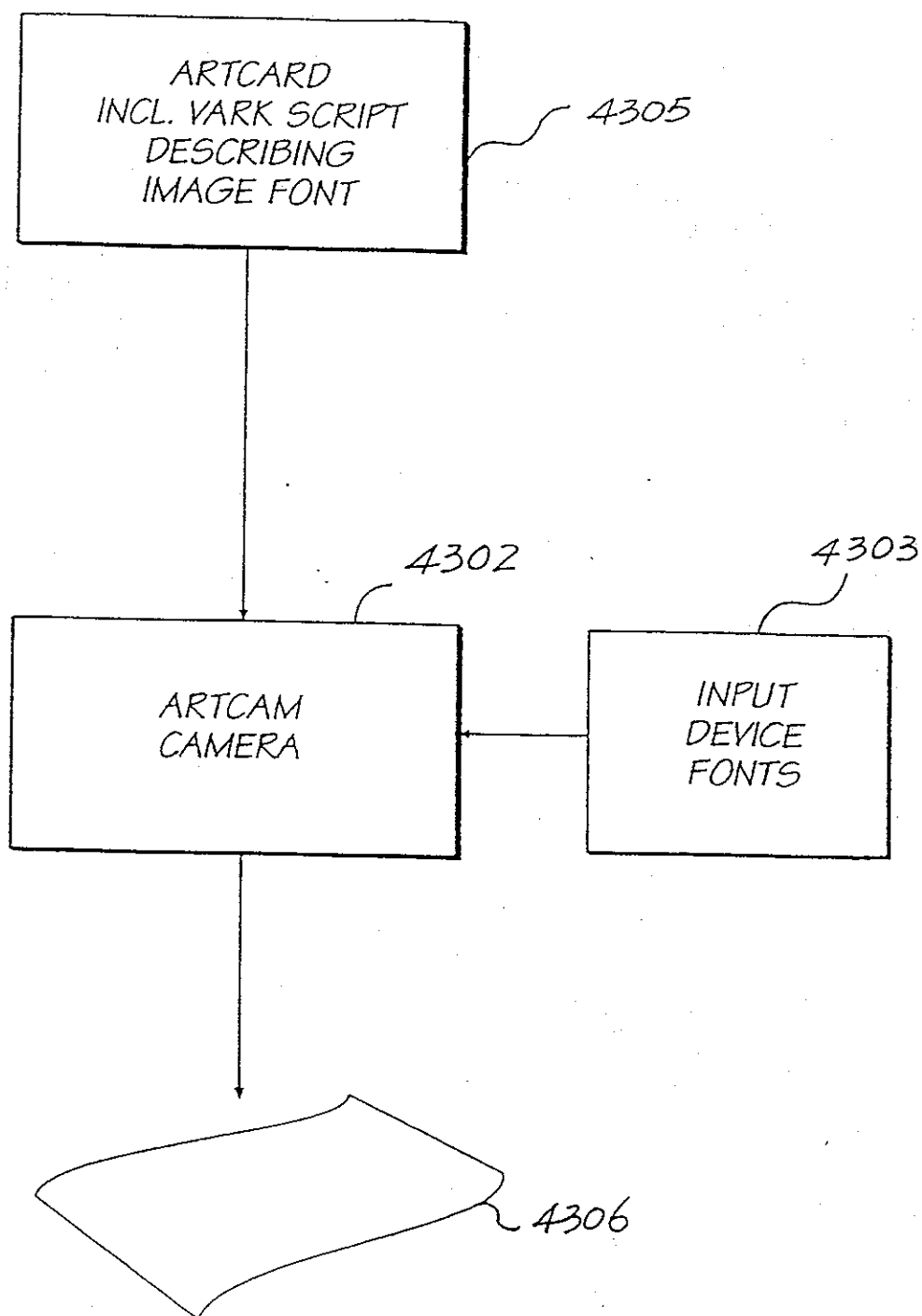


FIG. 289

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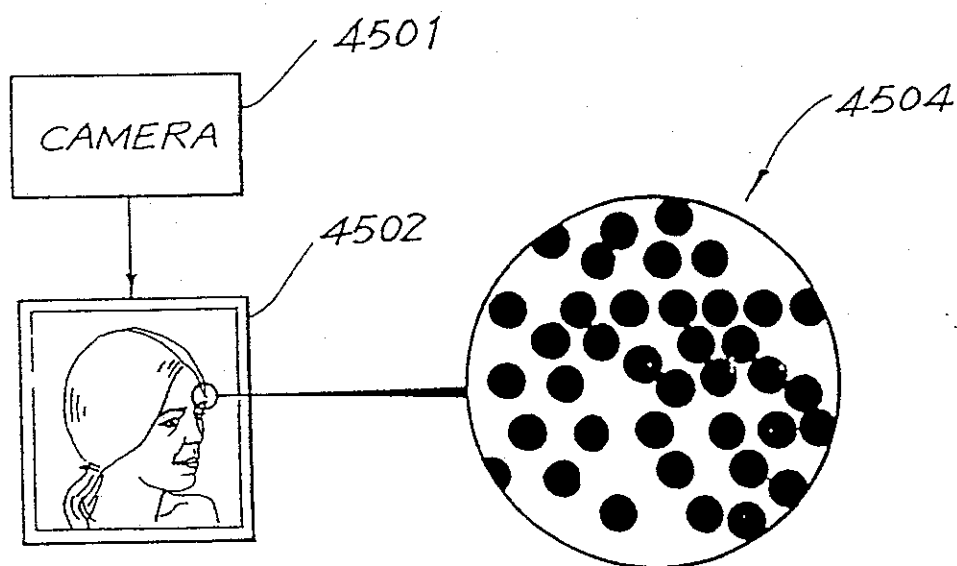


FIG. 290

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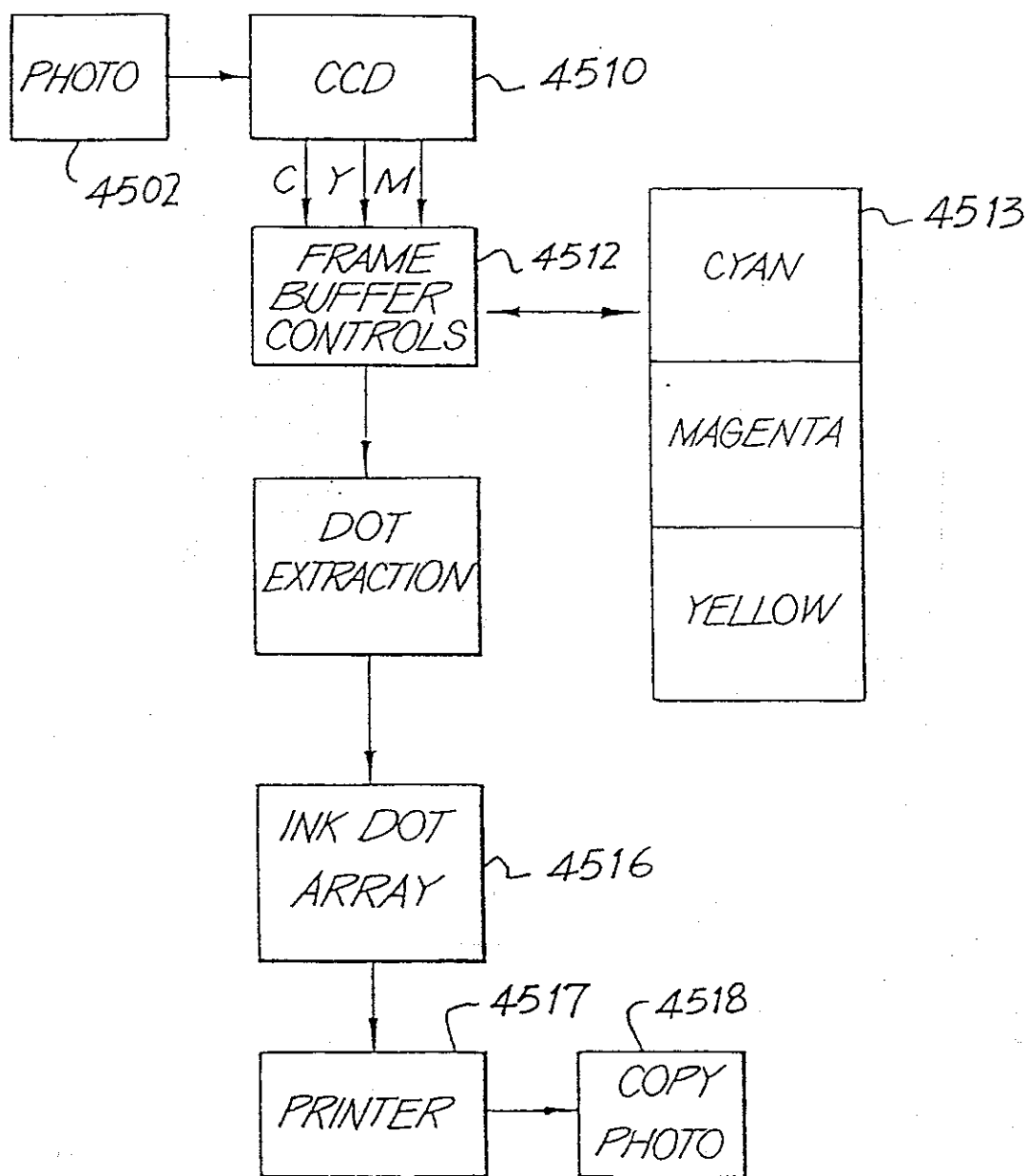


FIG. 291

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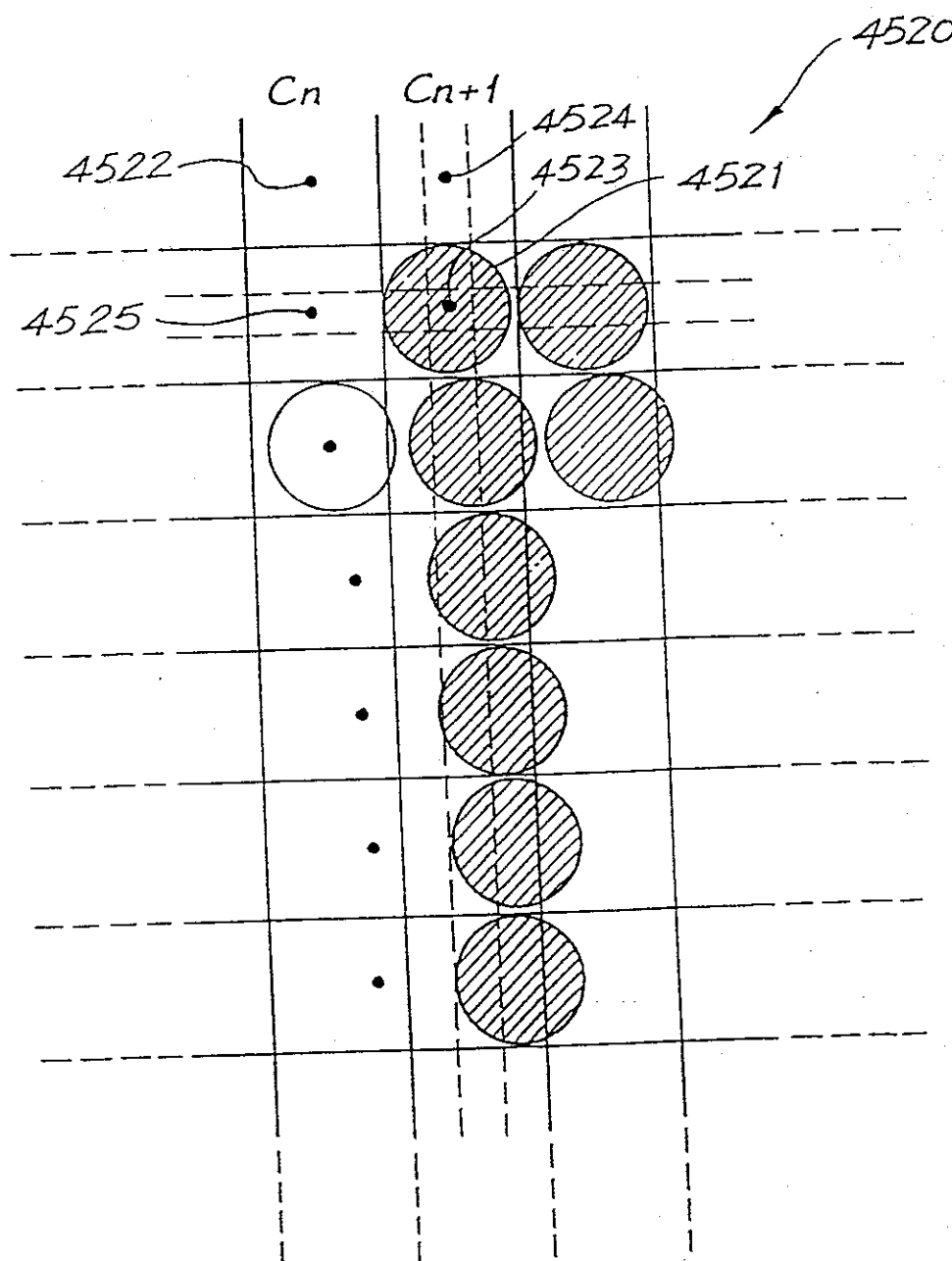


FIG. 292

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INK DOT ARRANGEMENTS

CCD VALUES

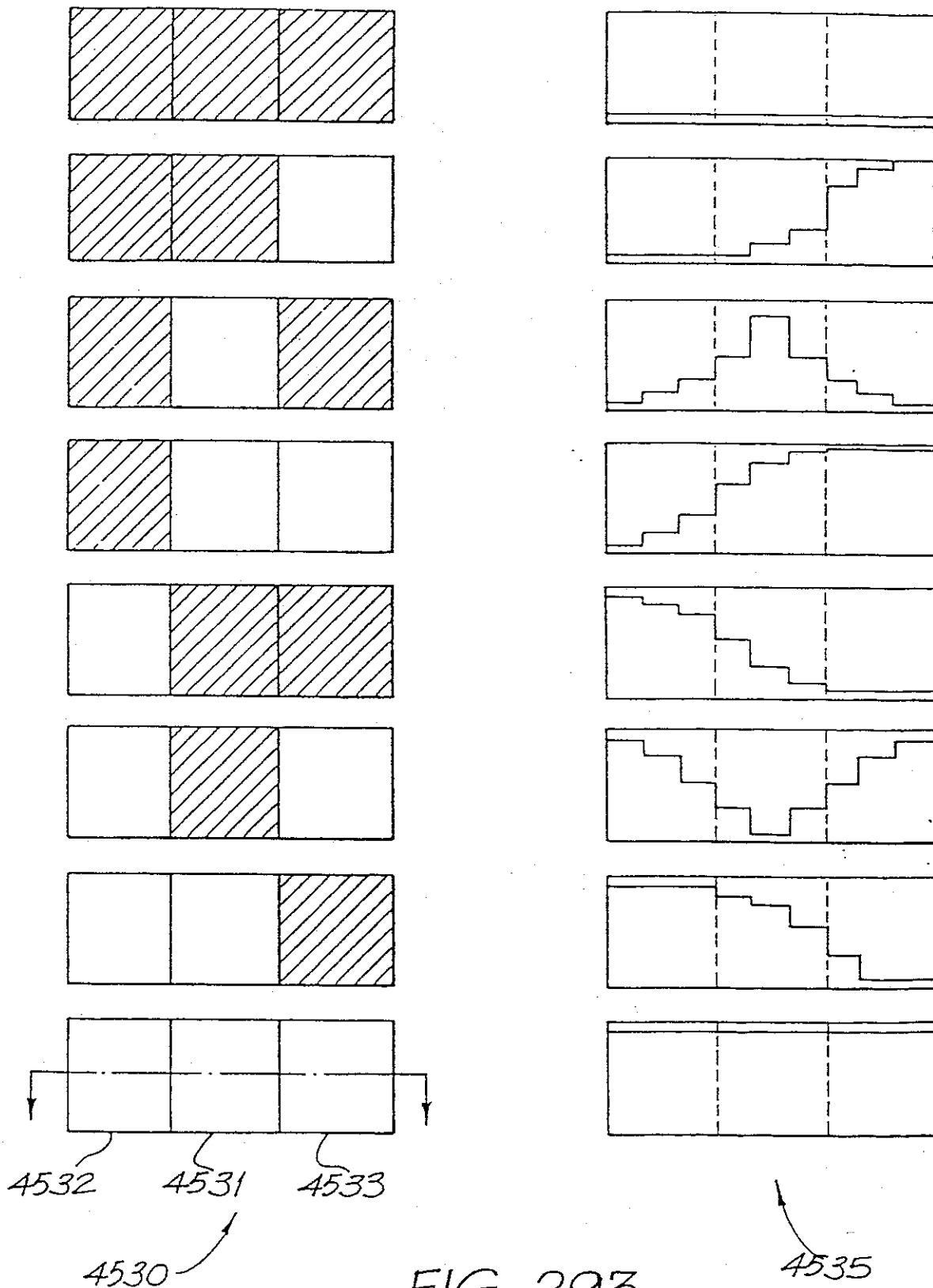


FIG. 293

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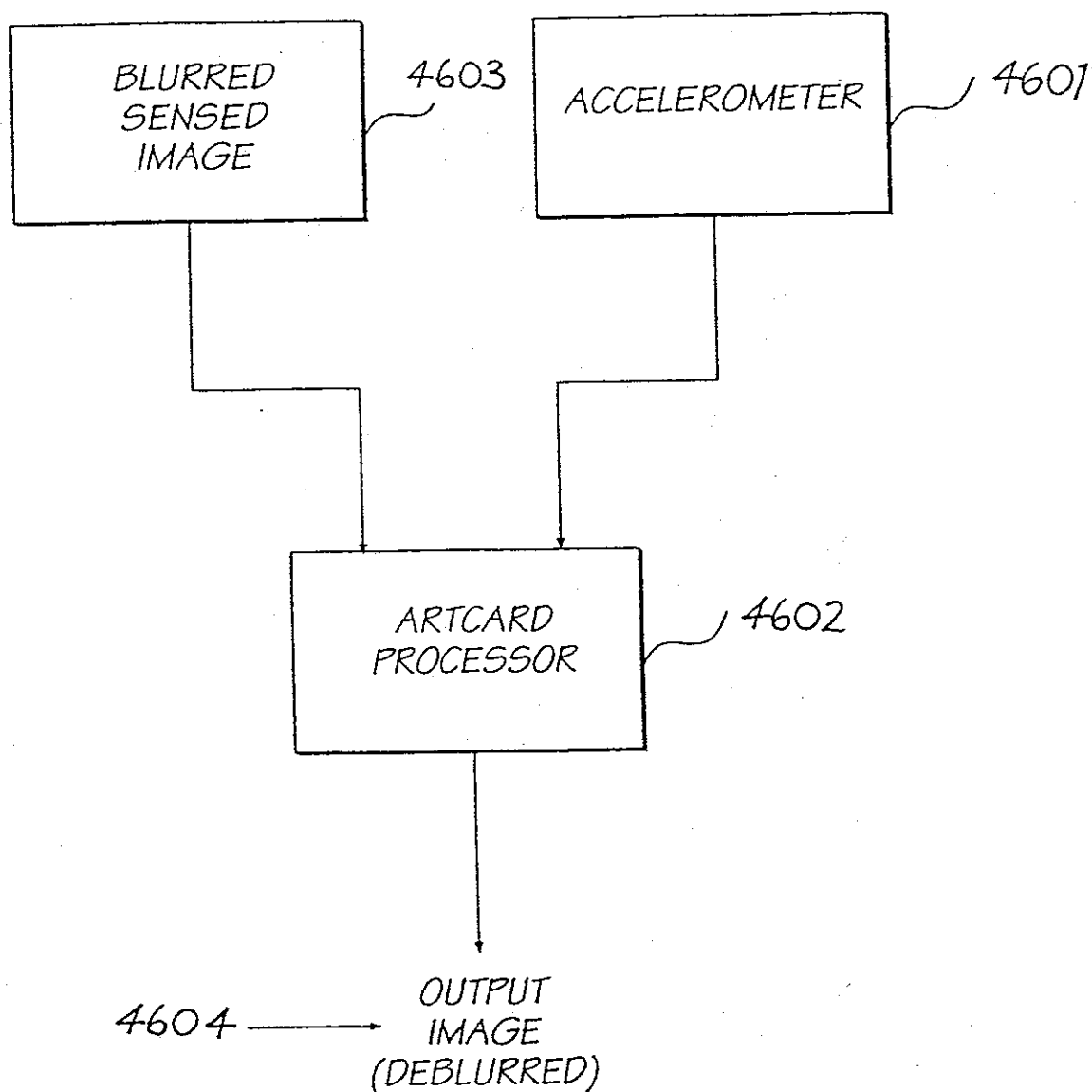


FIG. 294

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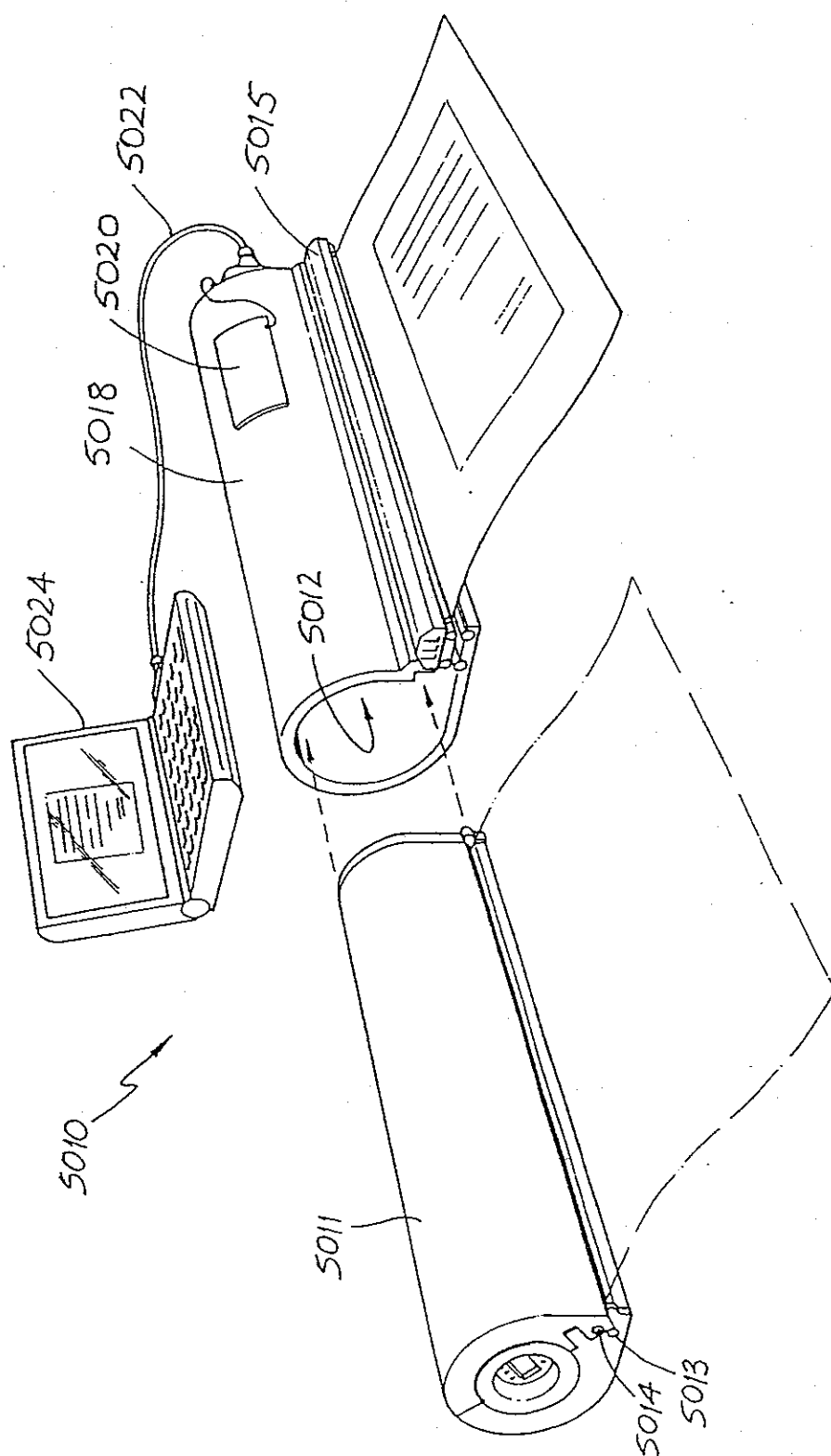


FIG. 295

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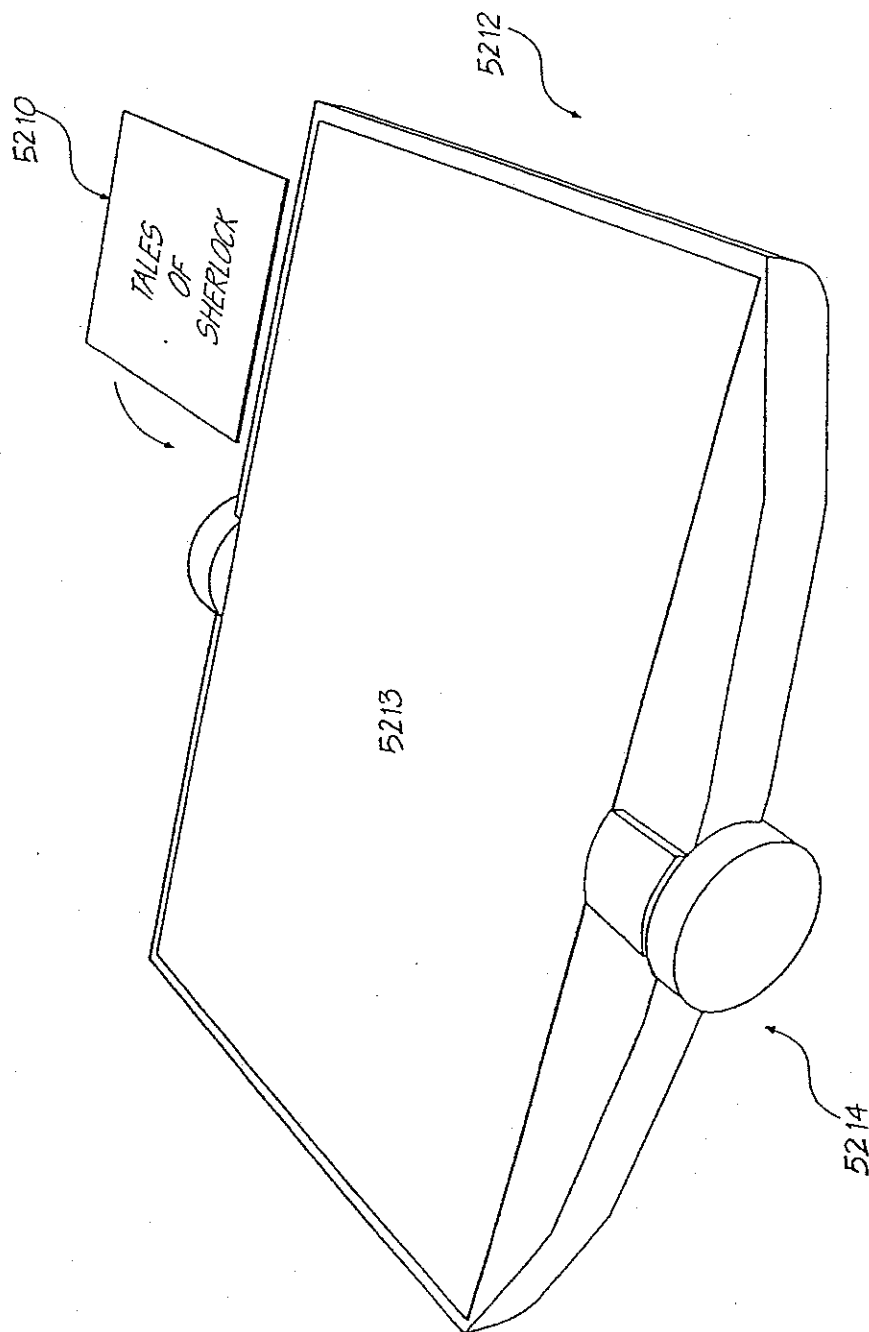
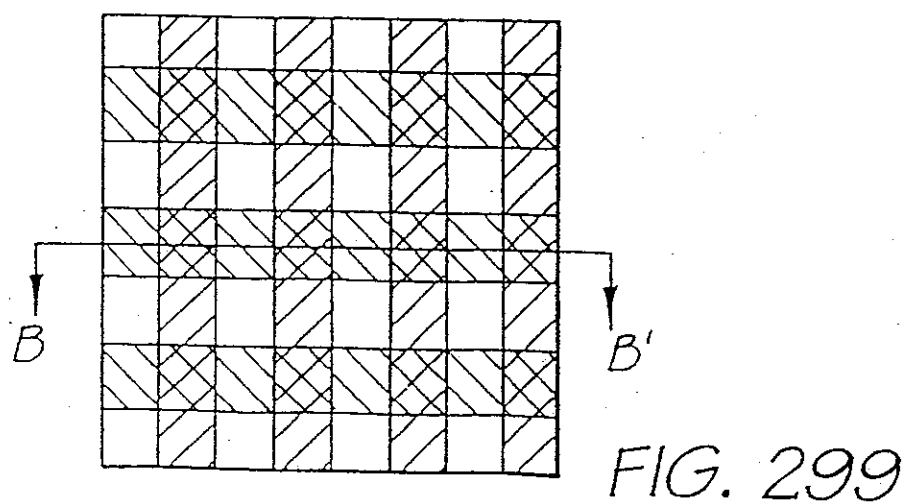
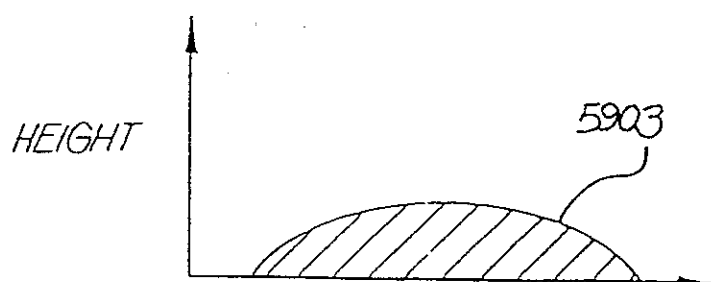
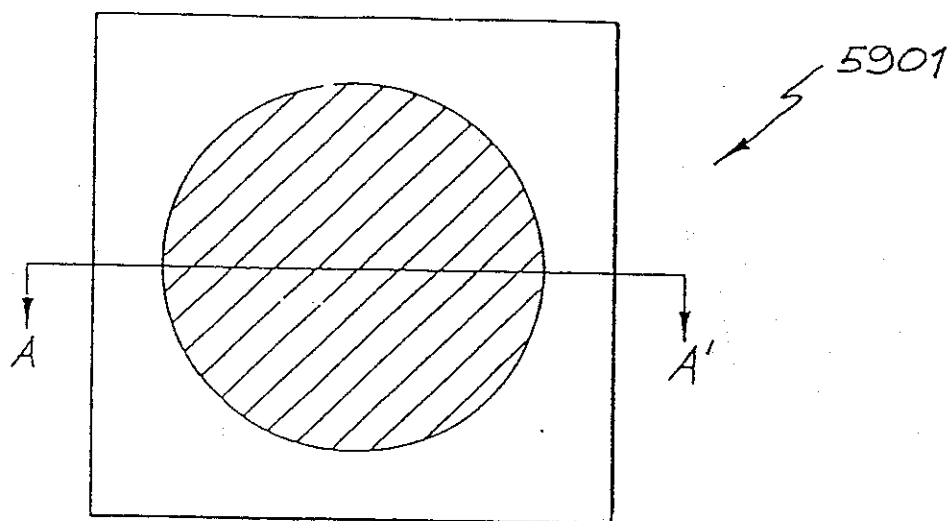


FIG. 296

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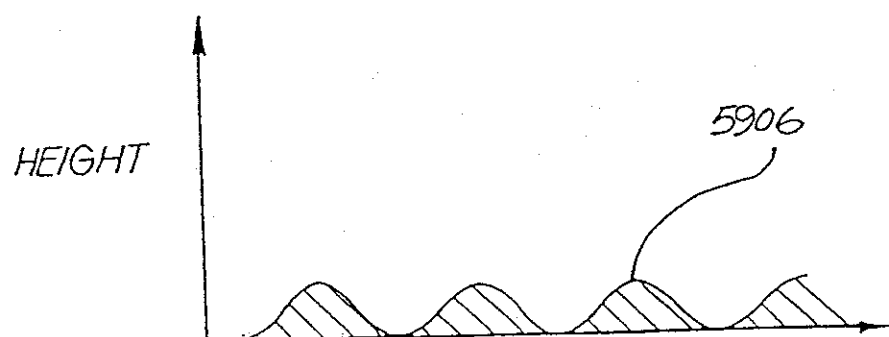


FIG. 300

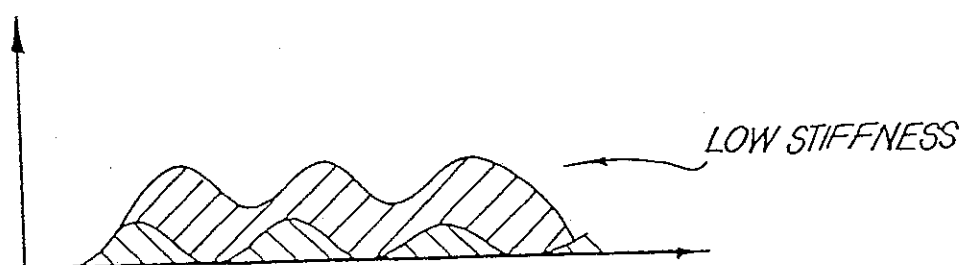


FIG. 301

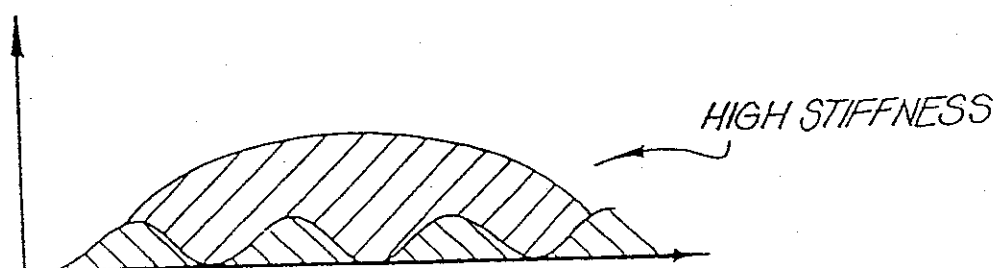


FIG. 302

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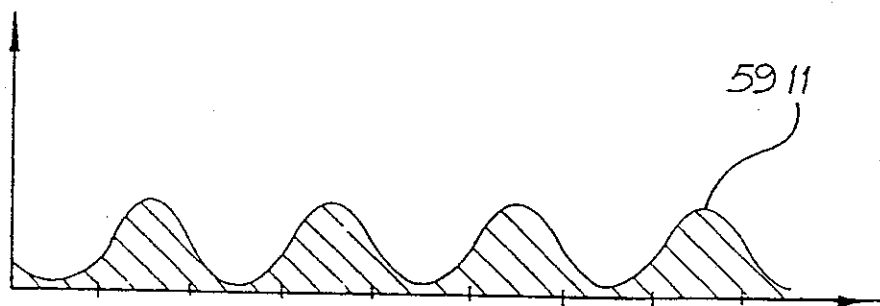


FIG. 303

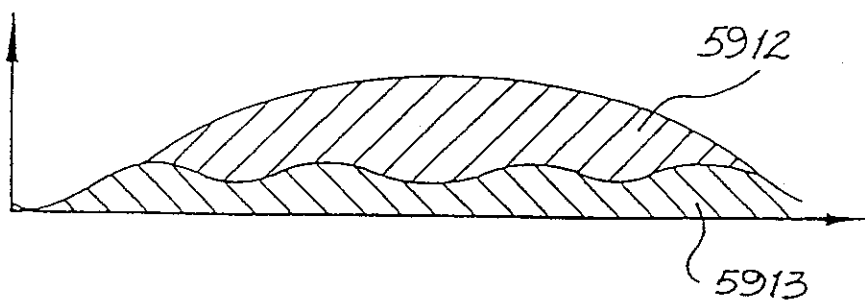


FIG. 304

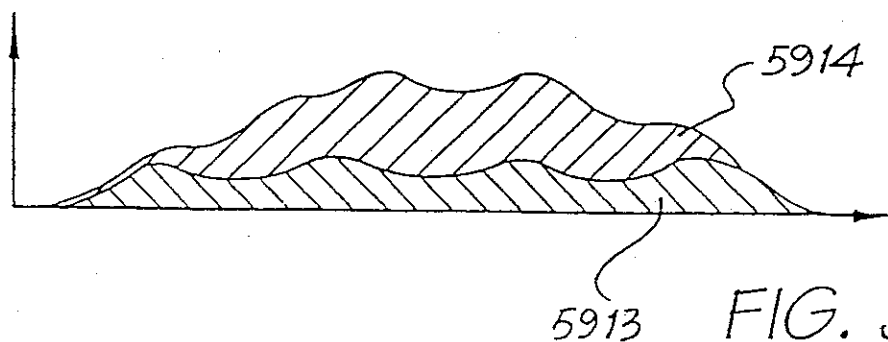


FIG. 305

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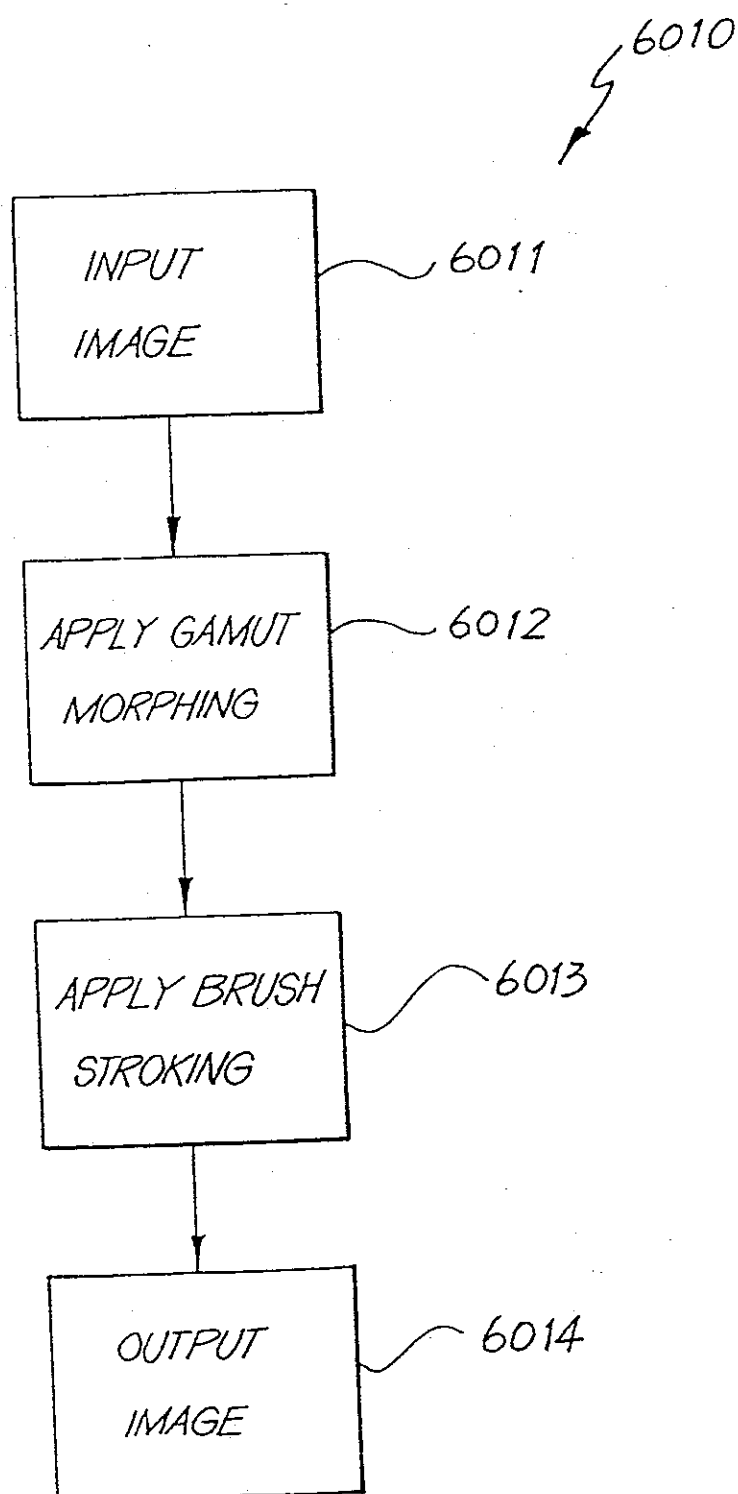


FIG. 306

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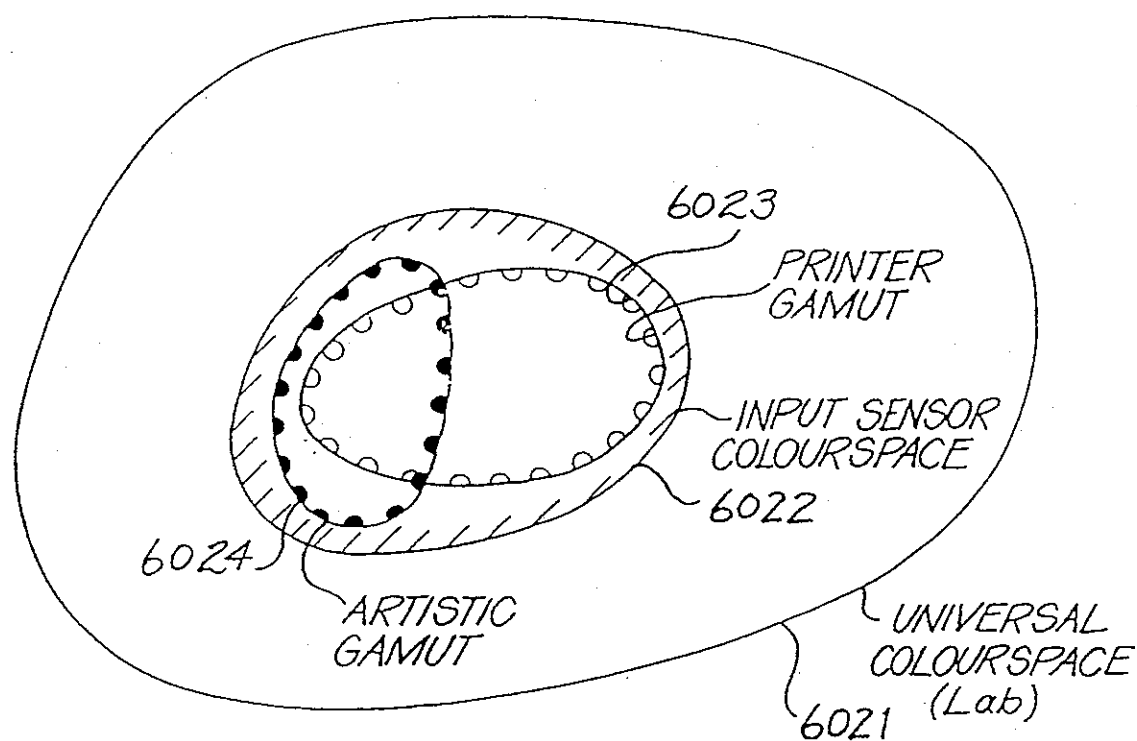


FIG. 307

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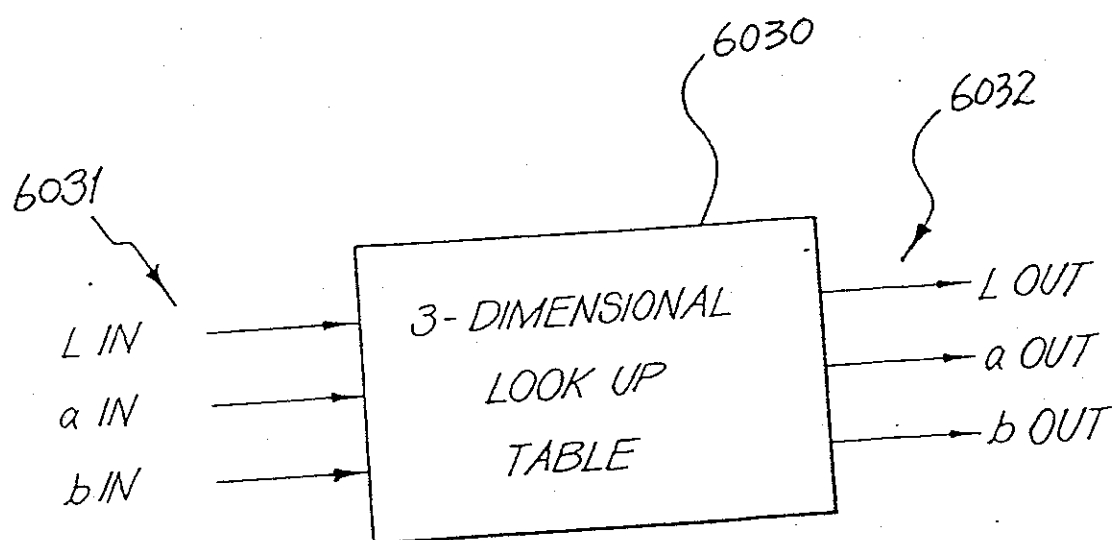


FIG. 308

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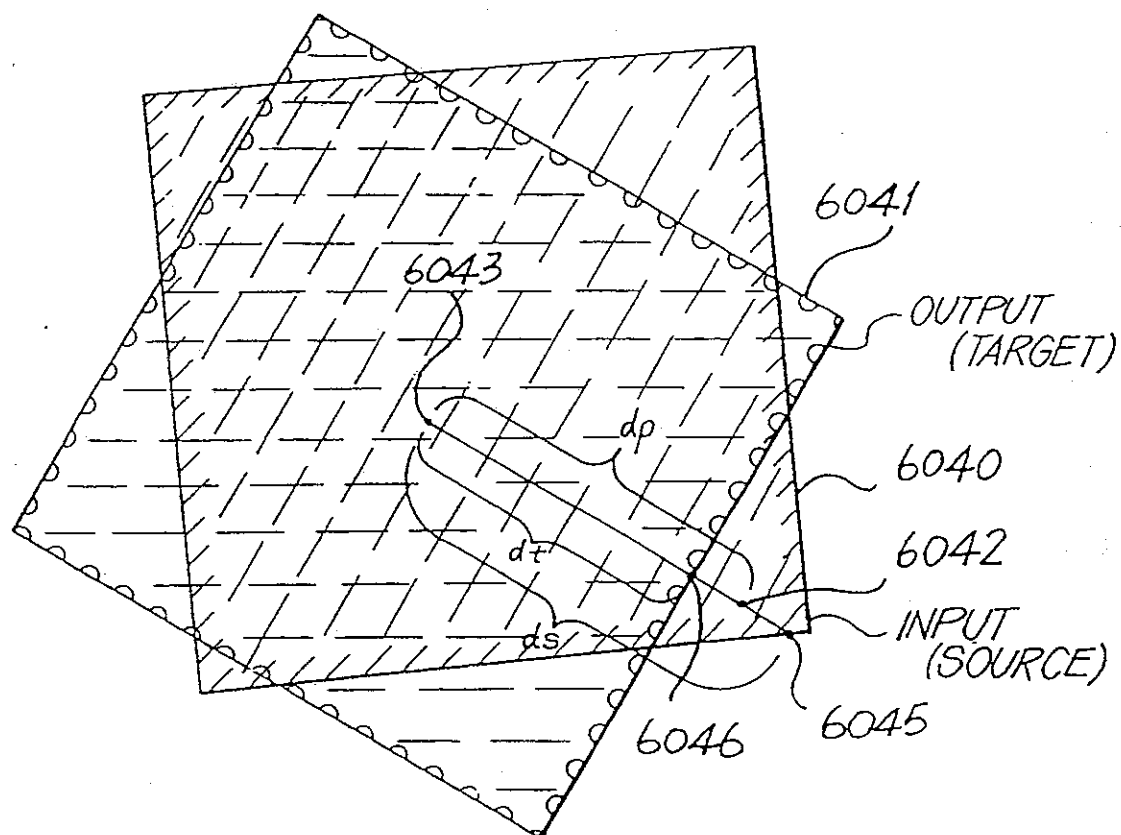


FIG. 309

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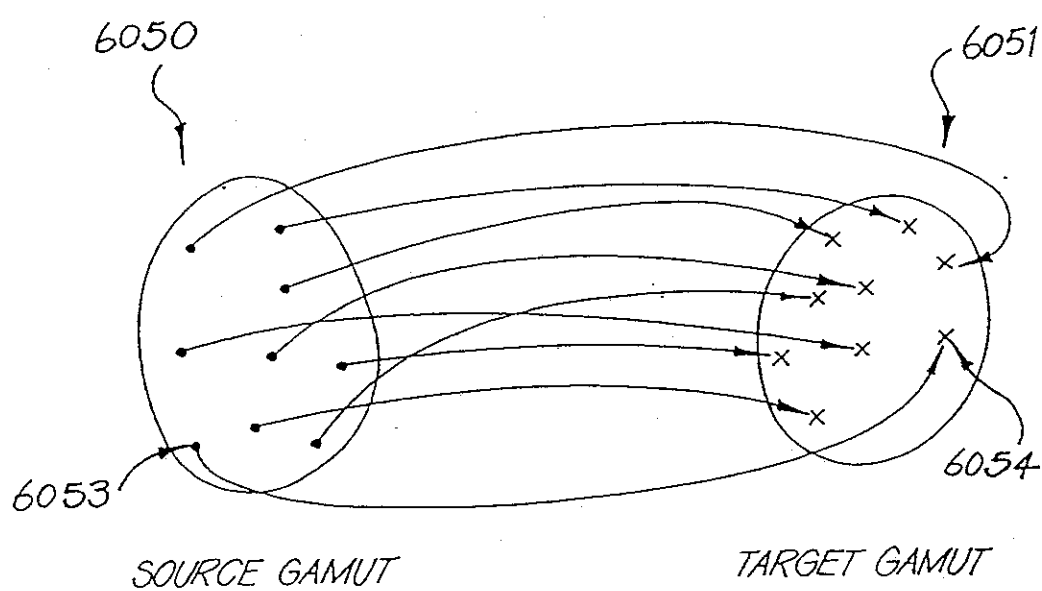


FIG. 310

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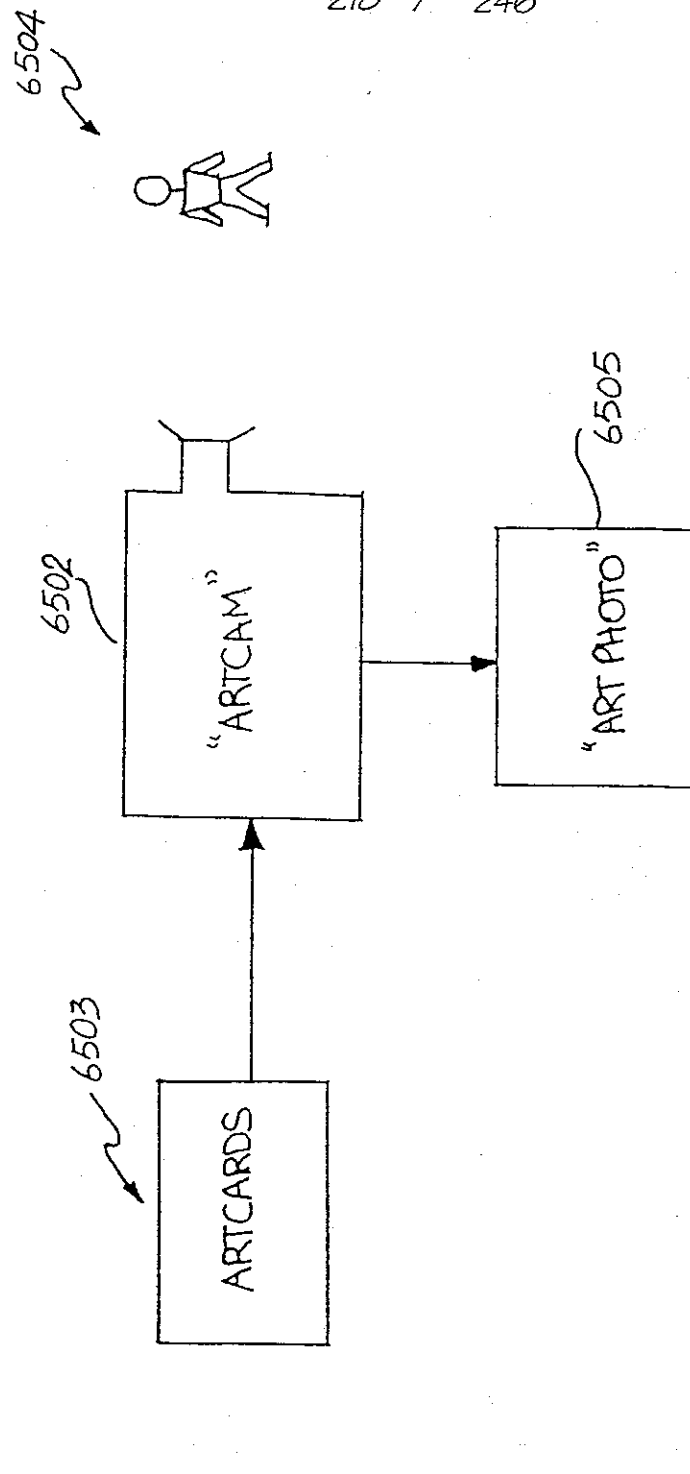


FIG. 311

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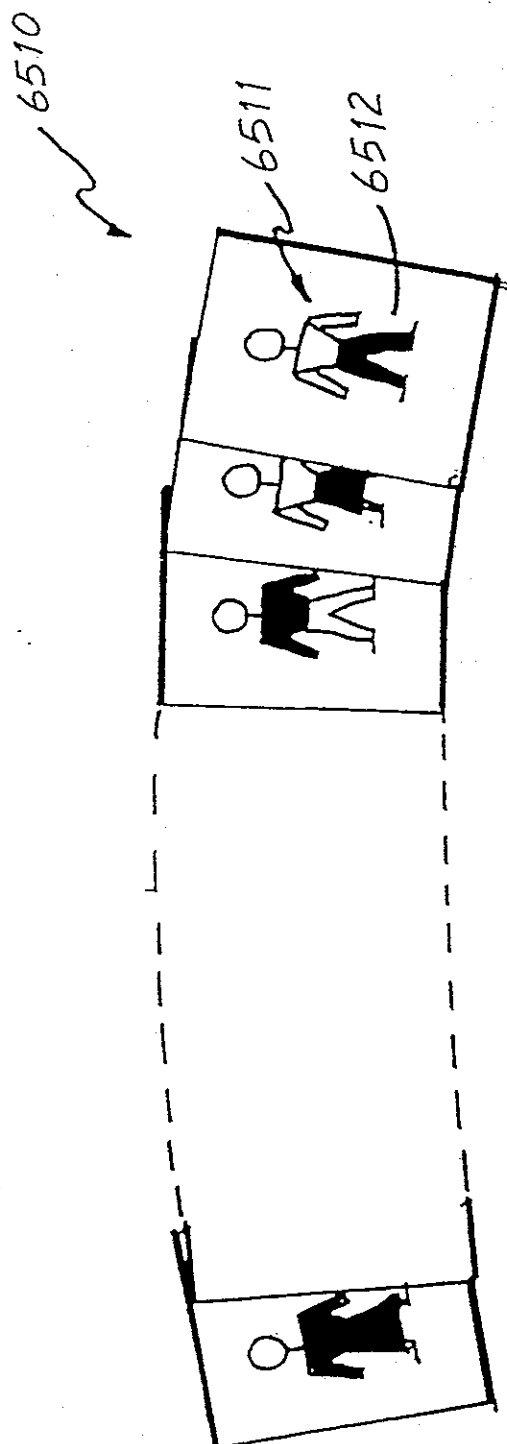


FIG. 312

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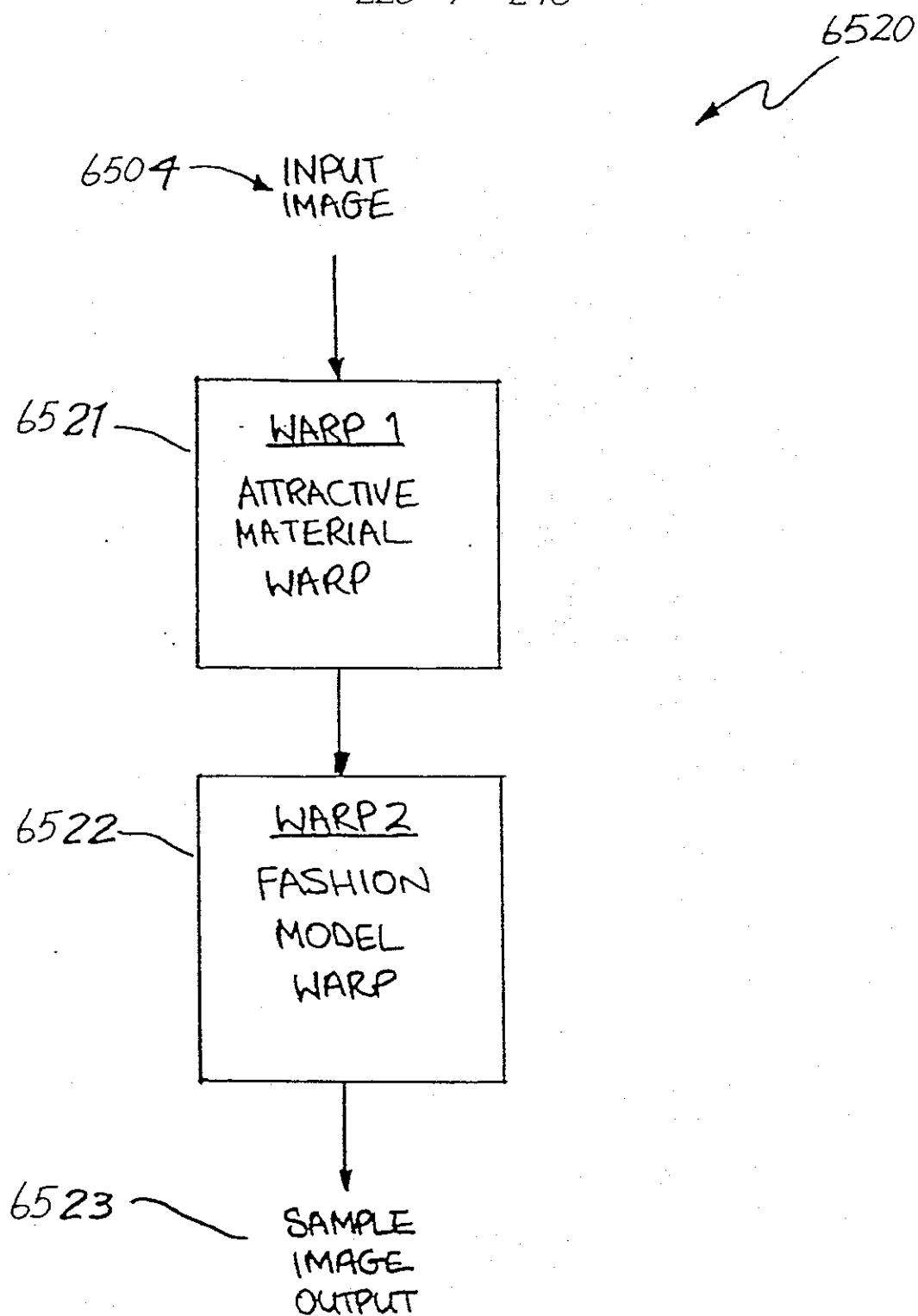


FIG. 313

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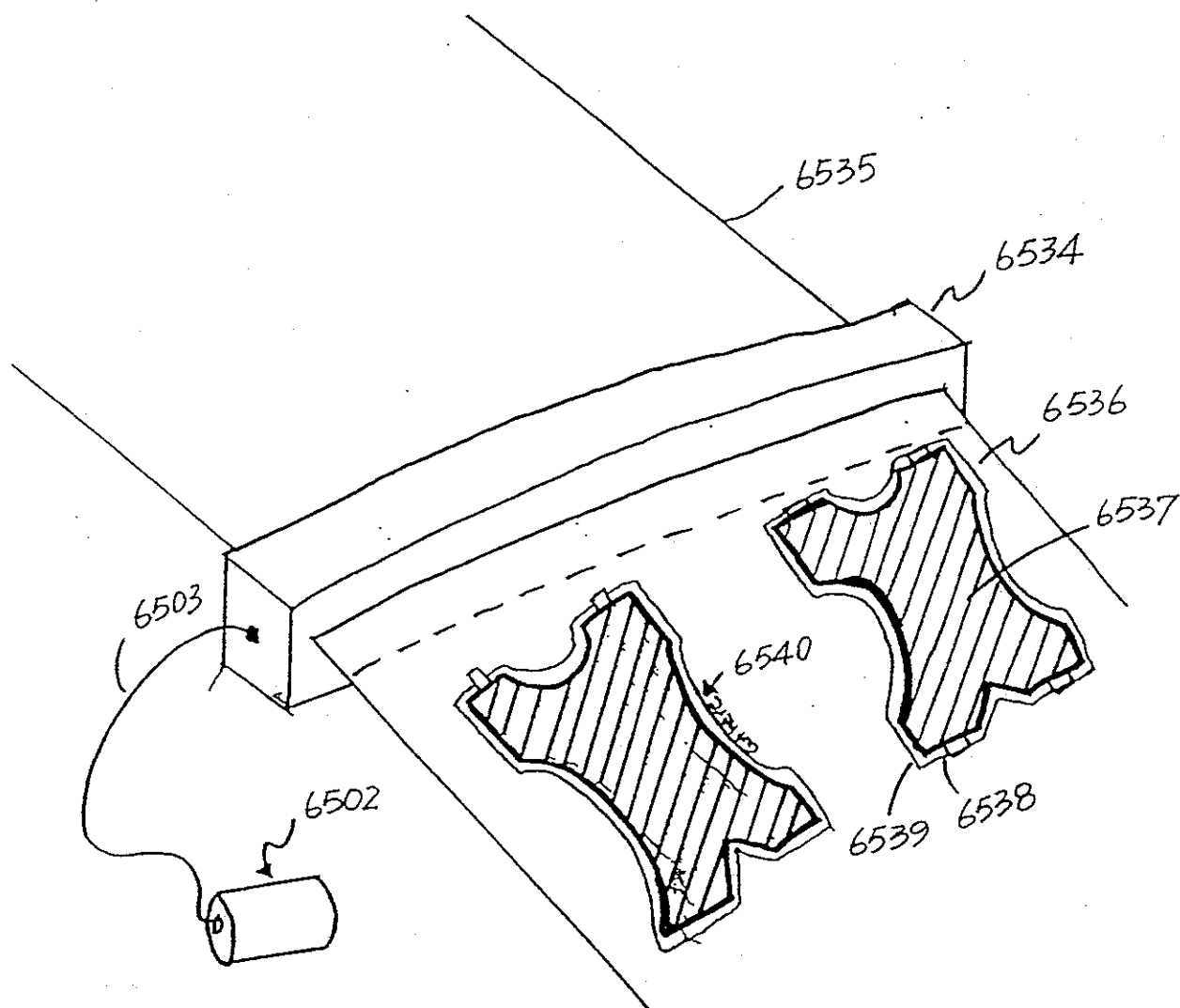


FIG. 314

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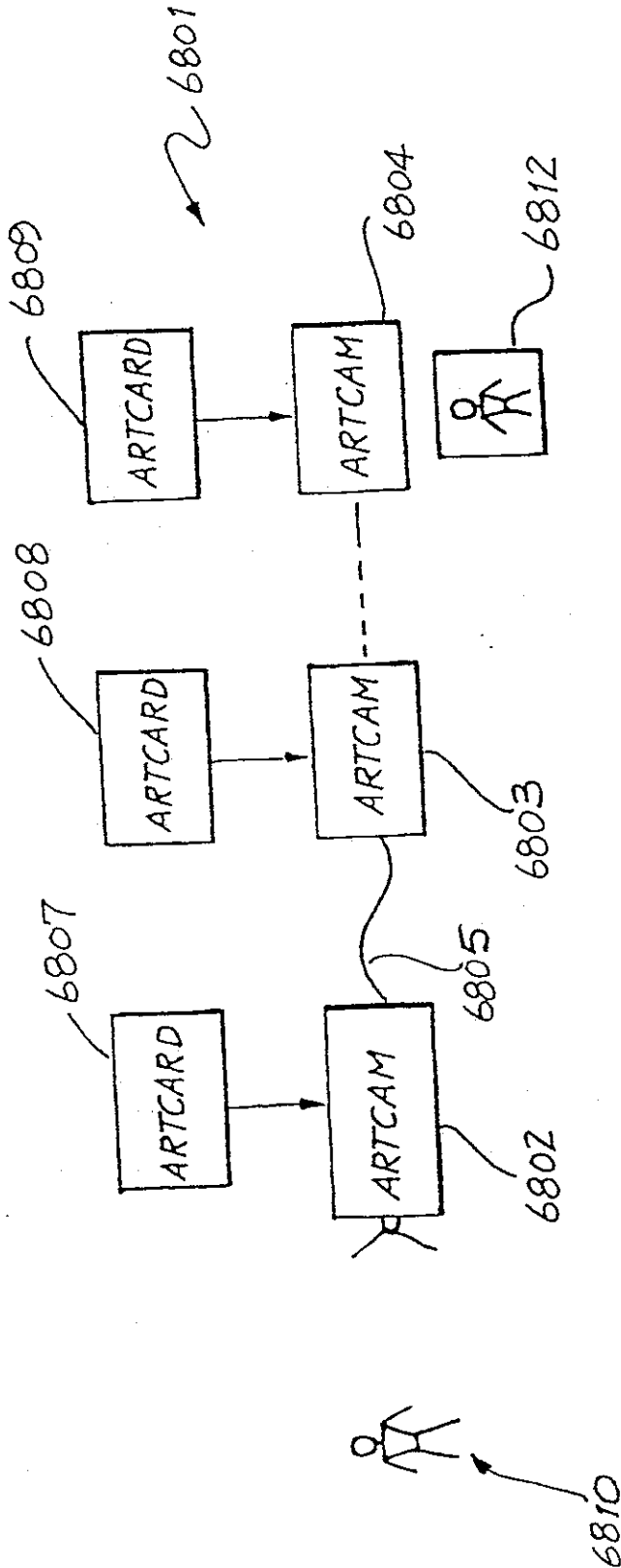


FIG. 315

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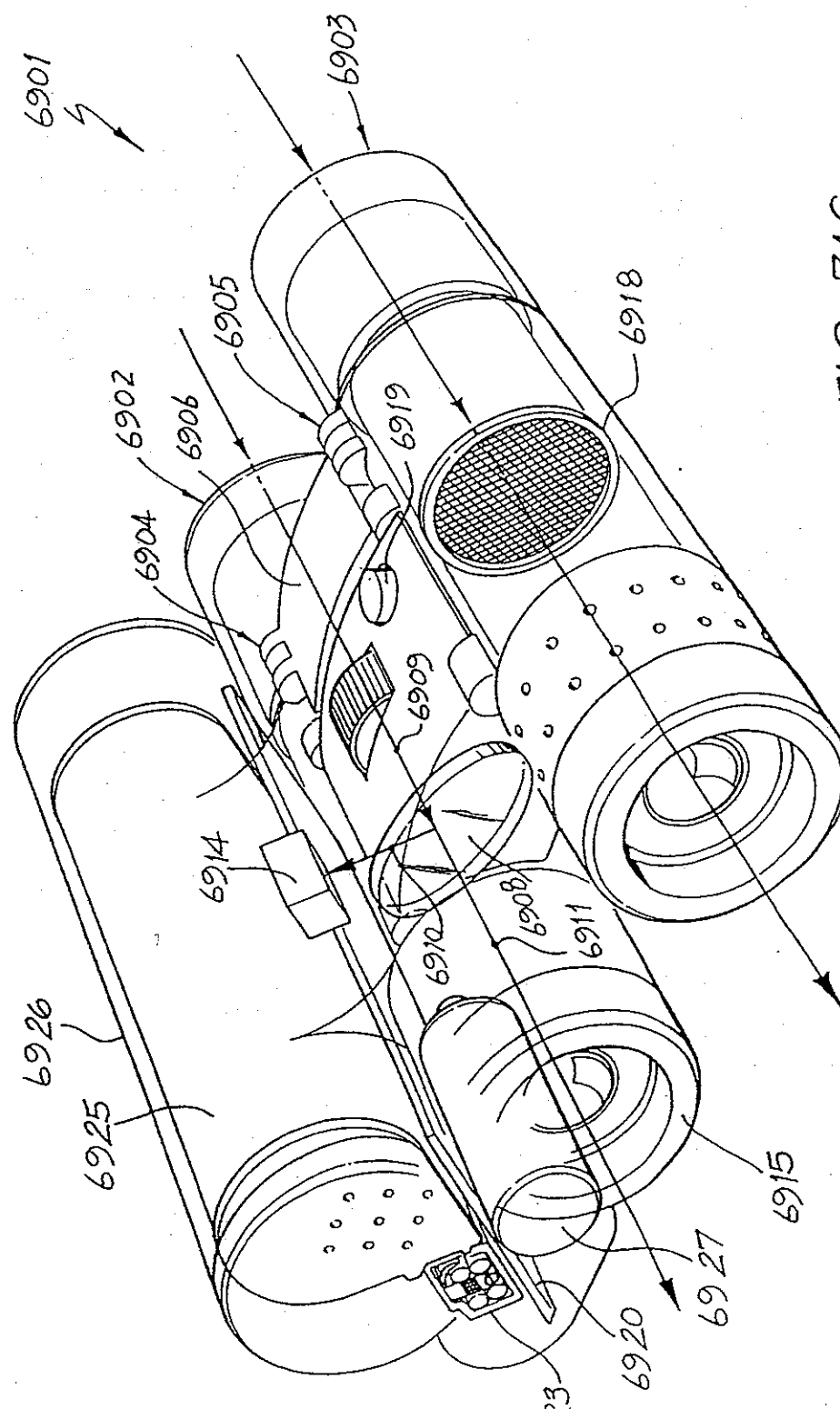


FIG. 316

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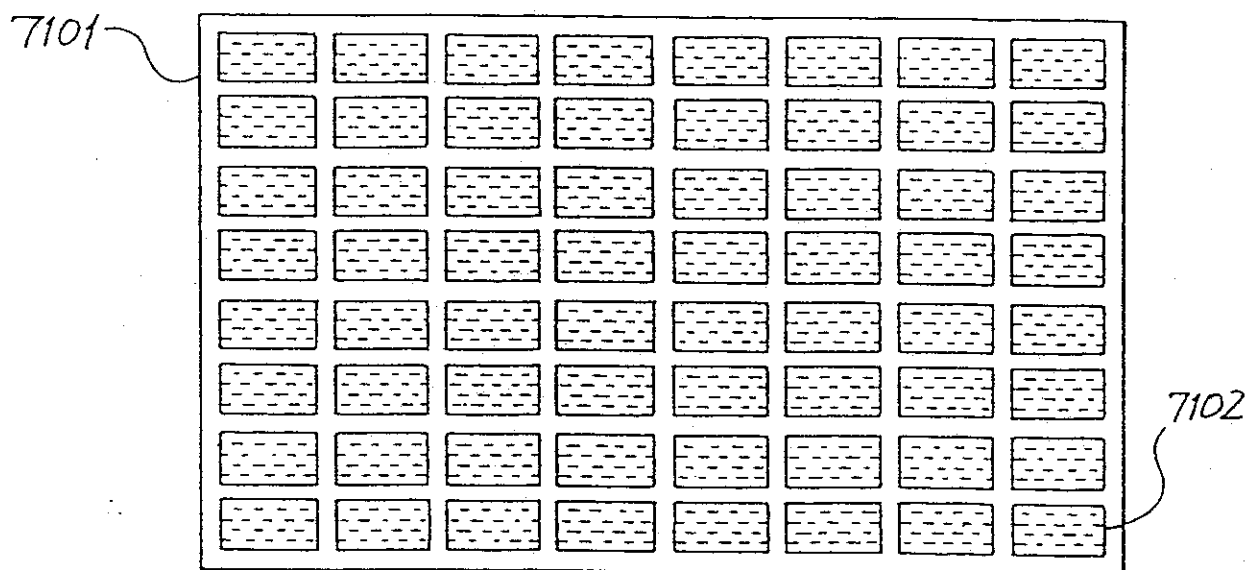


FIG. 317

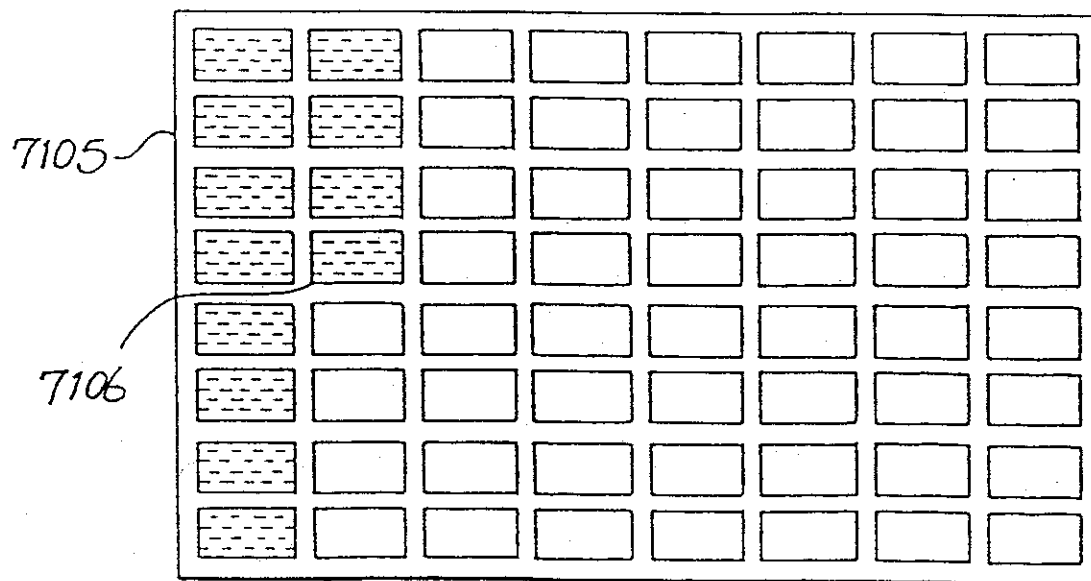


FIG. 318

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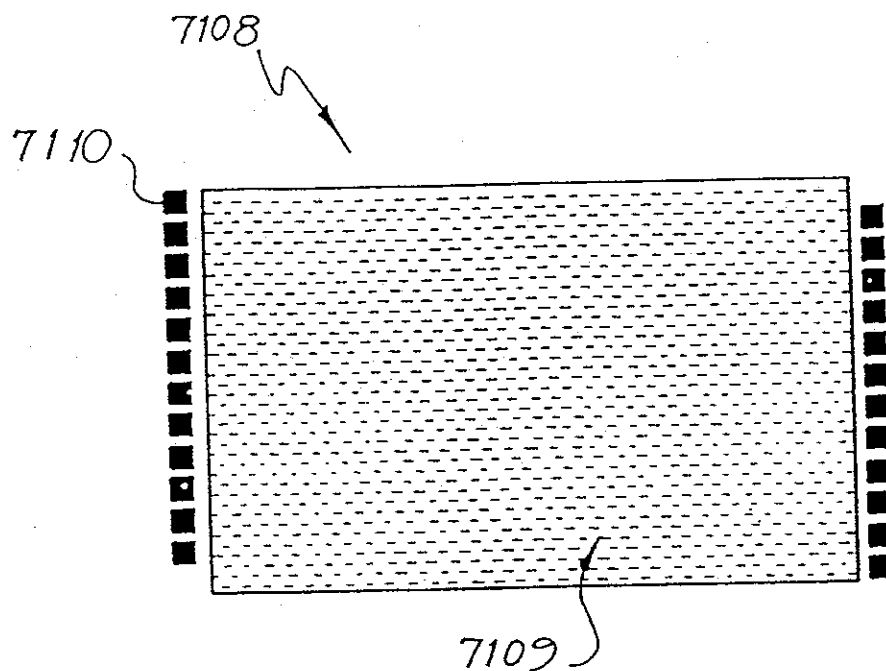


FIG. 319

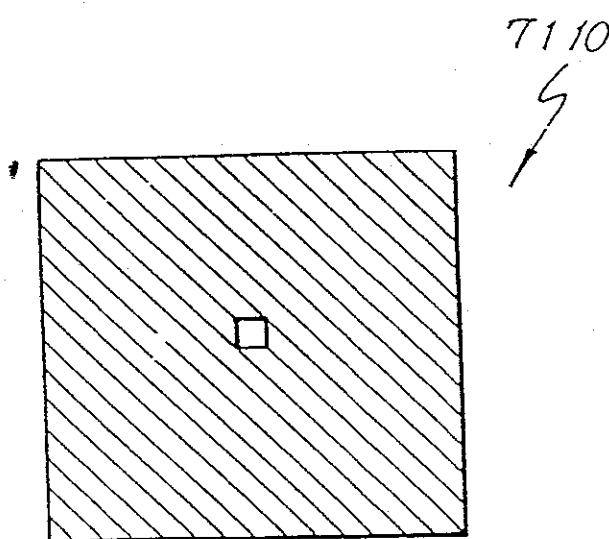


FIG. 320

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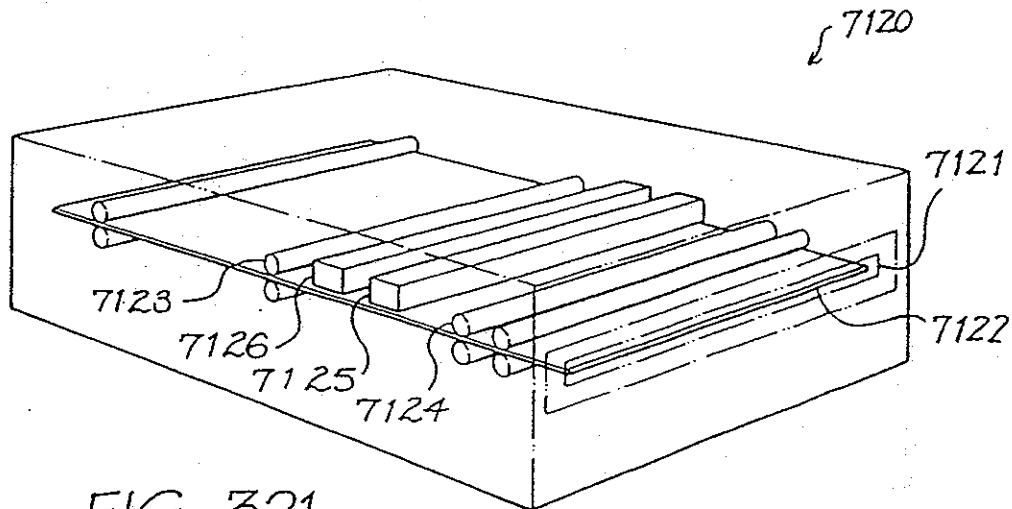


FIG. 321

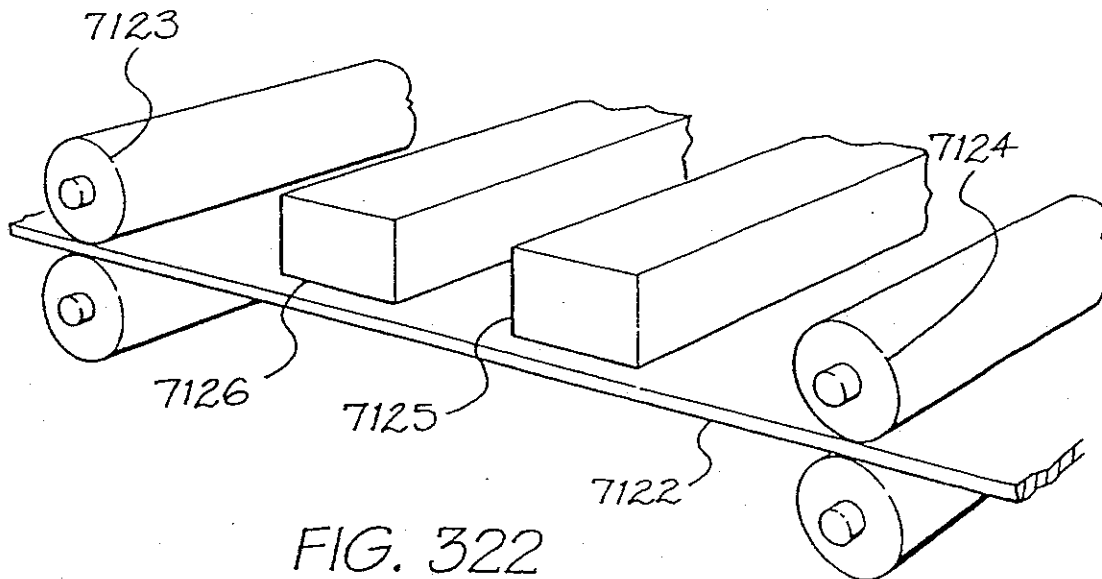
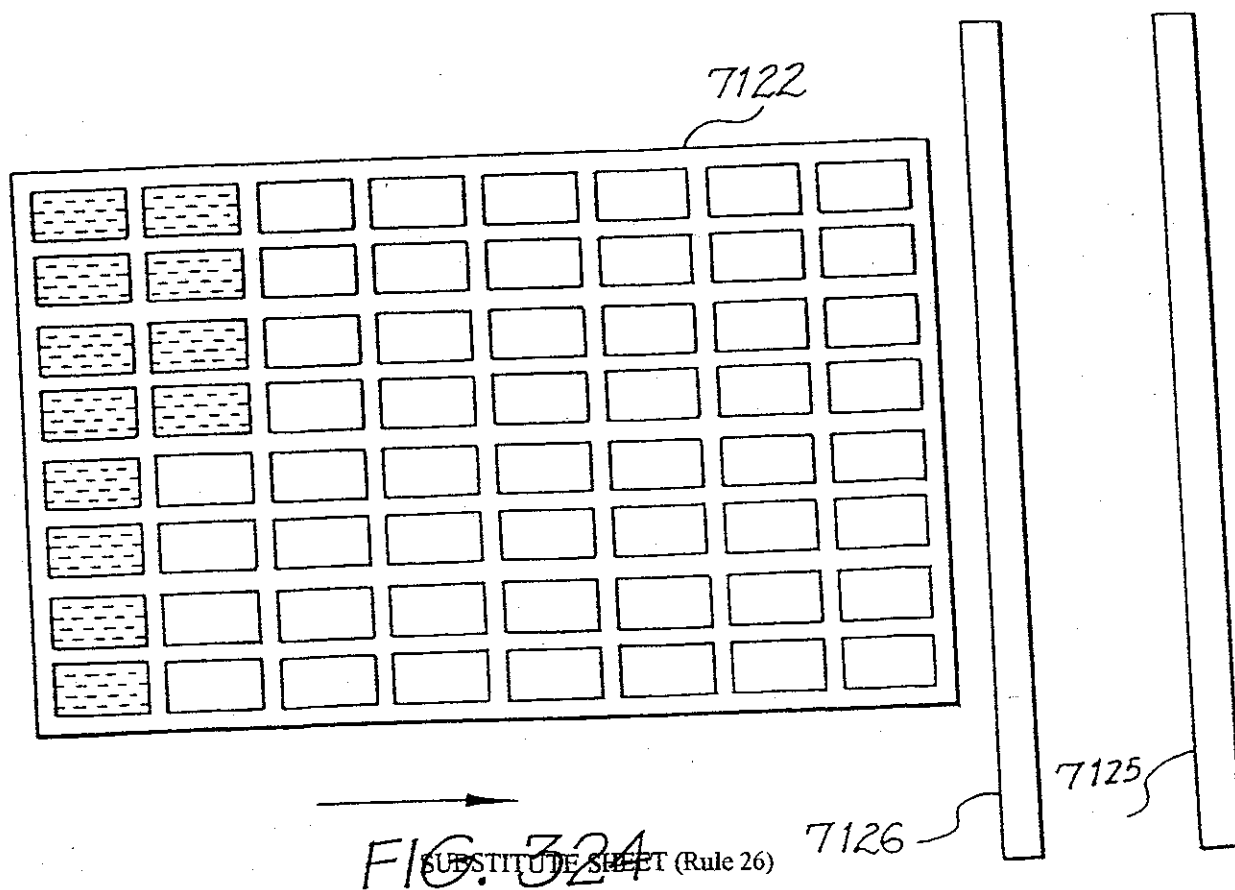
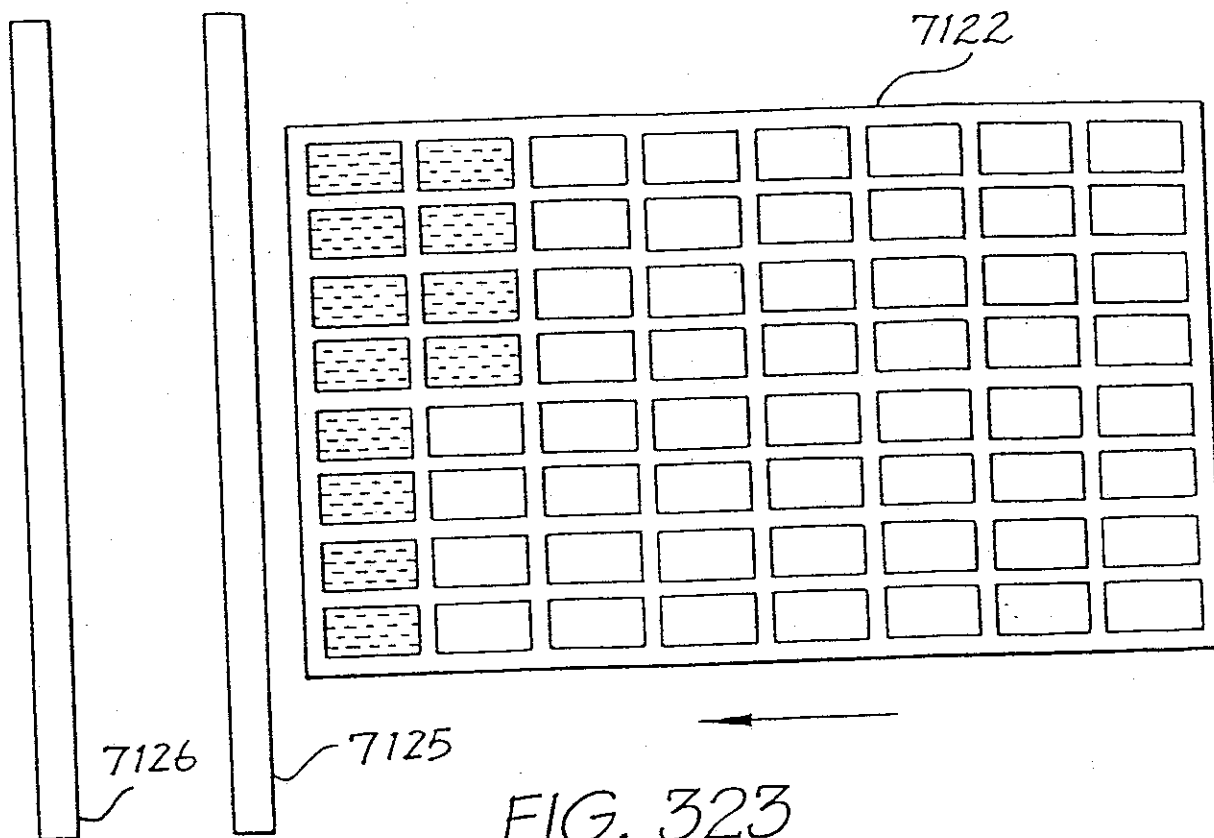
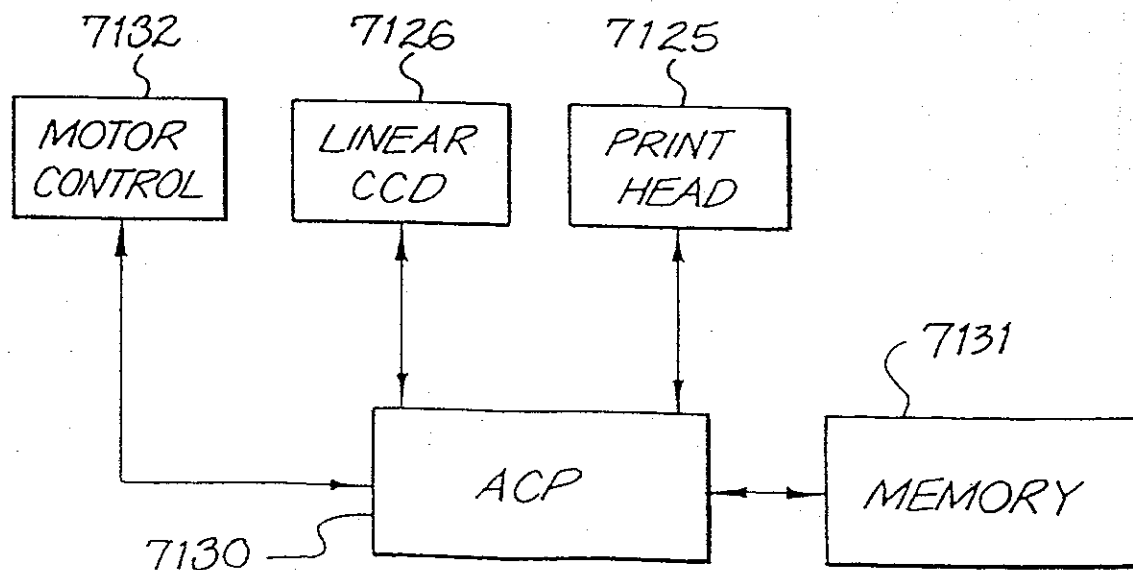
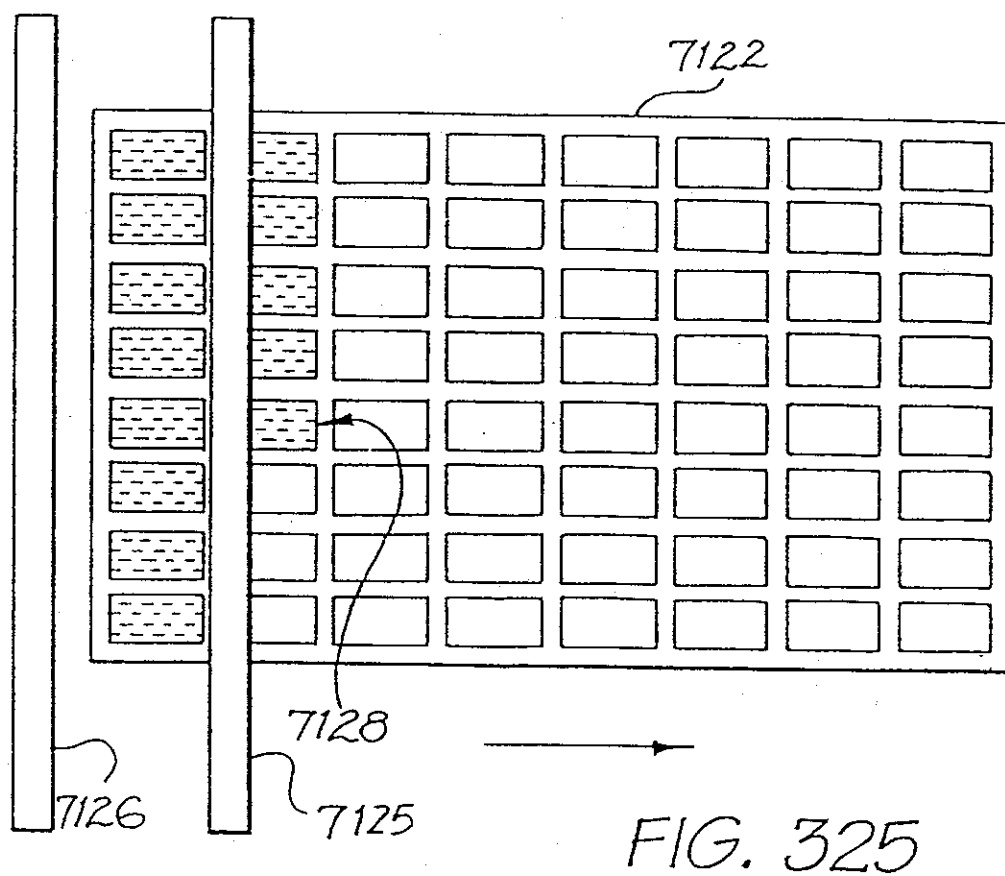


FIG. 322

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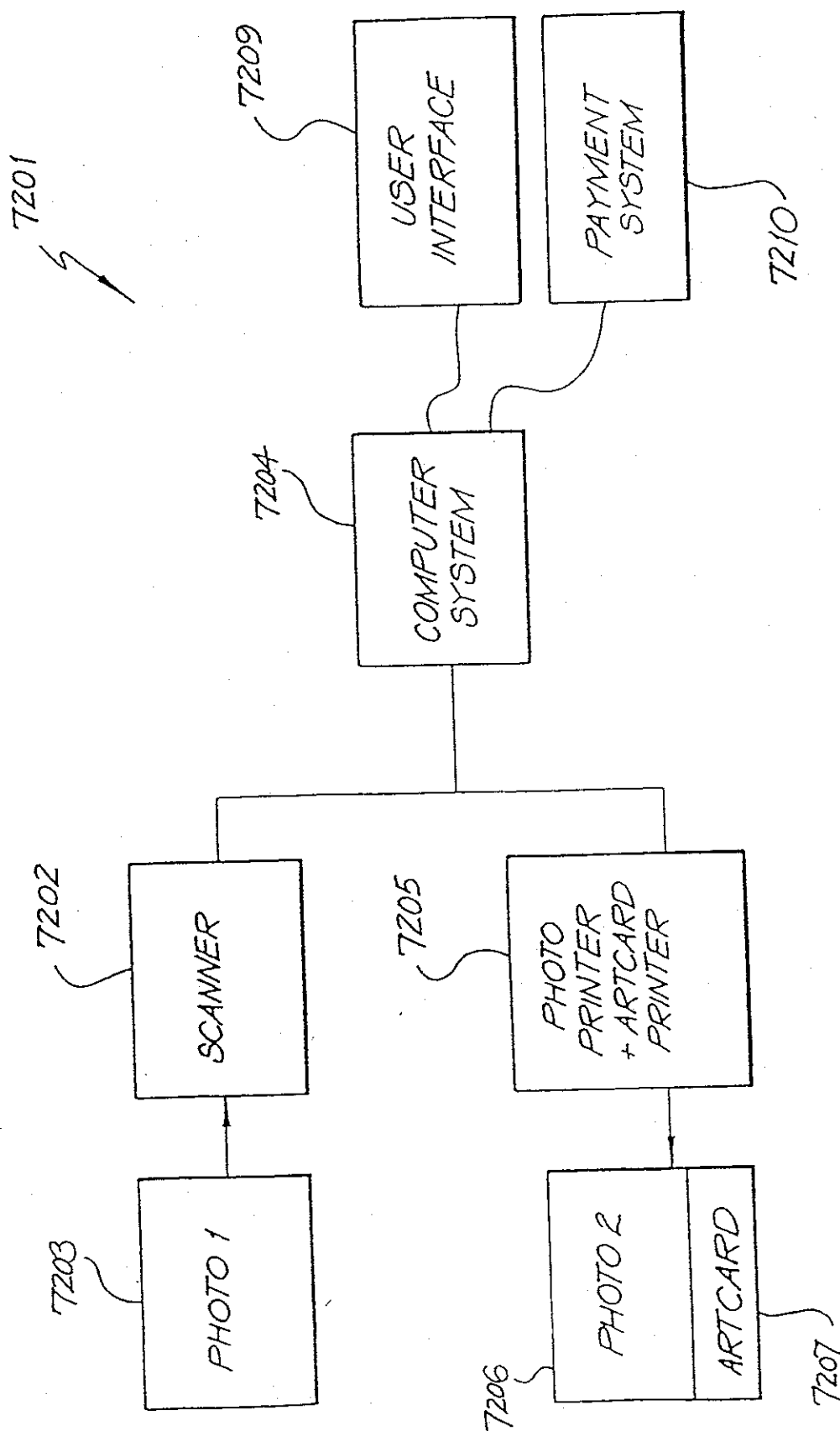


FIG. 327

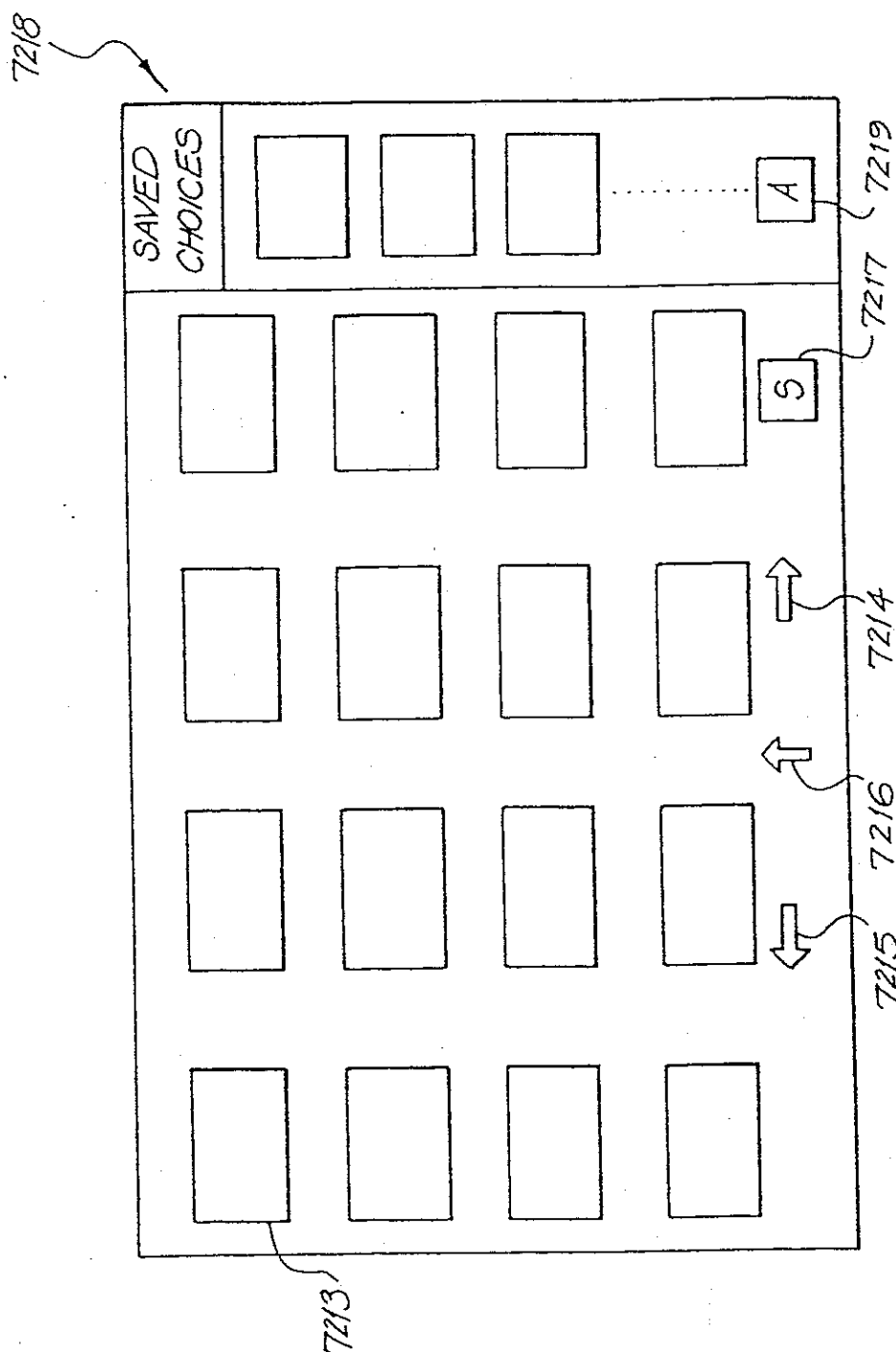
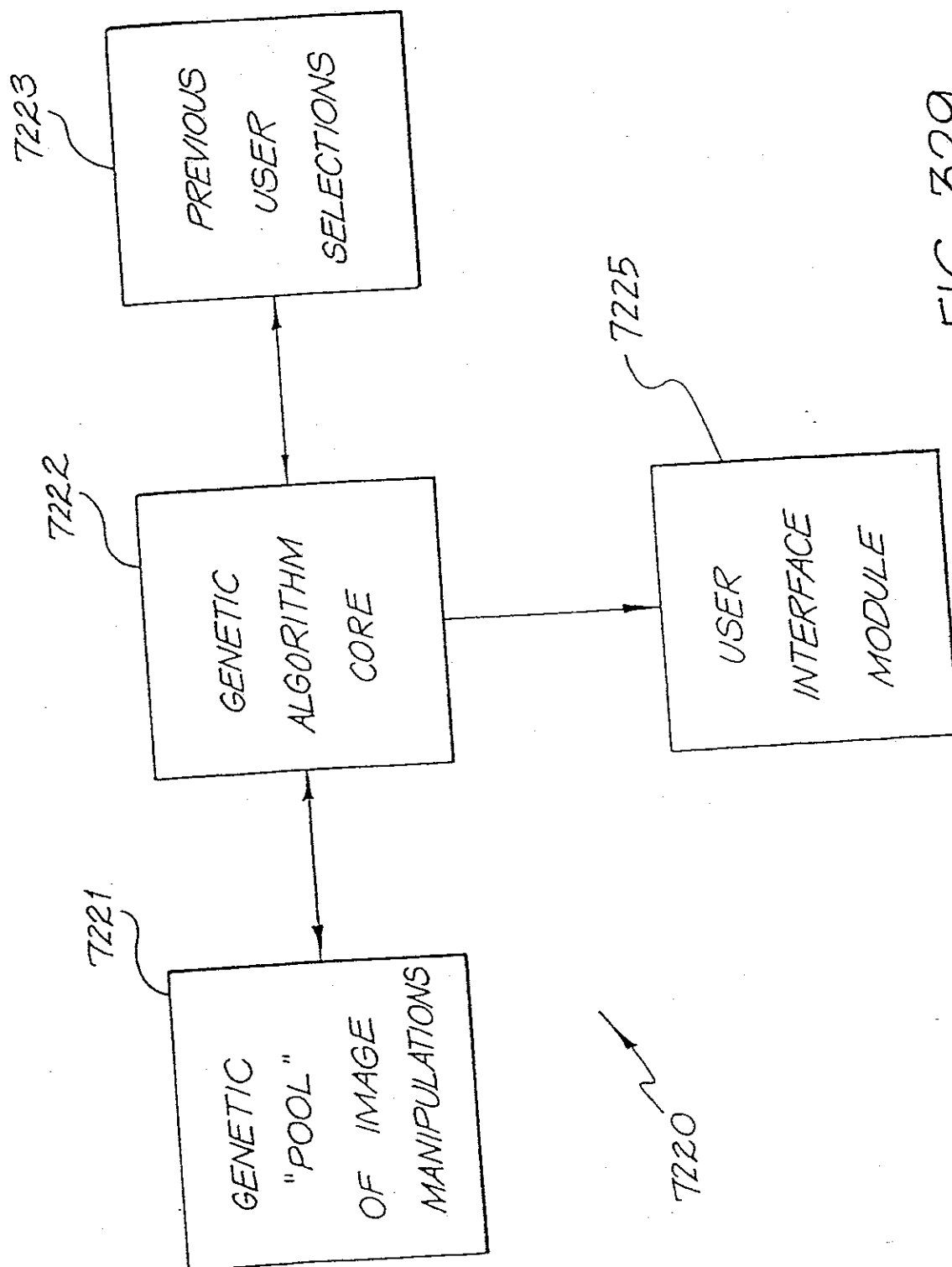


FIG. 328

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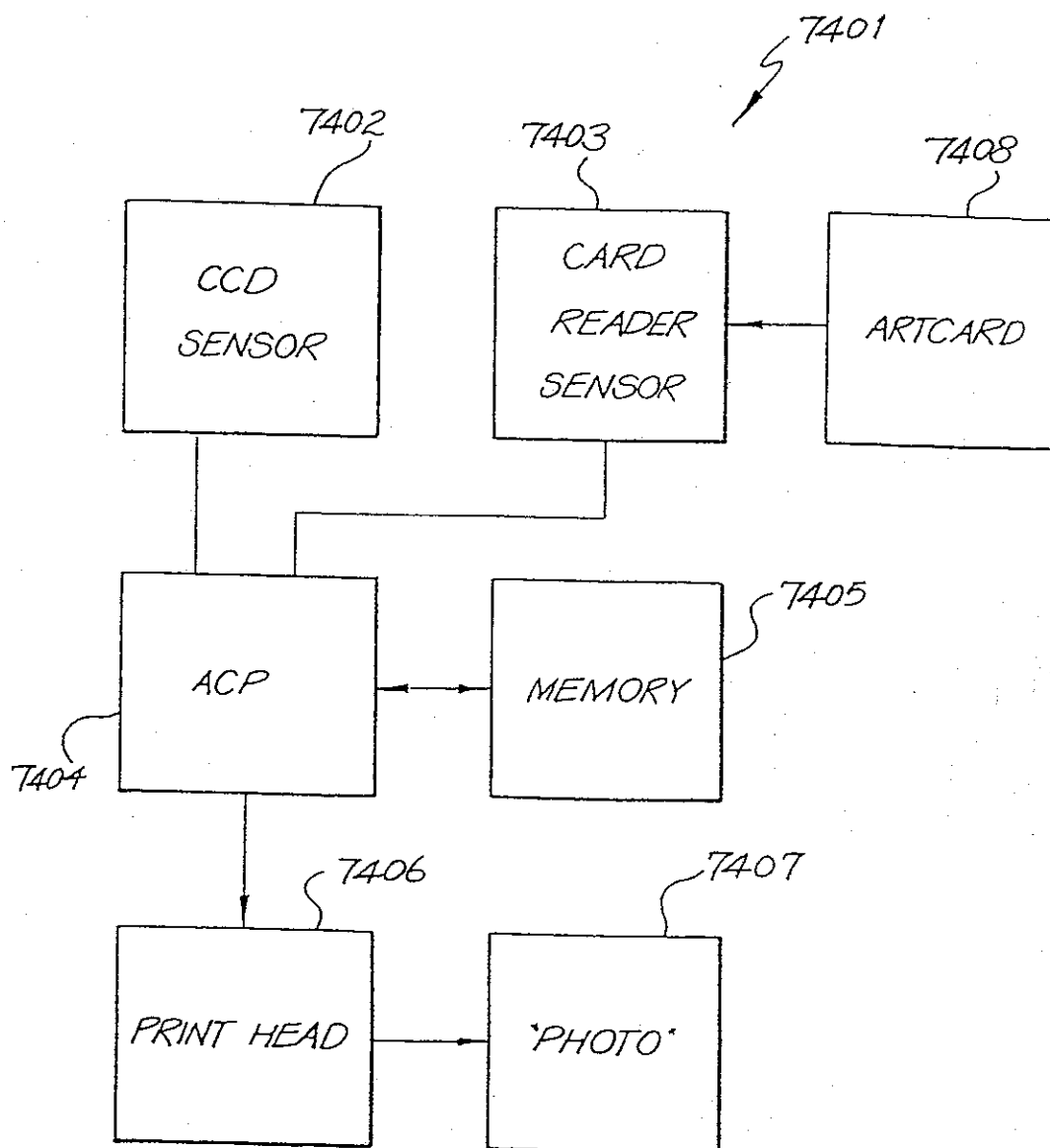


FIG. 330

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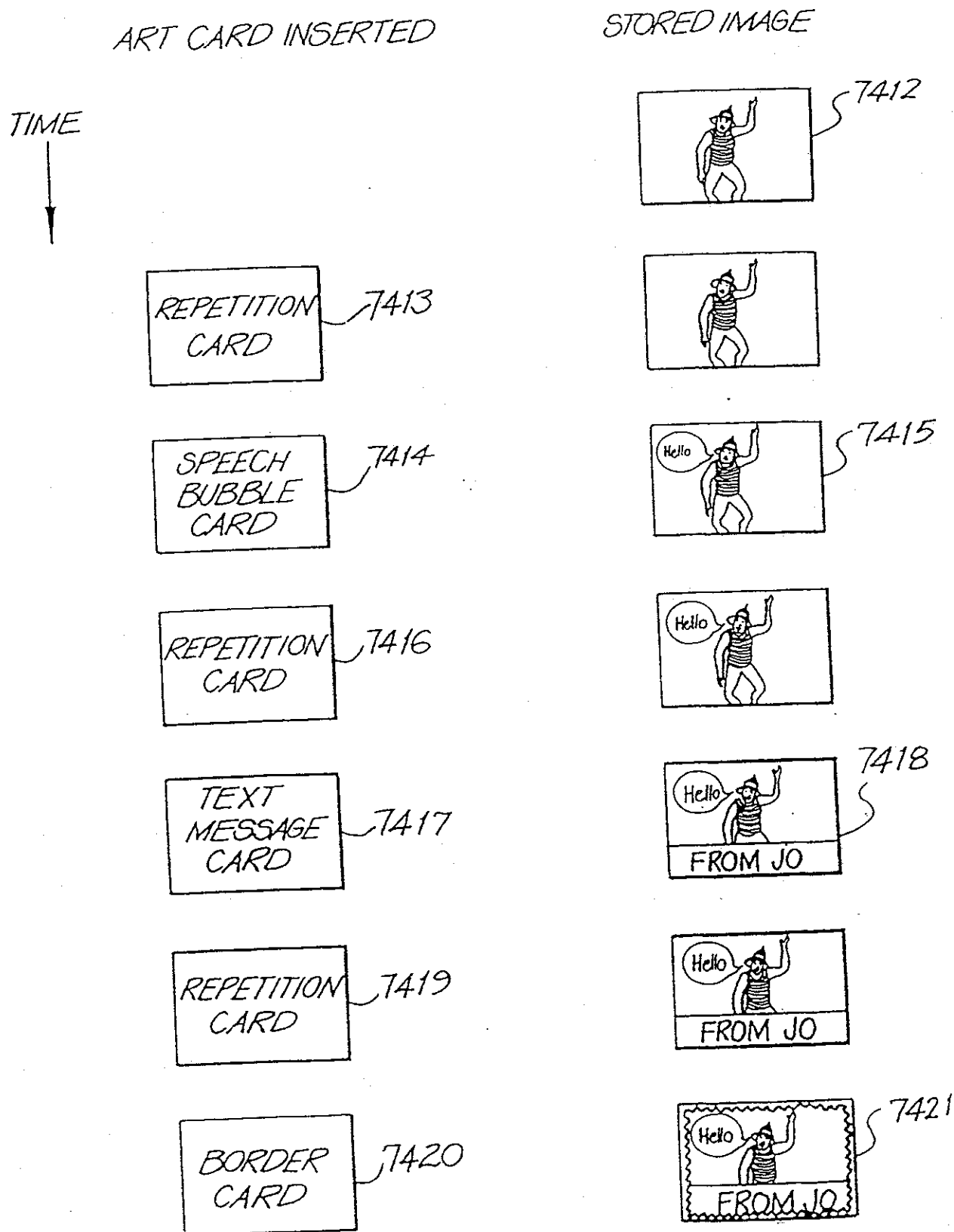


FIG. 331

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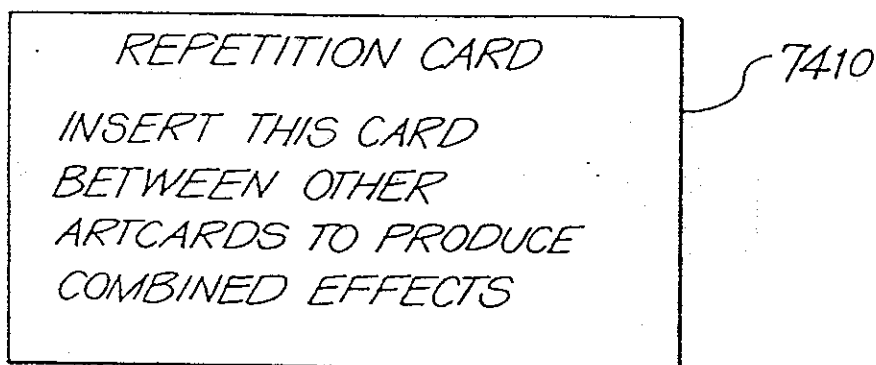


FIG. 332

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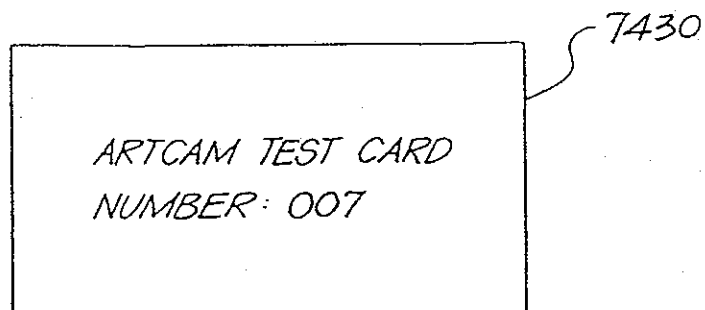


FIG. 333

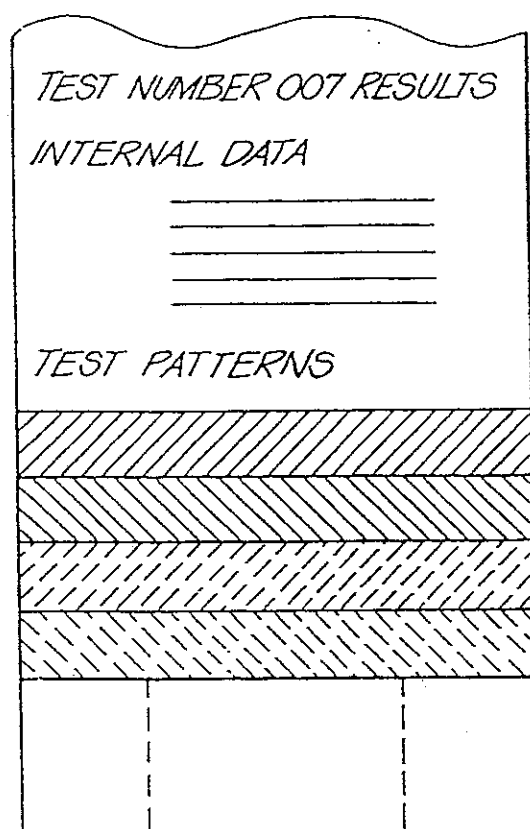


FIG. 334

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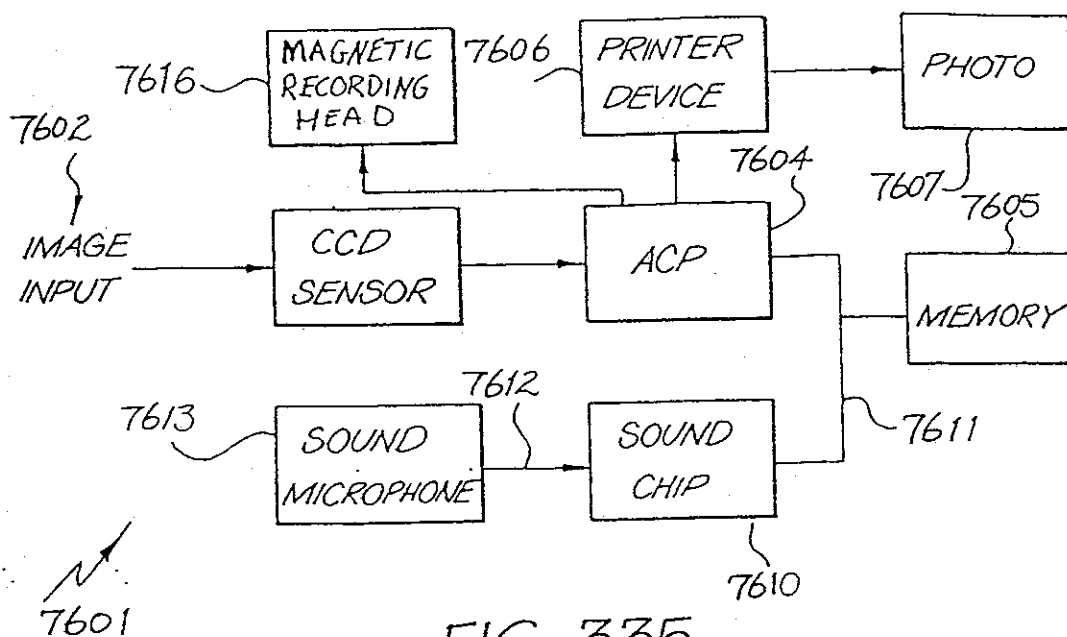


FIG. 335

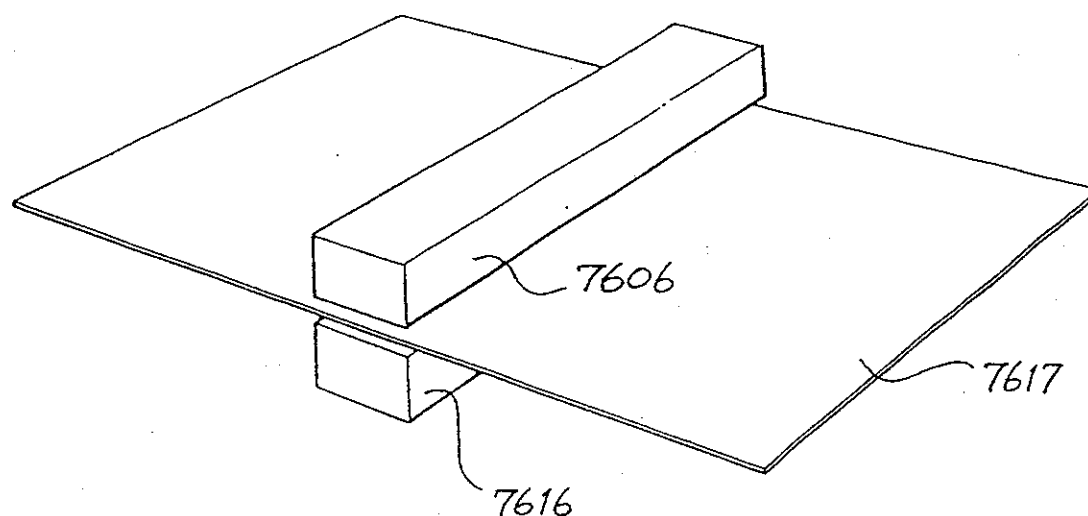


FIG. 336

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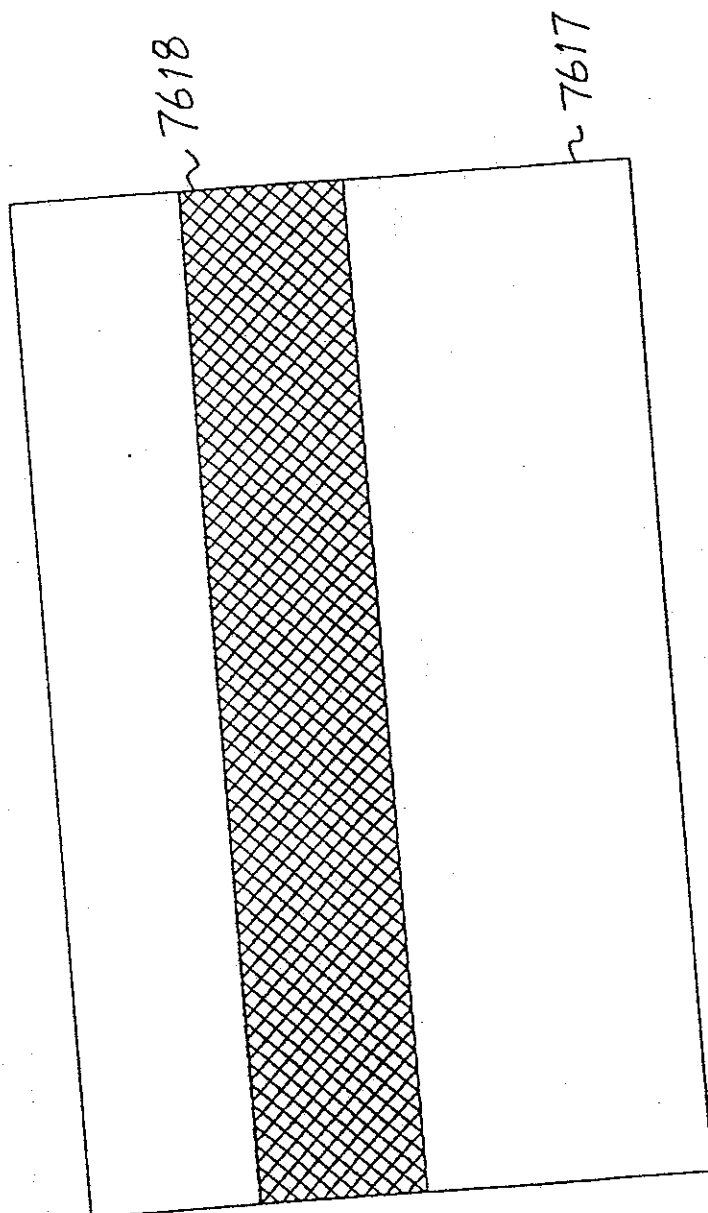


FIG. 337

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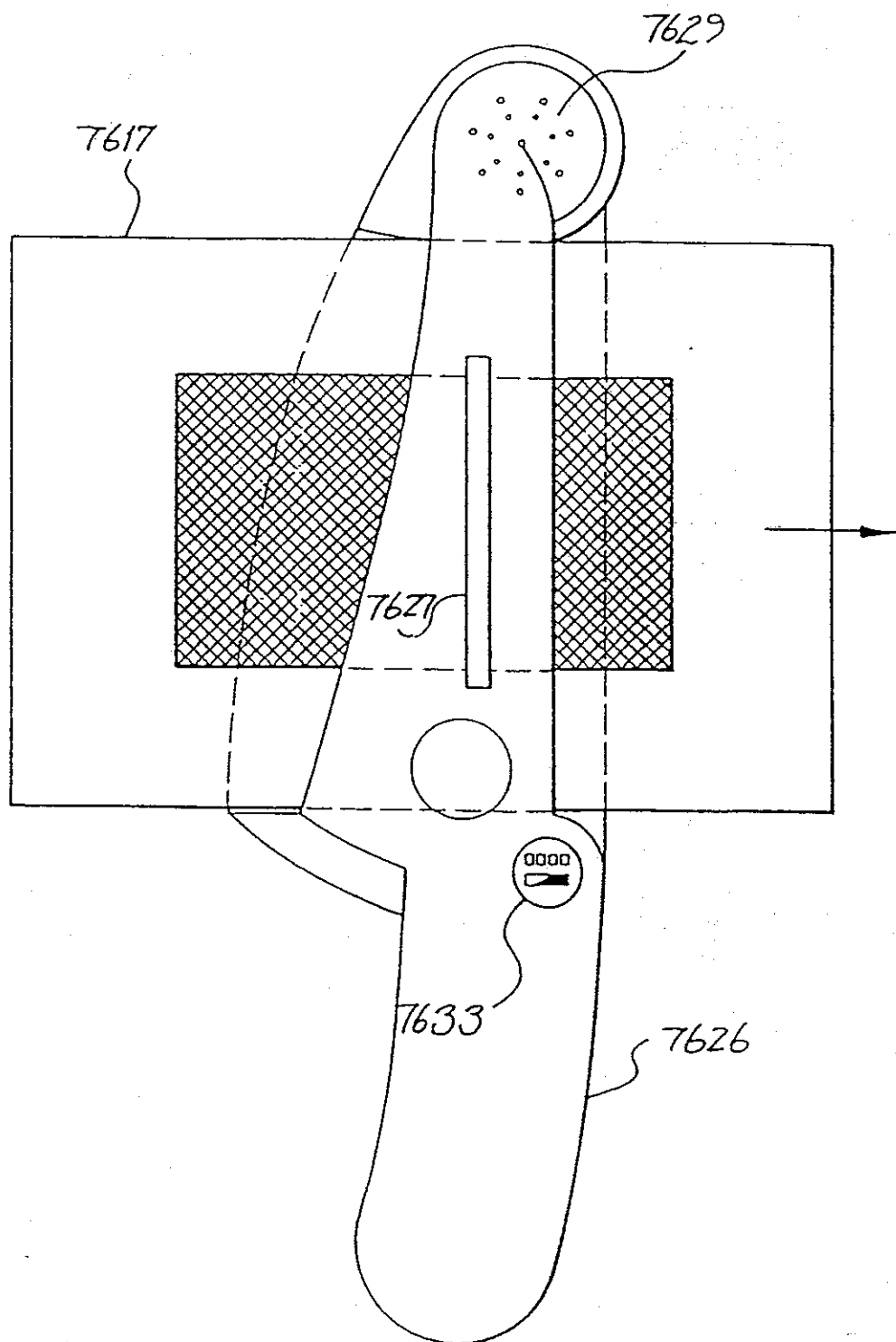


FIG. 338

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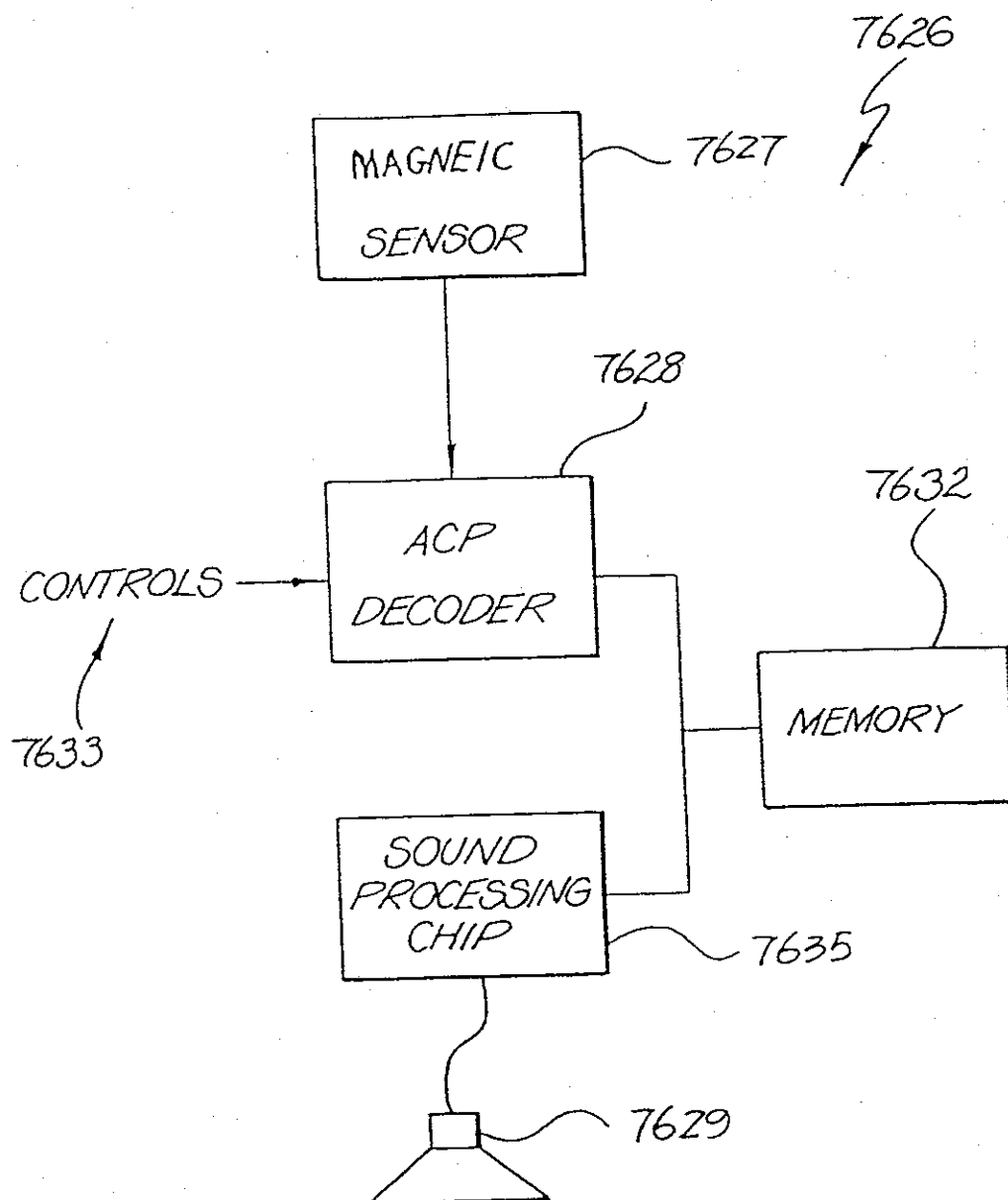


FIG. 339

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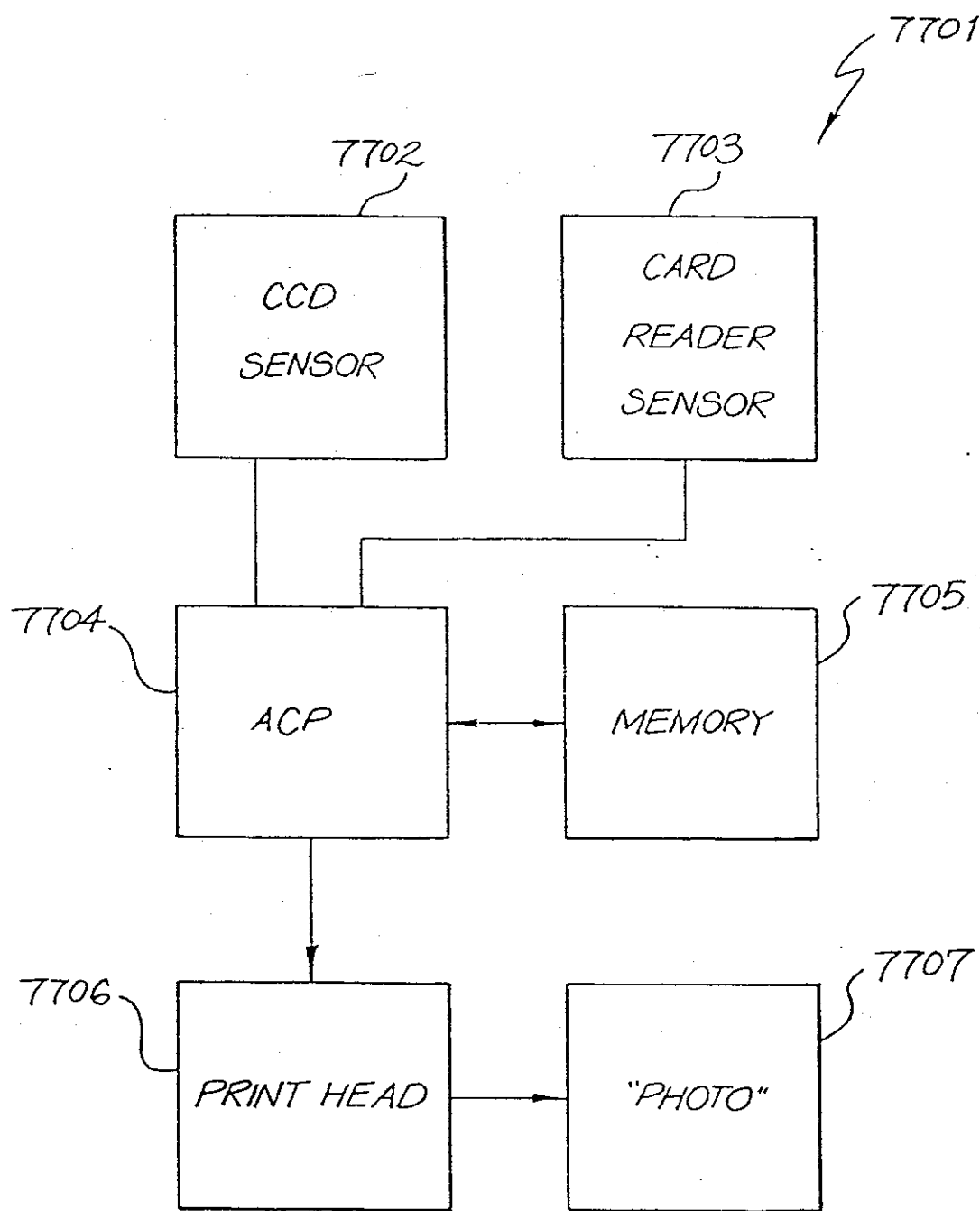


FIG. 340

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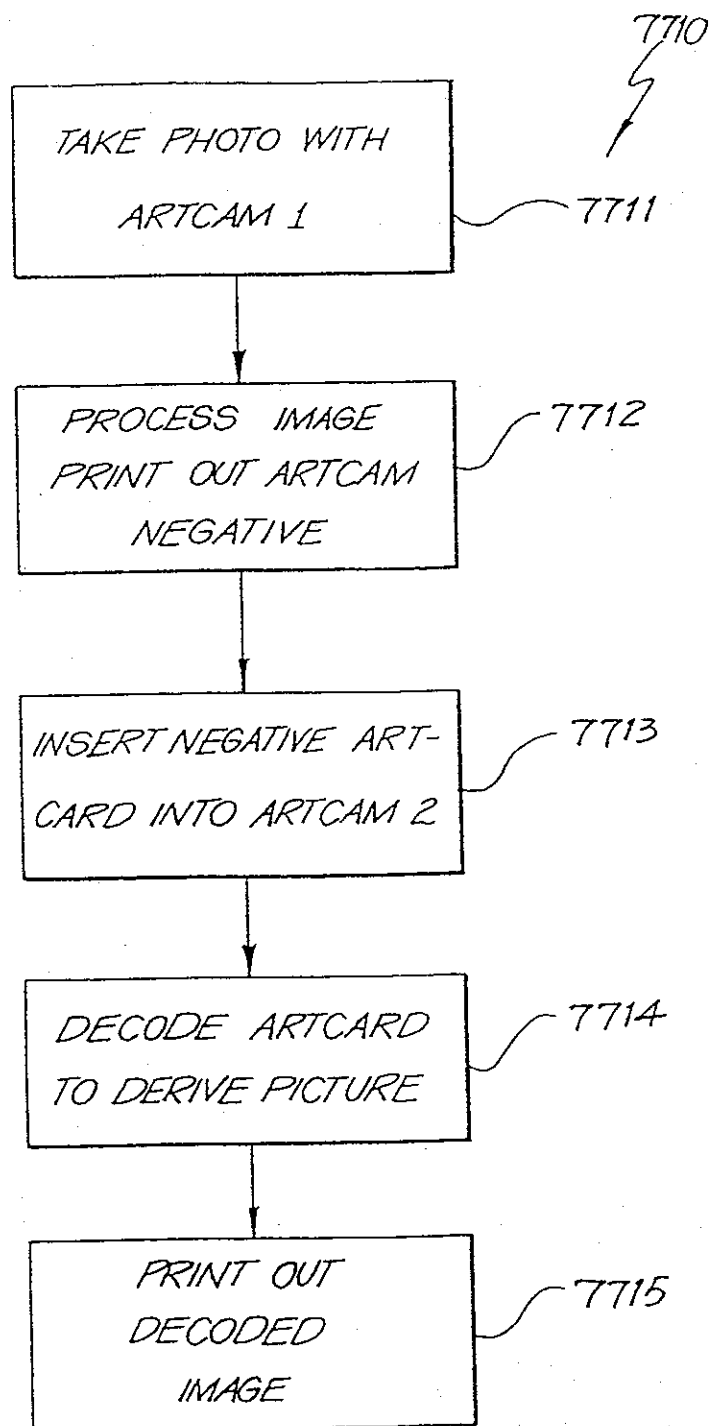


FIG. 341

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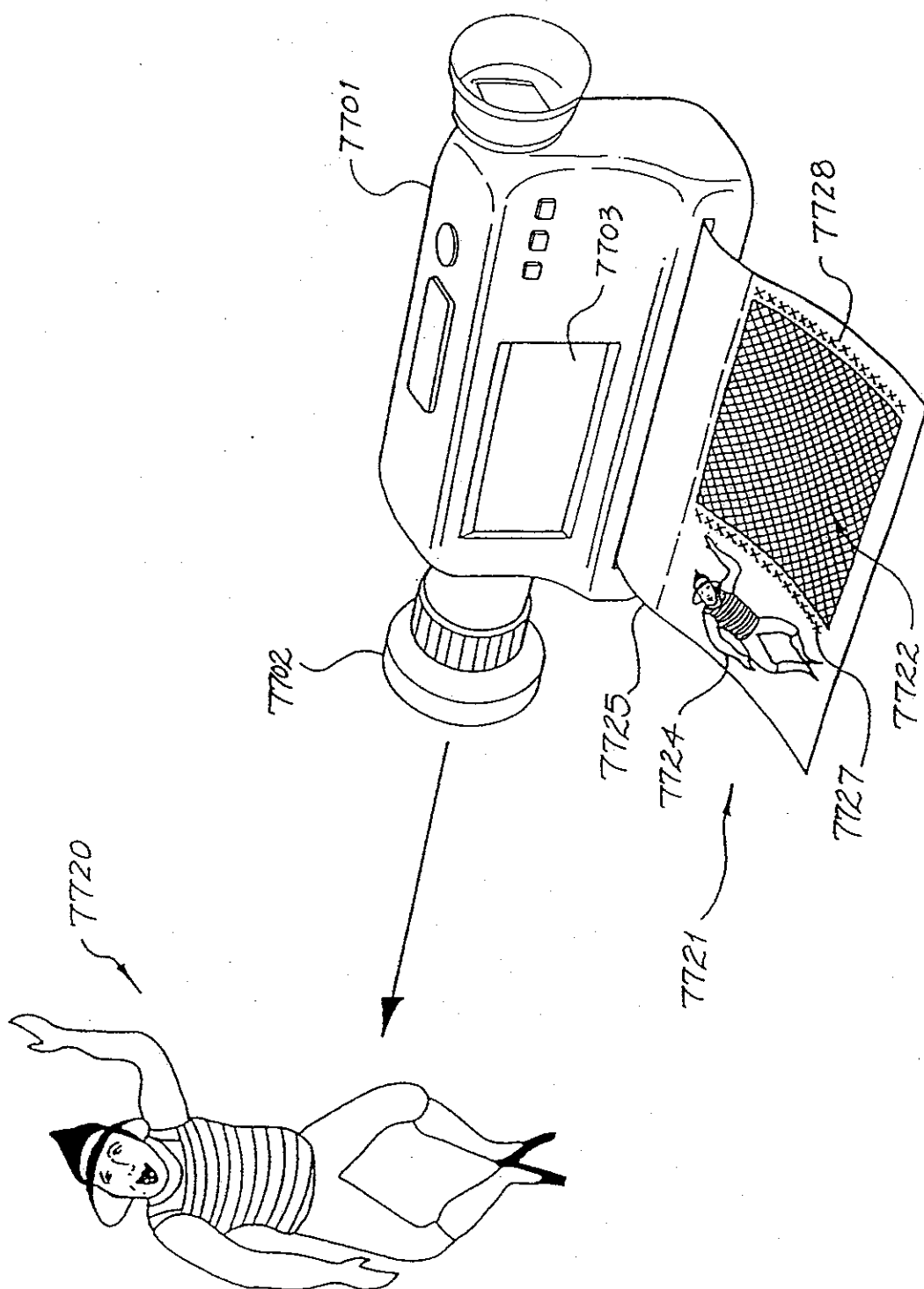


FIG. 342

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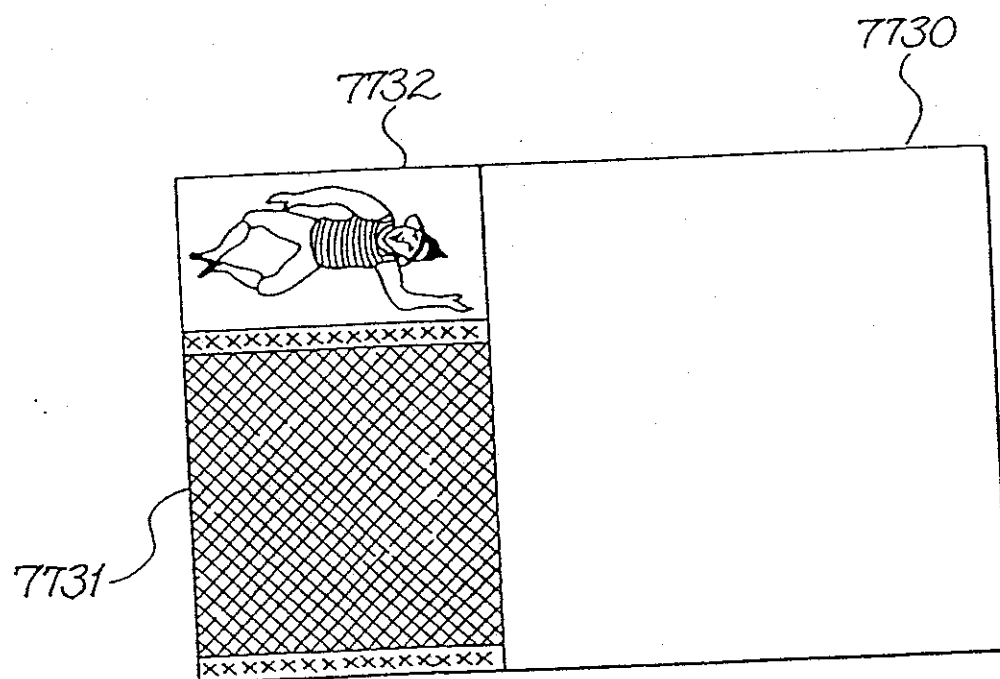
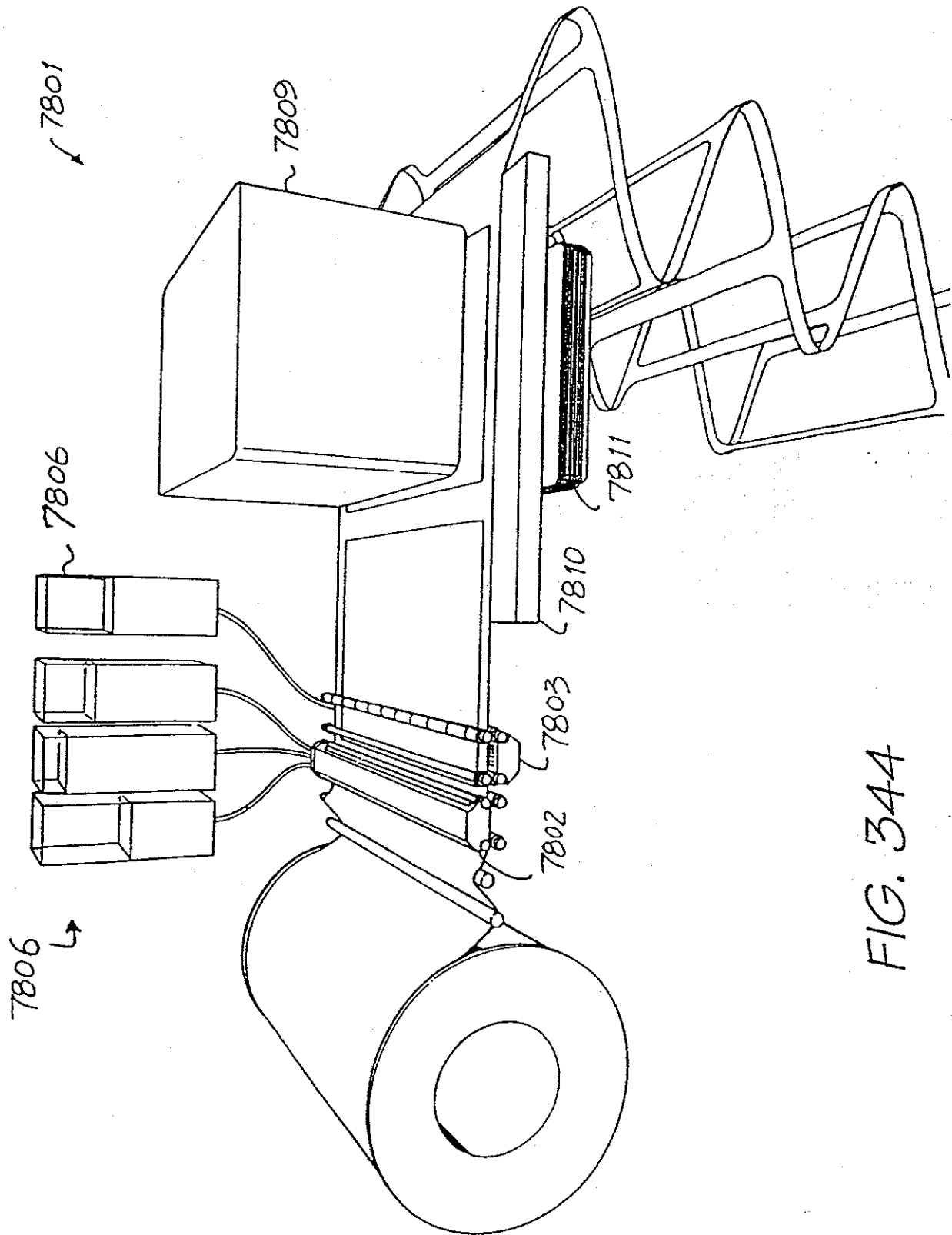


FIG. 343

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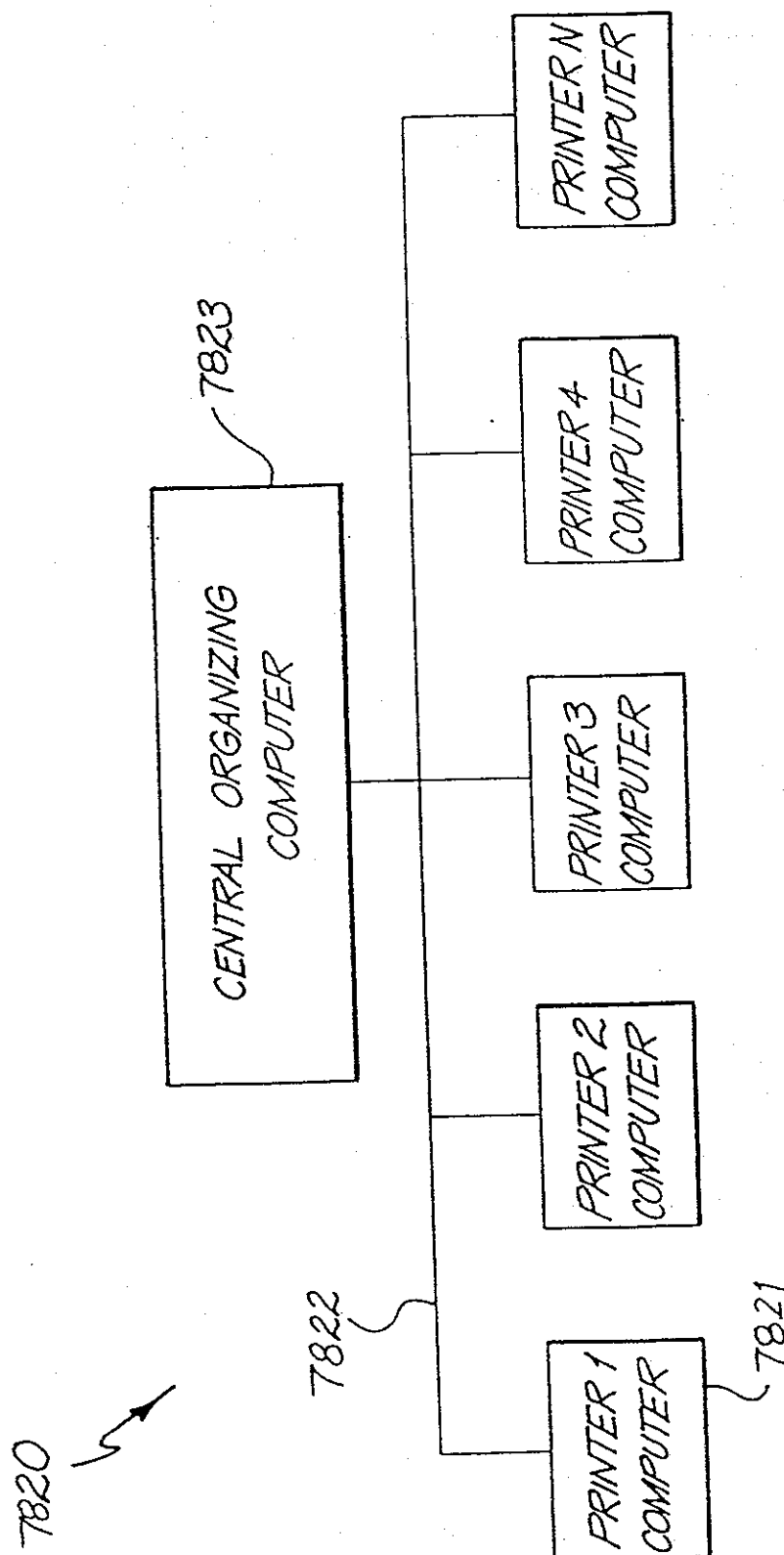


FIG. 345

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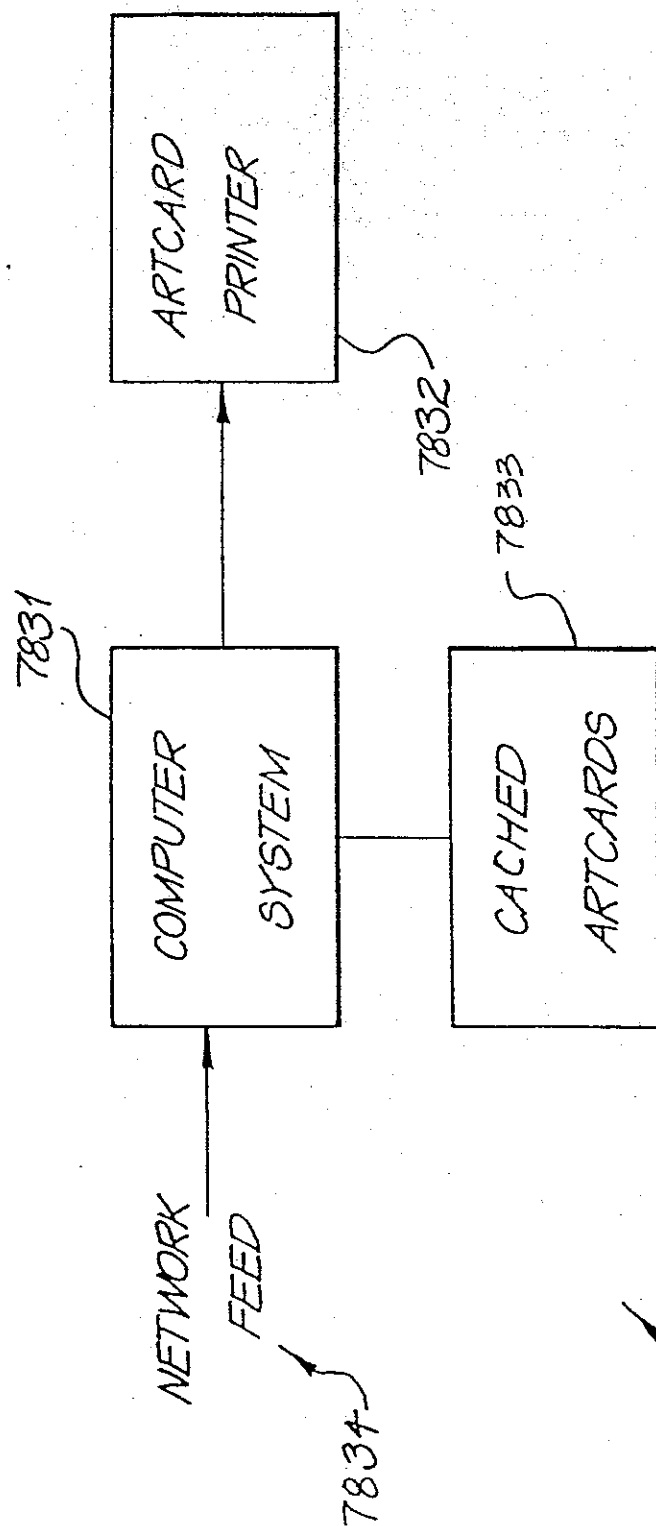


FIG. 346