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(54) Title: METHODS, APPARATUSES, AND SYSTEMS FOR IMPLEMENTING ORDERS WITH MINIMAL LATENCY

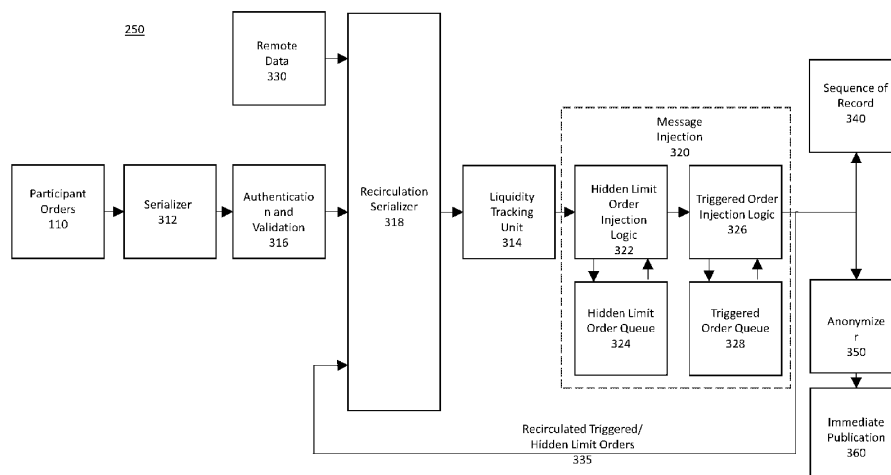


FIG. 3A

(57) Abstract: Systems and methods for matching orders across a plurality of input ports are disclosed herein. A system may include a plurality of input ports, a processor, and a storage medium. The processor may be configured to receive a plurality of data packets from the input ports, defining a first message and a second message, wherein the first and second messages have a message type. The processor may be configured to sequence the first and second messages into a sequence of record based on one or more arbitration rules; evaluate the first message and the second message based on the sequence of record to generate matching information; and publish, over the network interface, the matching information. The processor may be further configured to perform immediate publication of the sequence of record.

[Continued on next page]

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