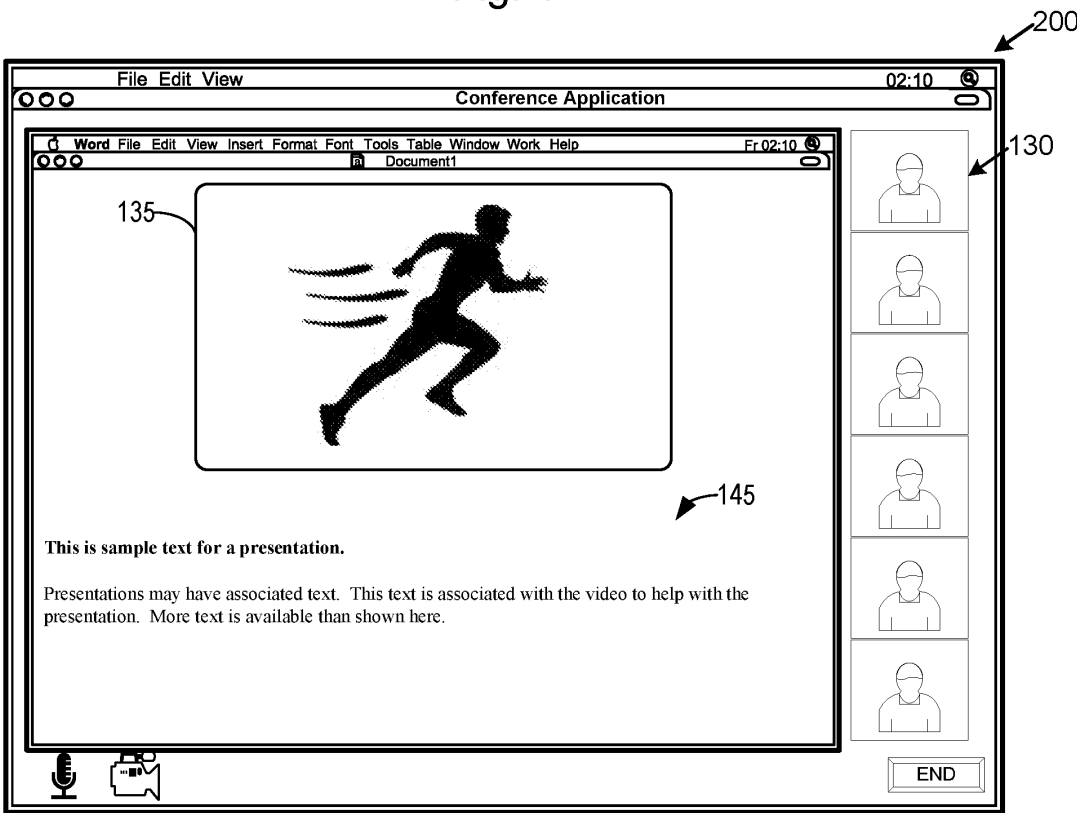
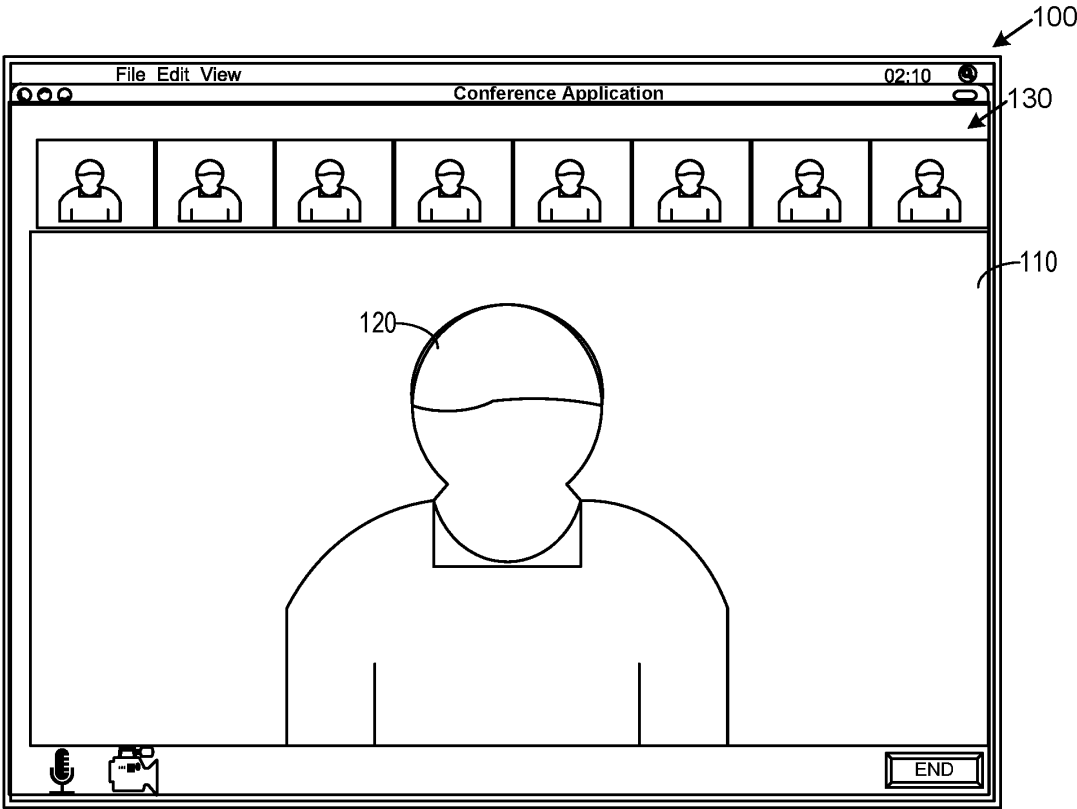




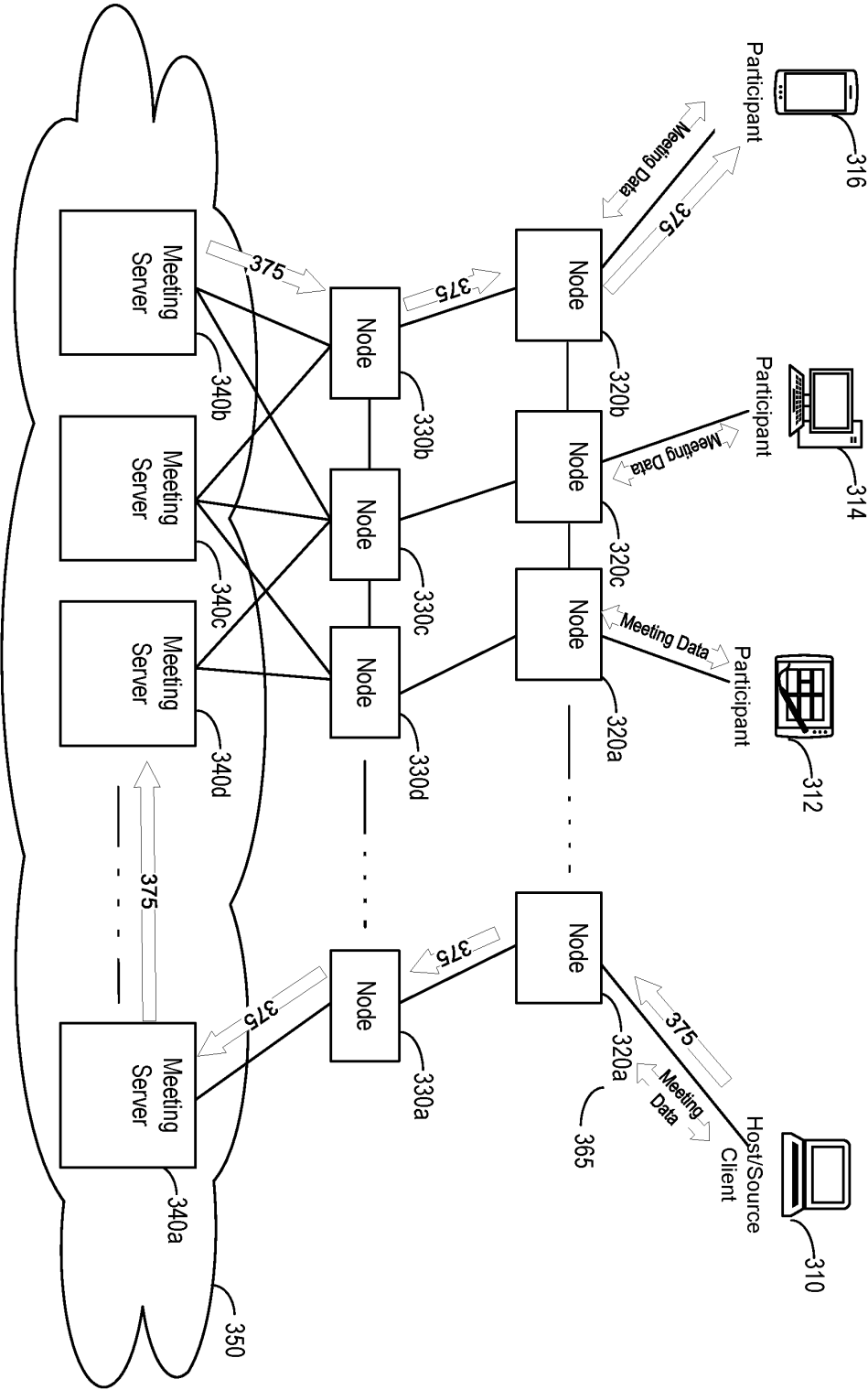


Fig. 3

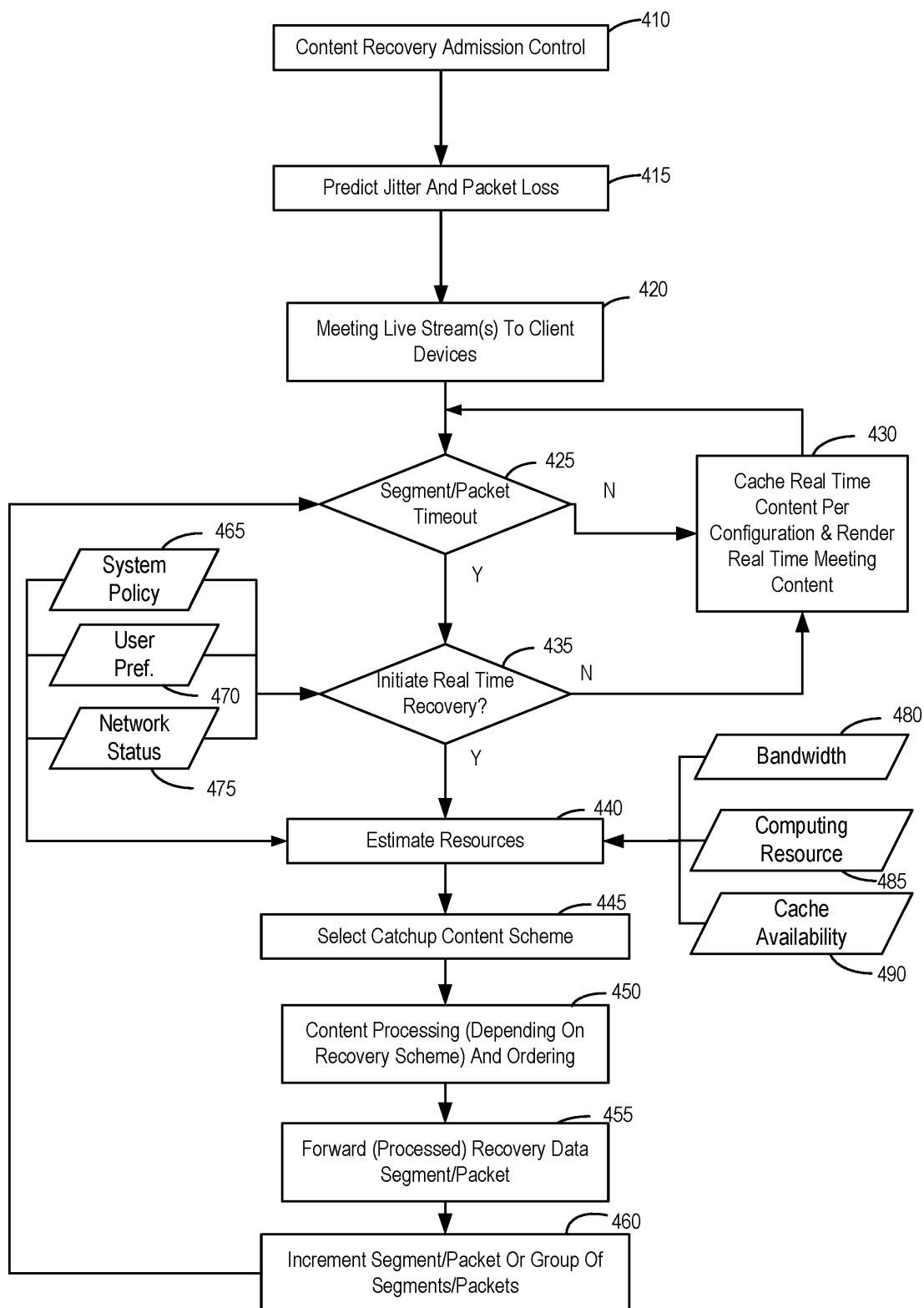


Fig. 4

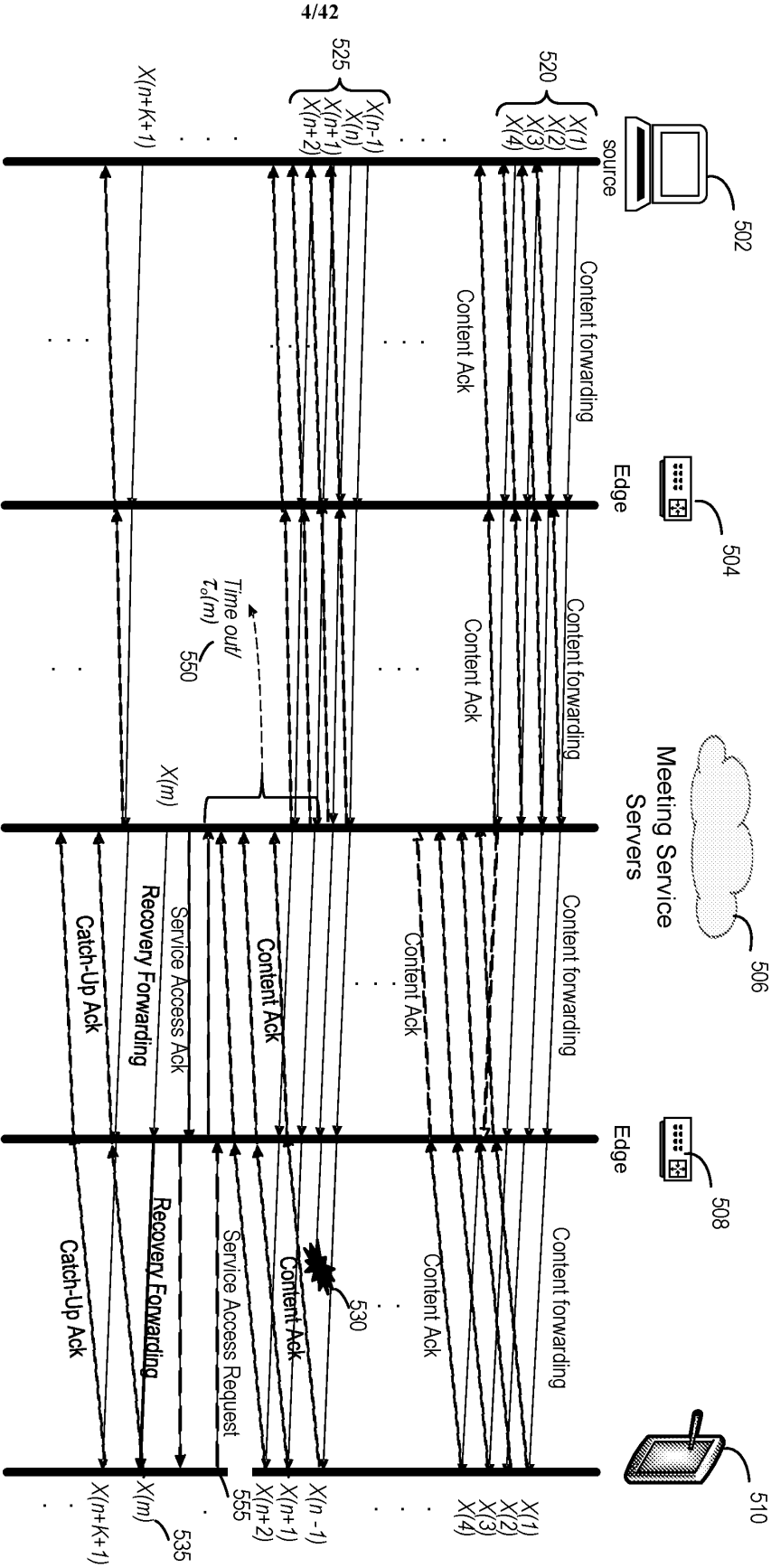


Fig. 5

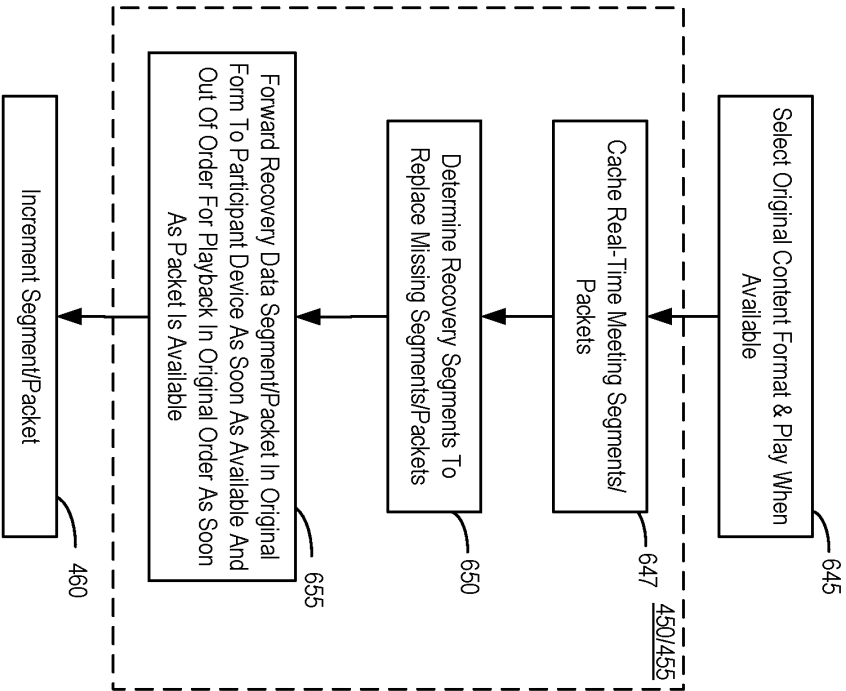


Fig. 6

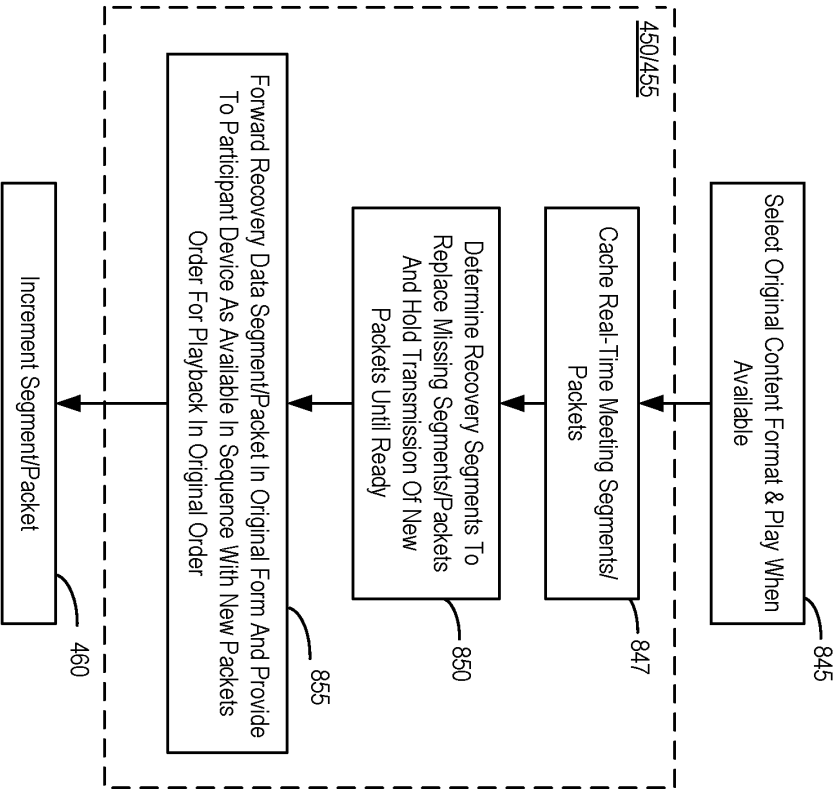


Fig. 8

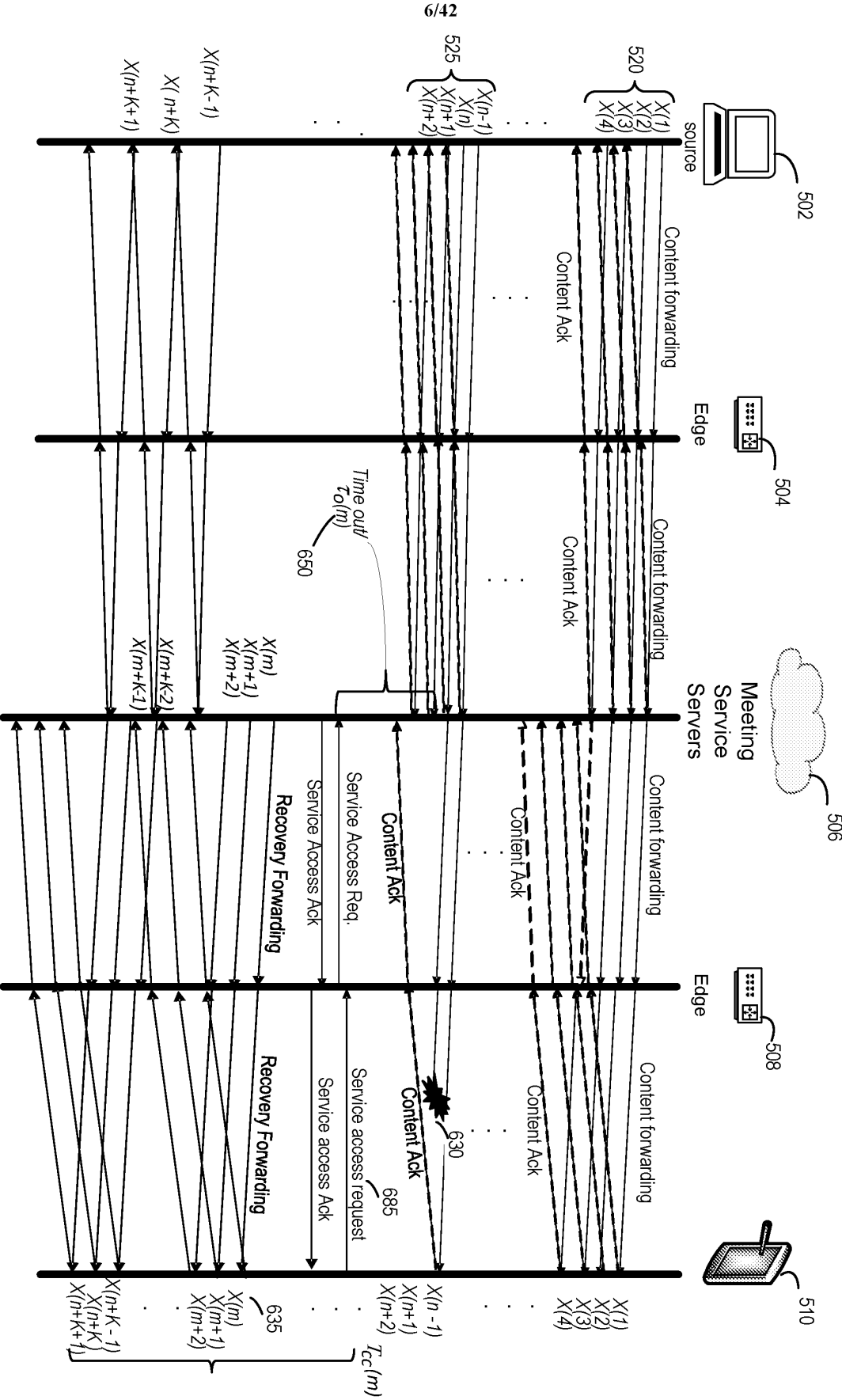


Fig. 7

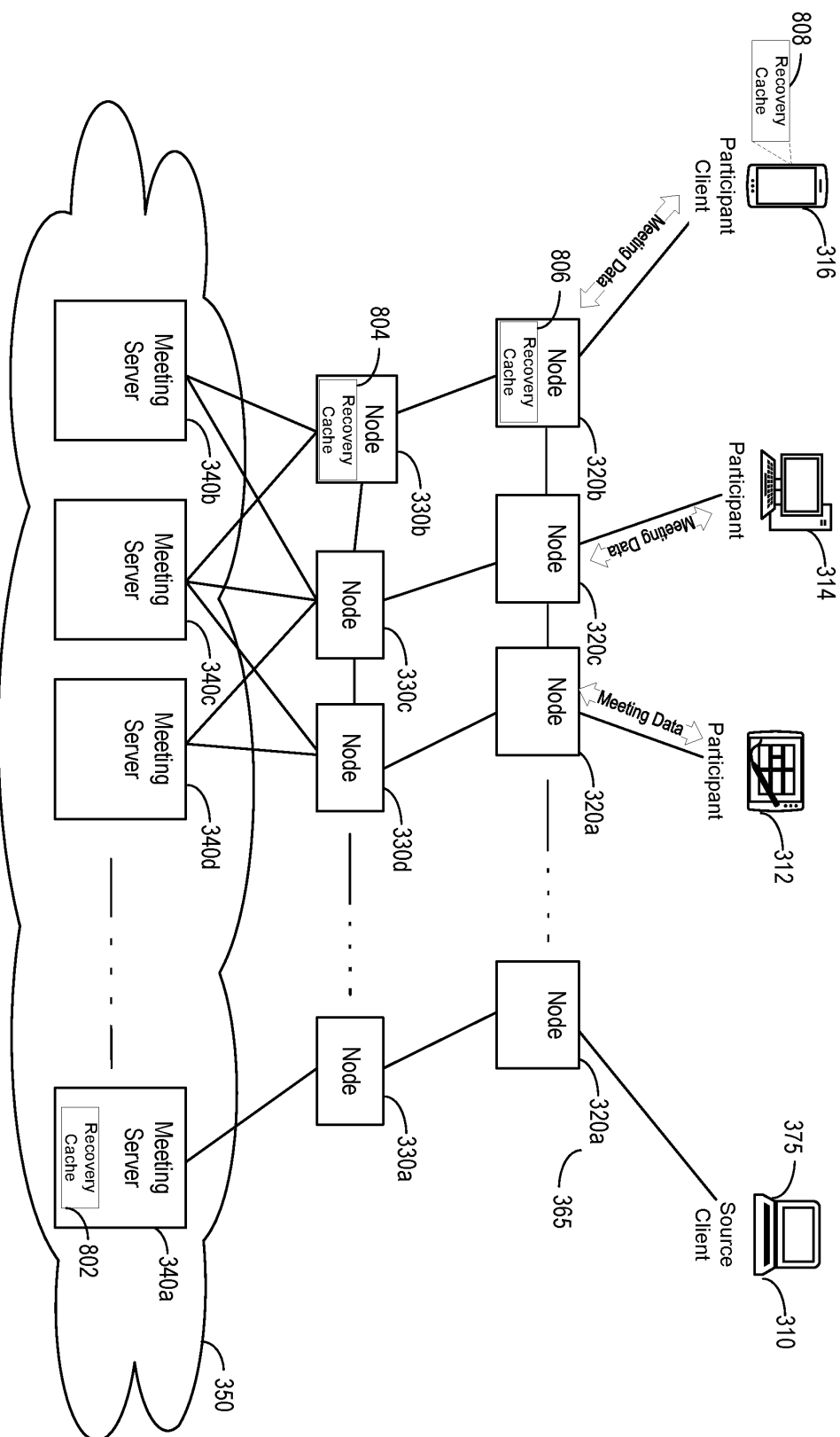
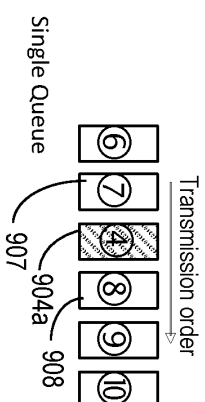
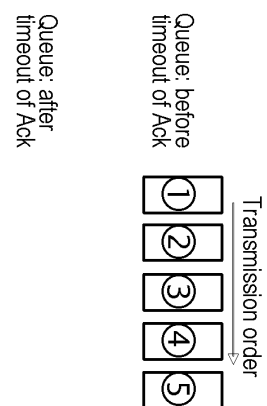
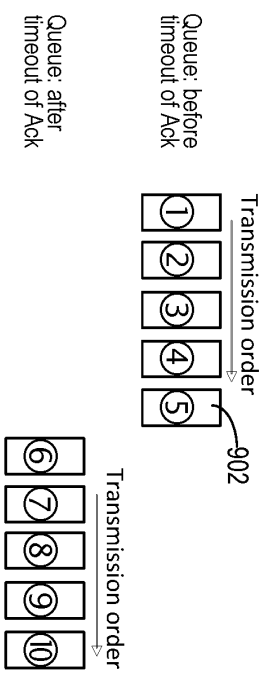
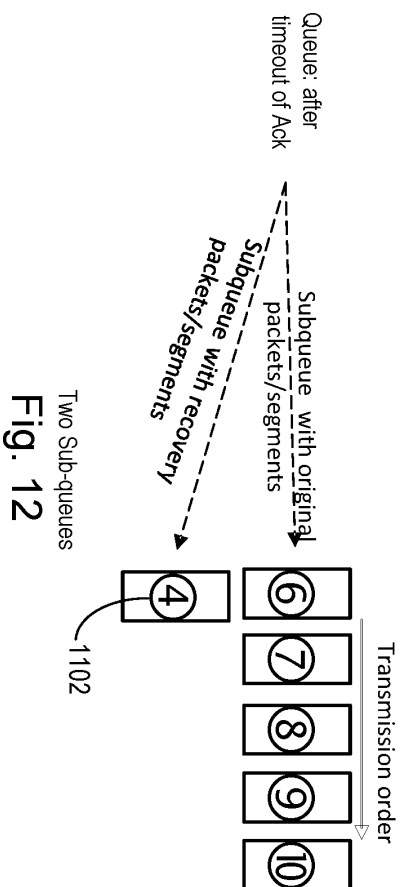
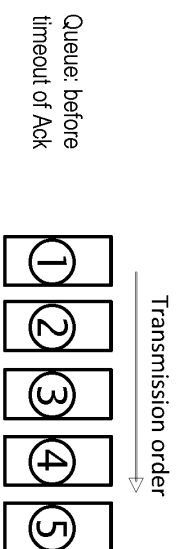


Fig. 9



Single Queue
Fig. 10
(Prior Art)



Two Sub-queues
Fig. 12

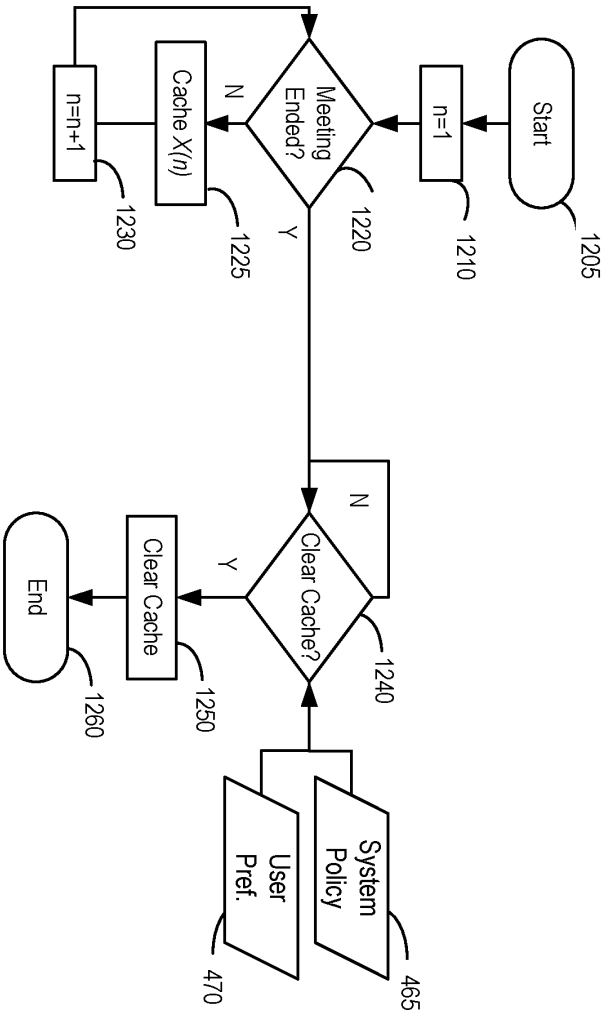


Fig. 13

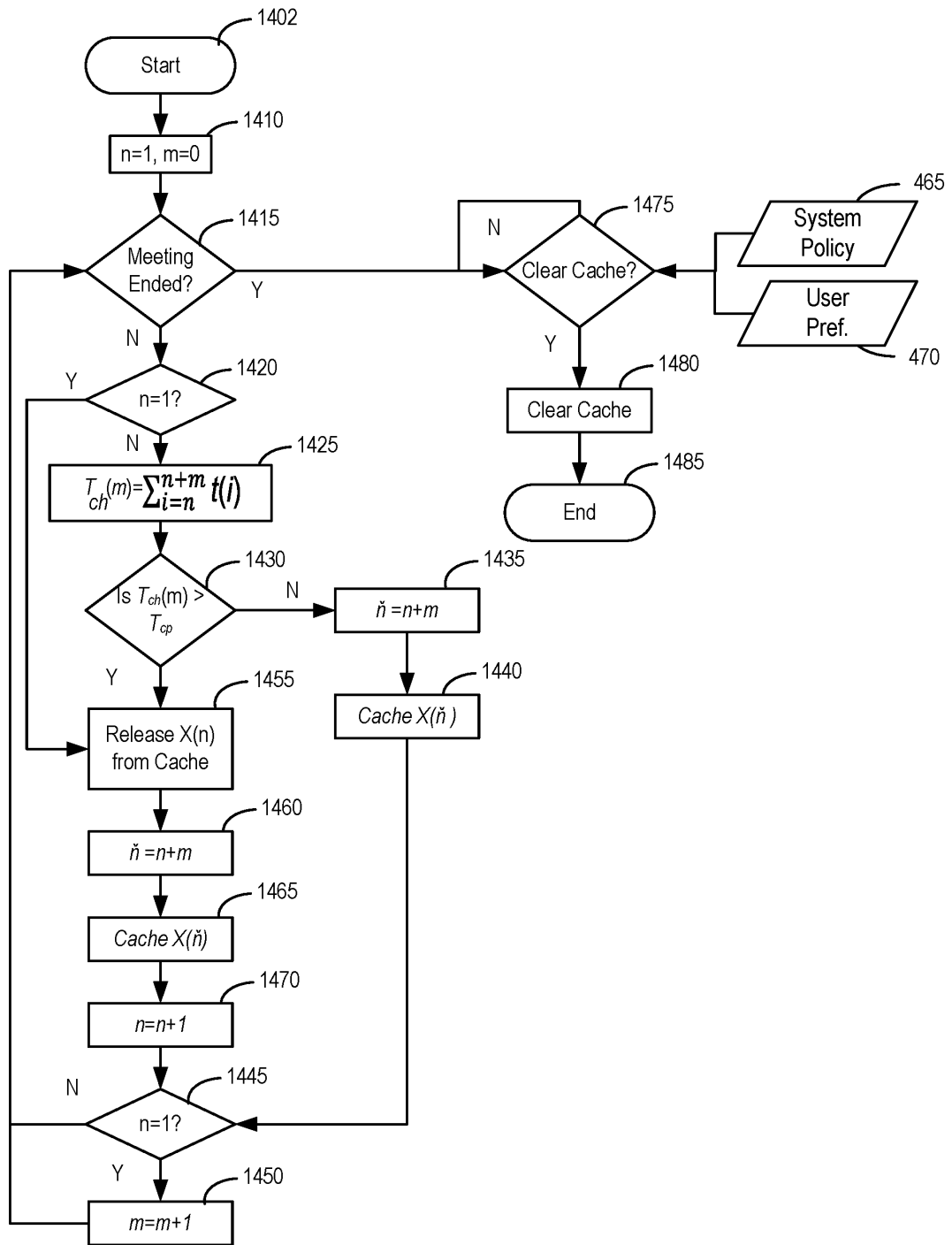


Fig. 14

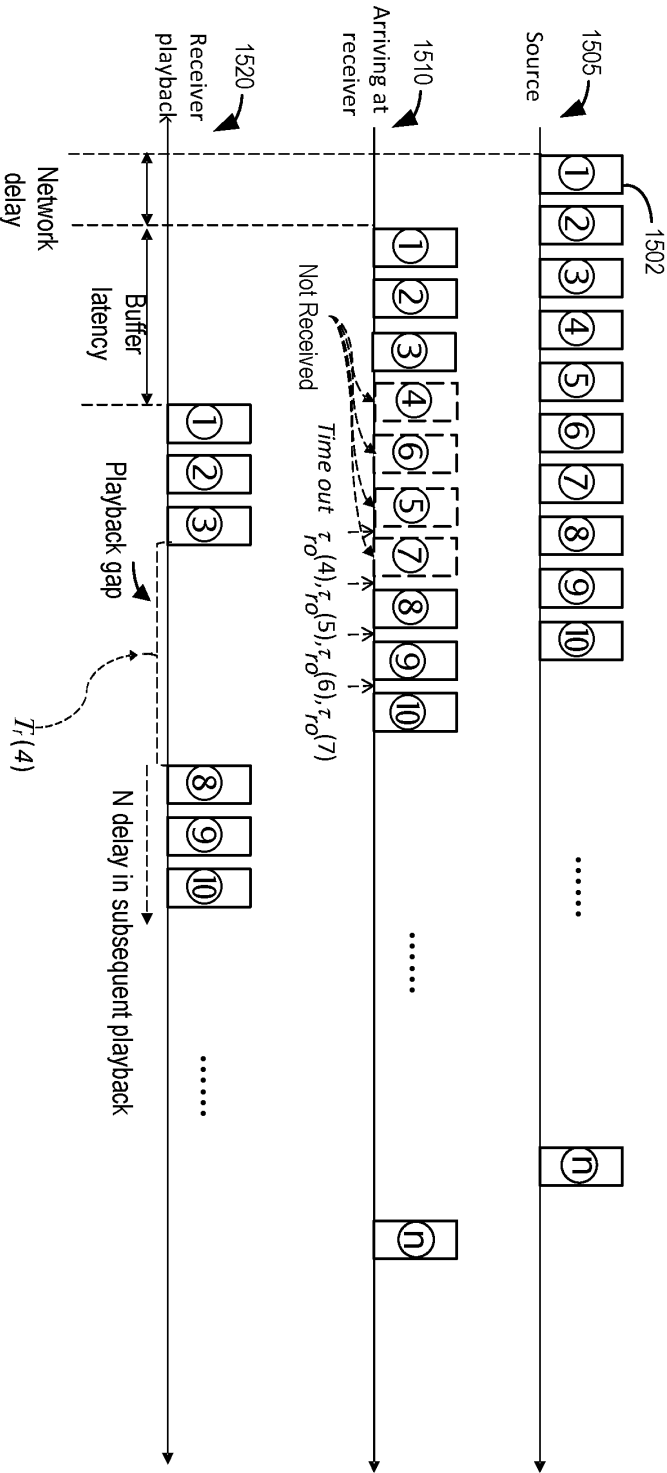


Fig. 15
(prior art)

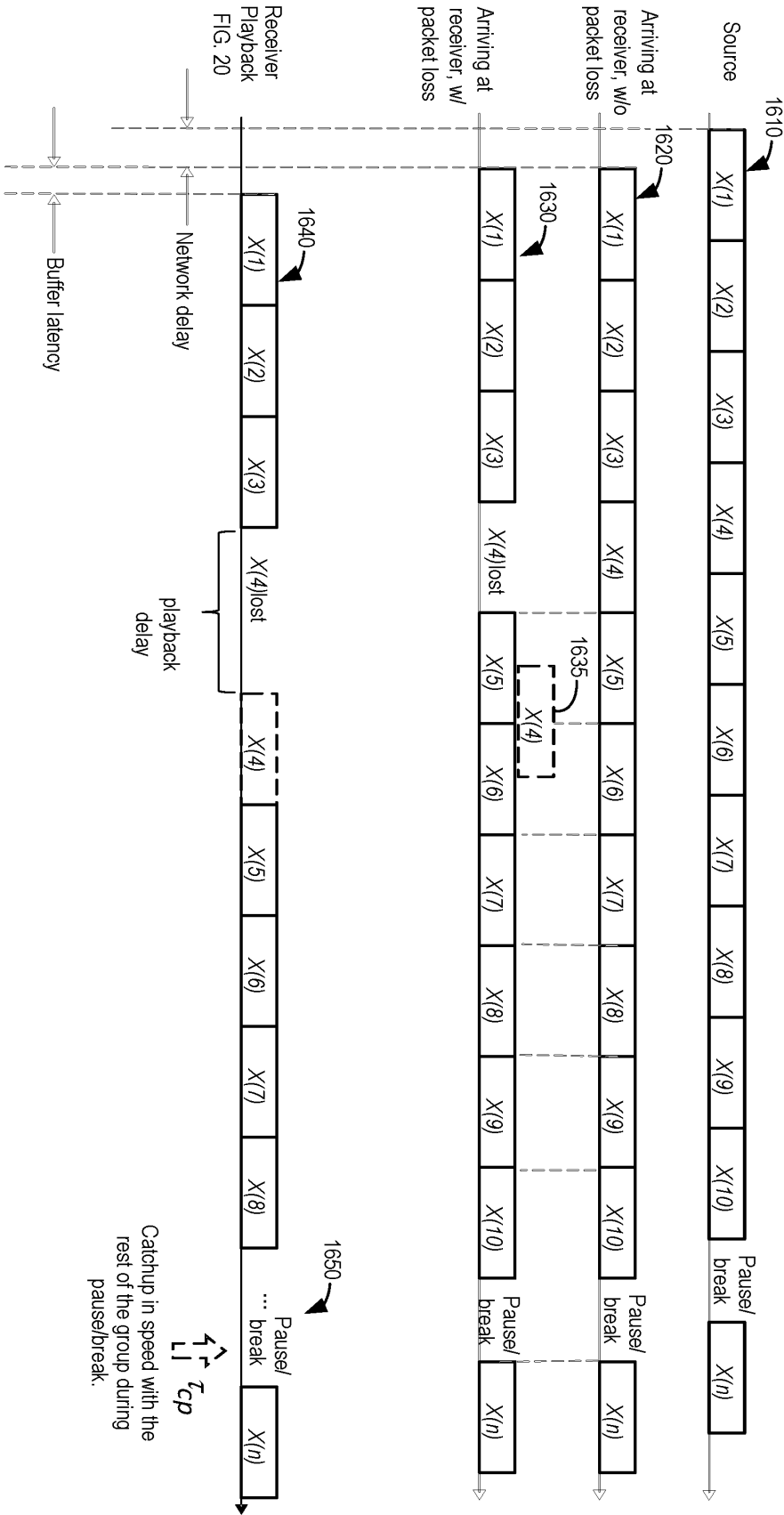


Fig. 16

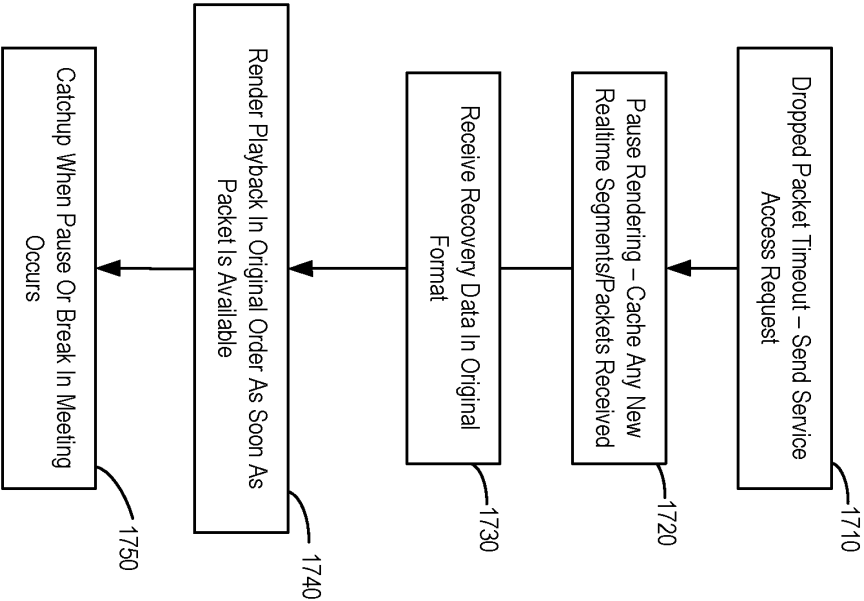


Fig. 17

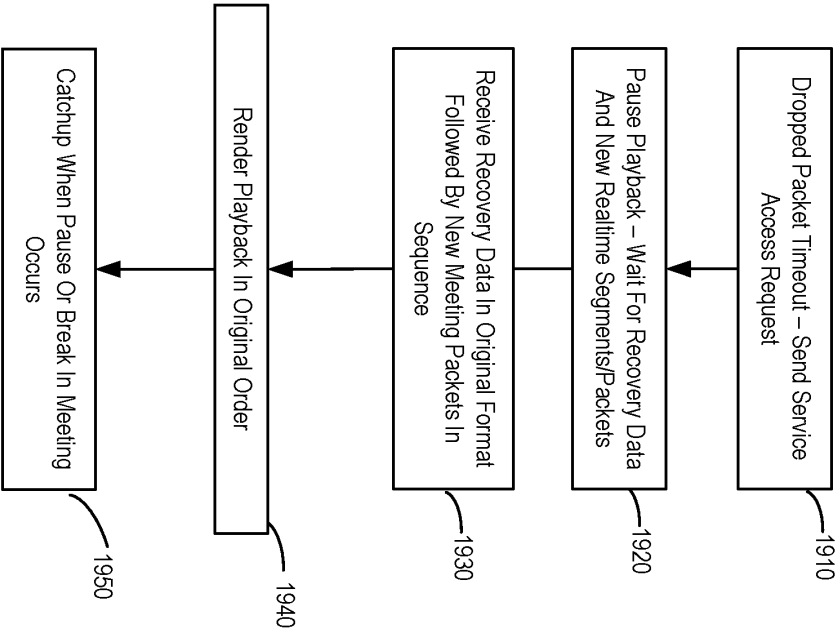


Fig. 19

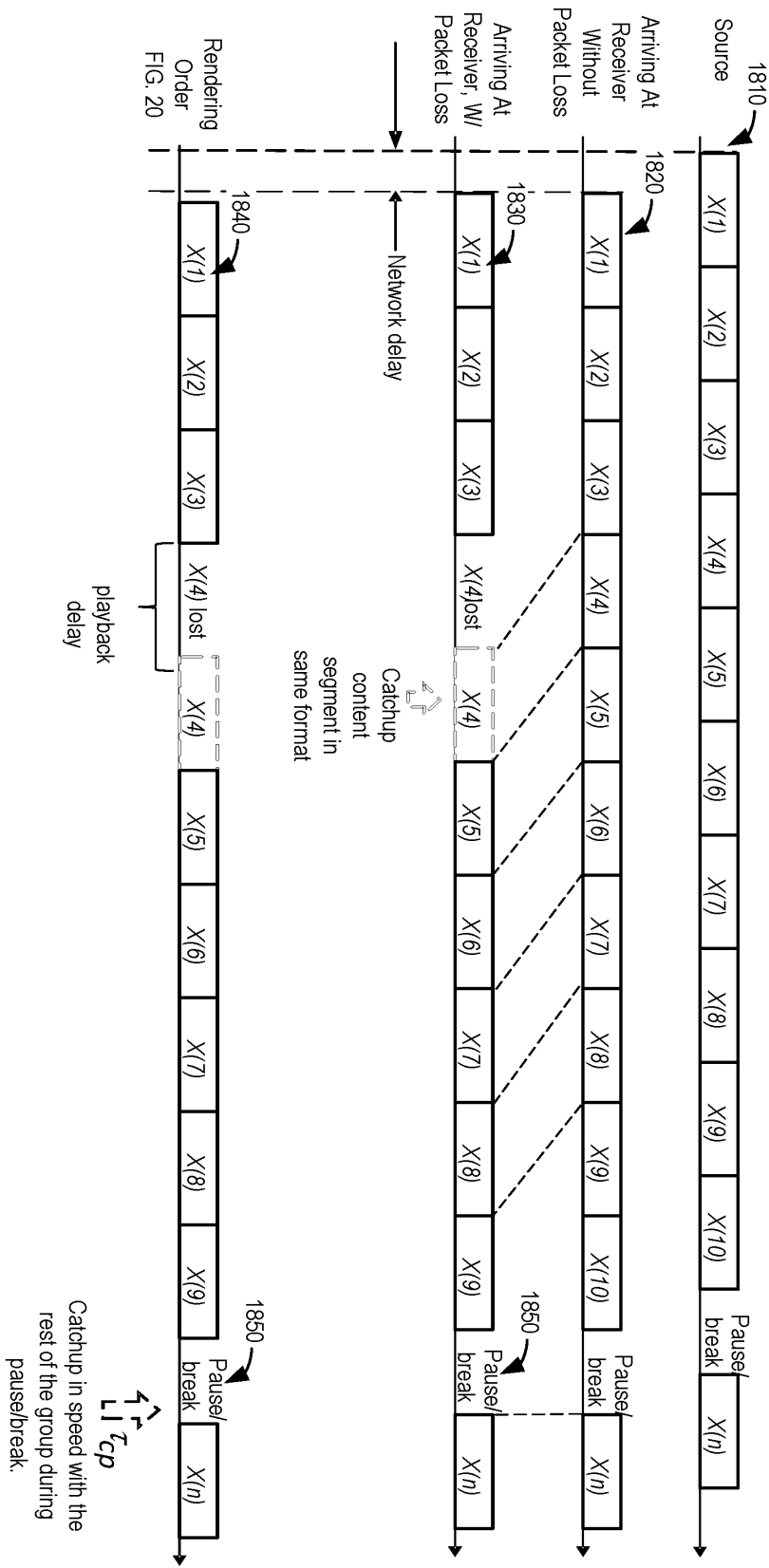


Fig. 18

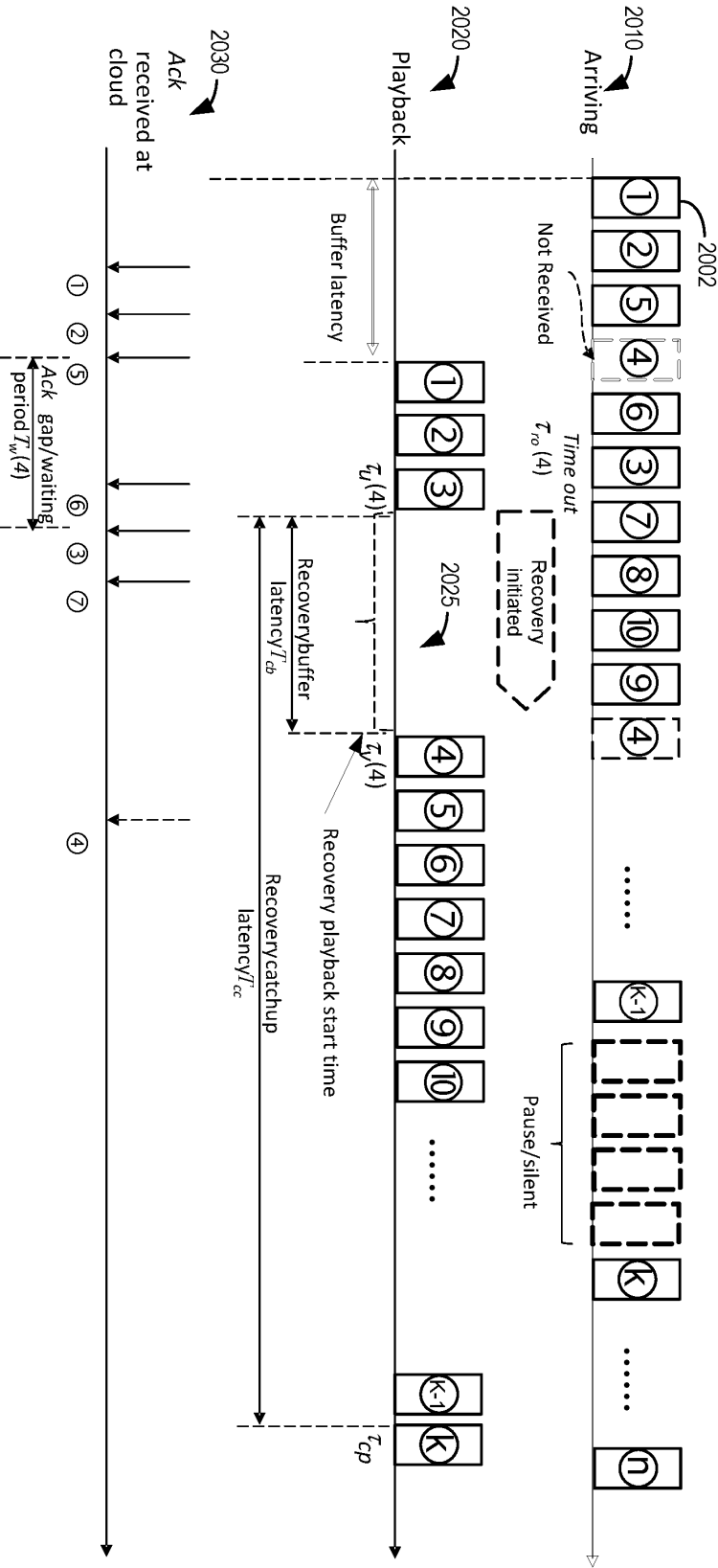


Fig. 20

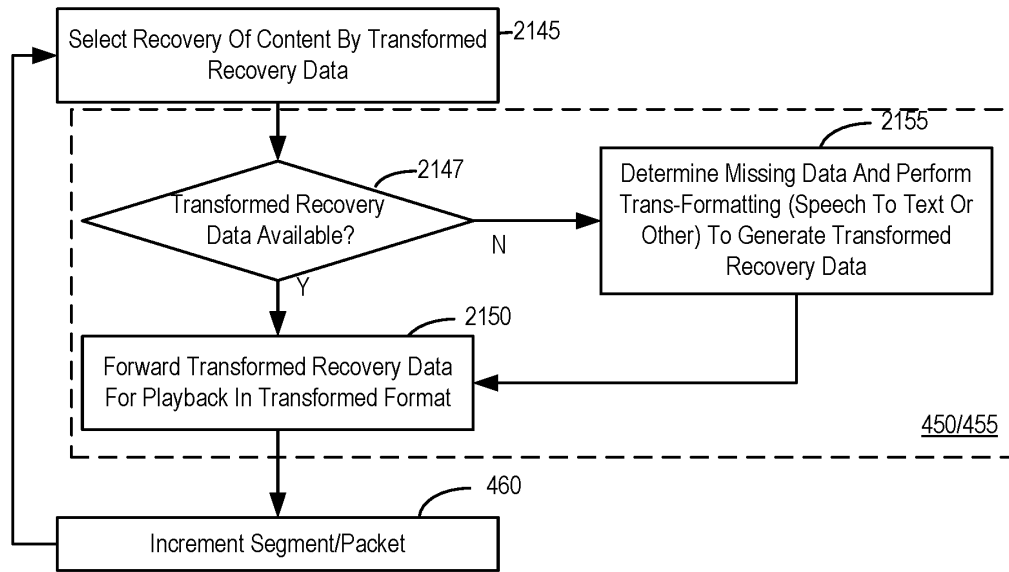


Fig. 21

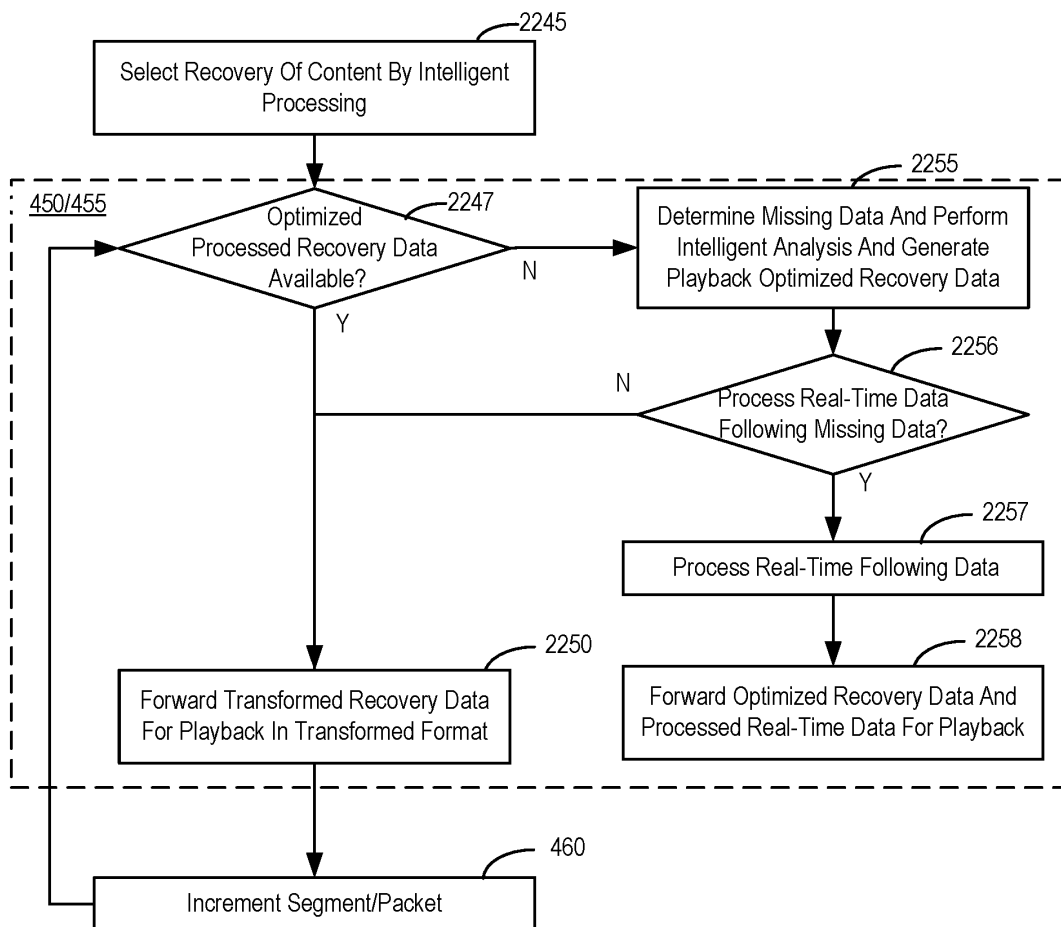


Fig. 22

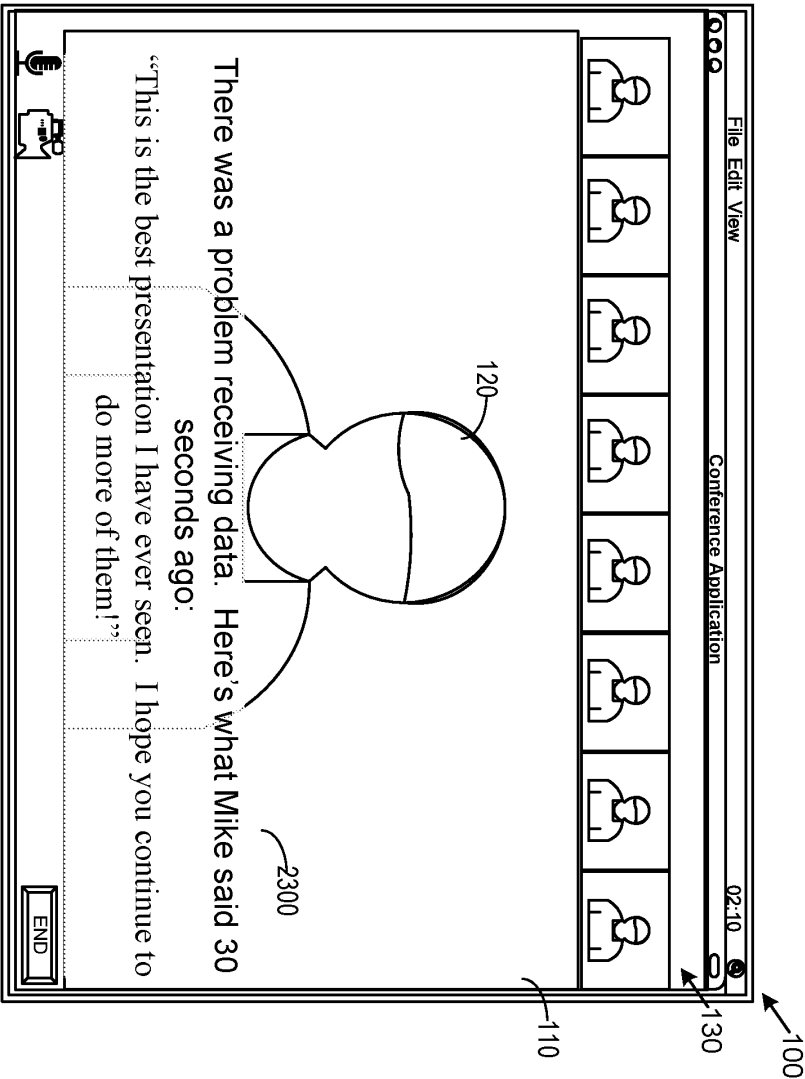


Fig. 23

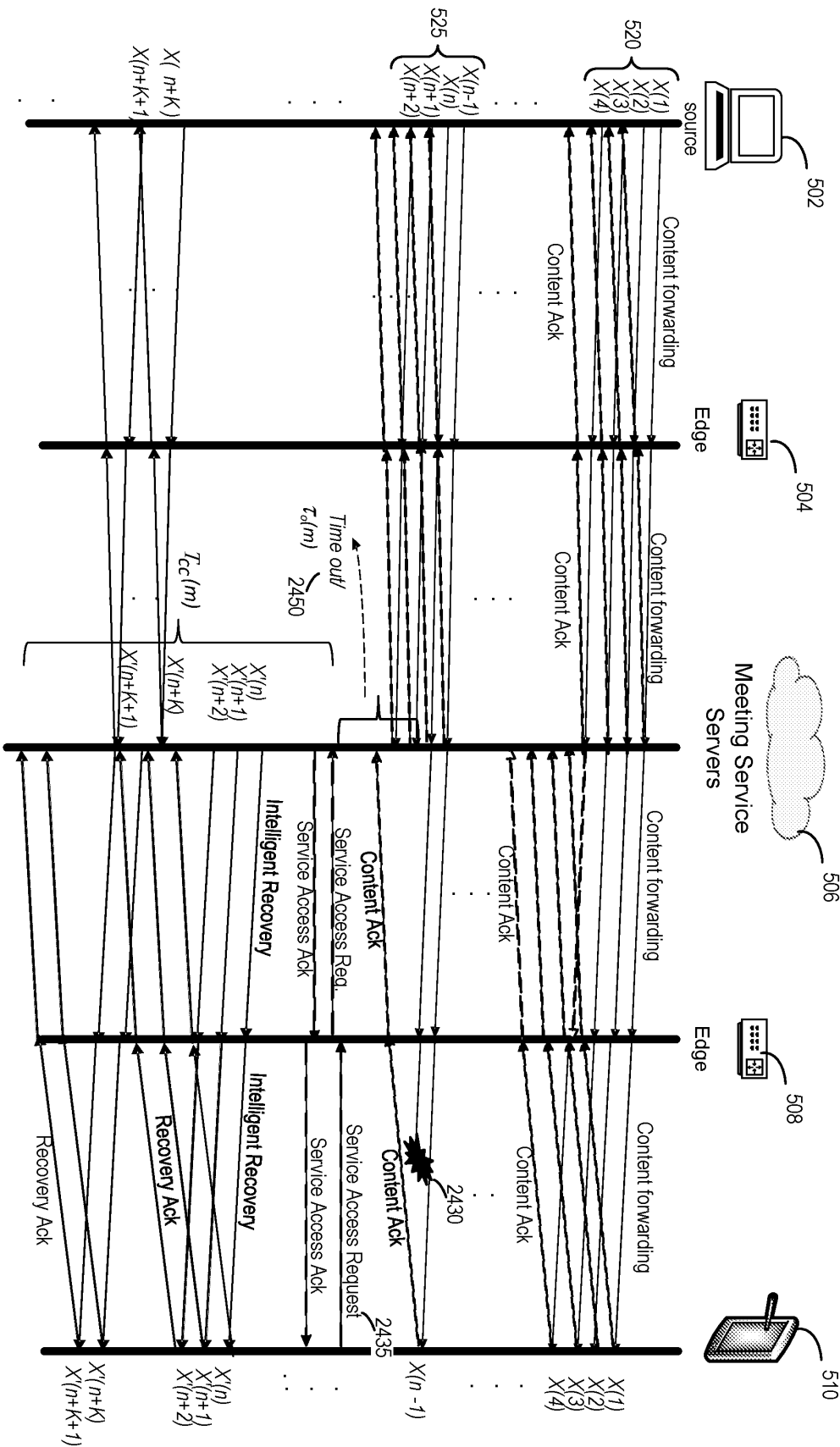


Fig. 24

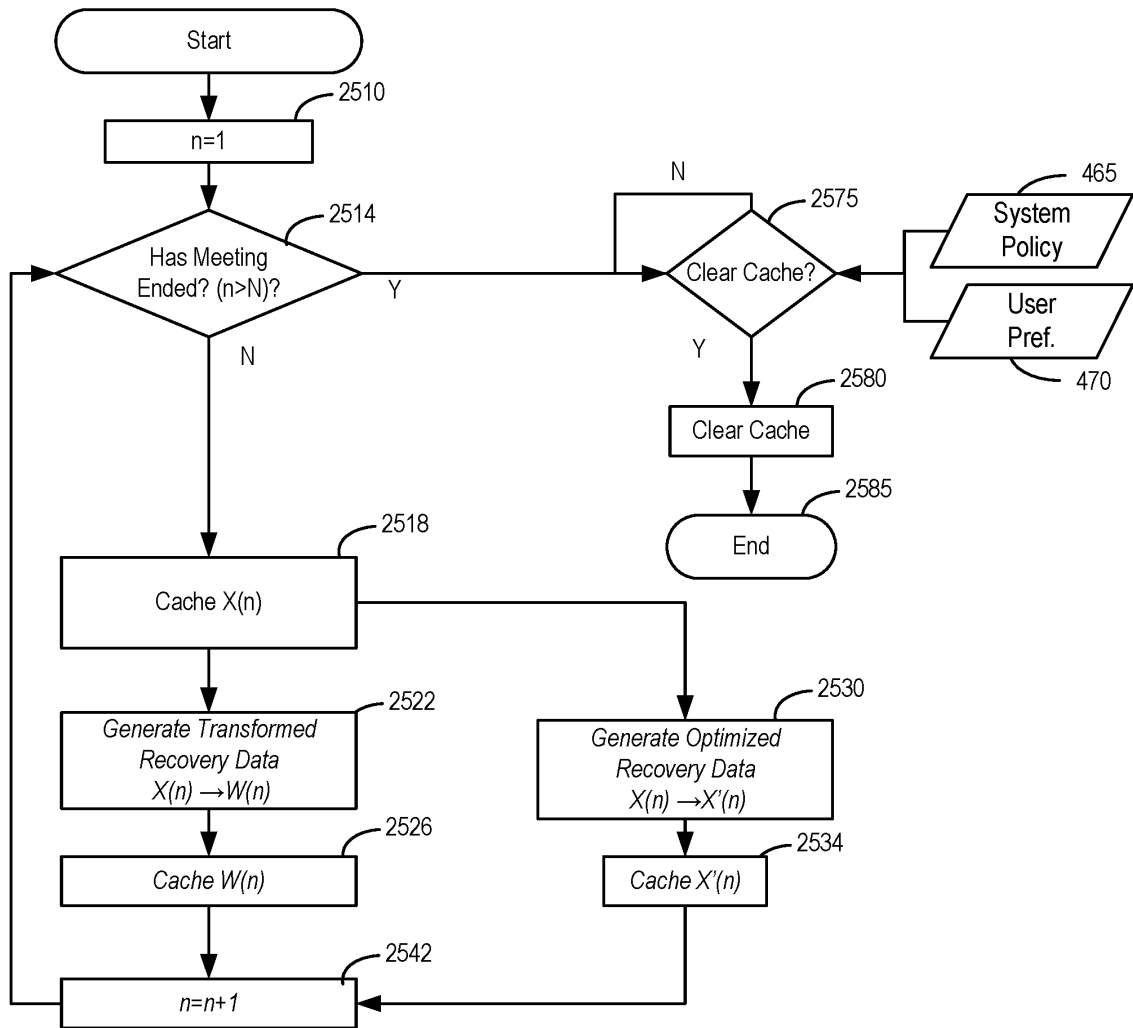
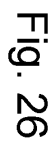


Fig. 25



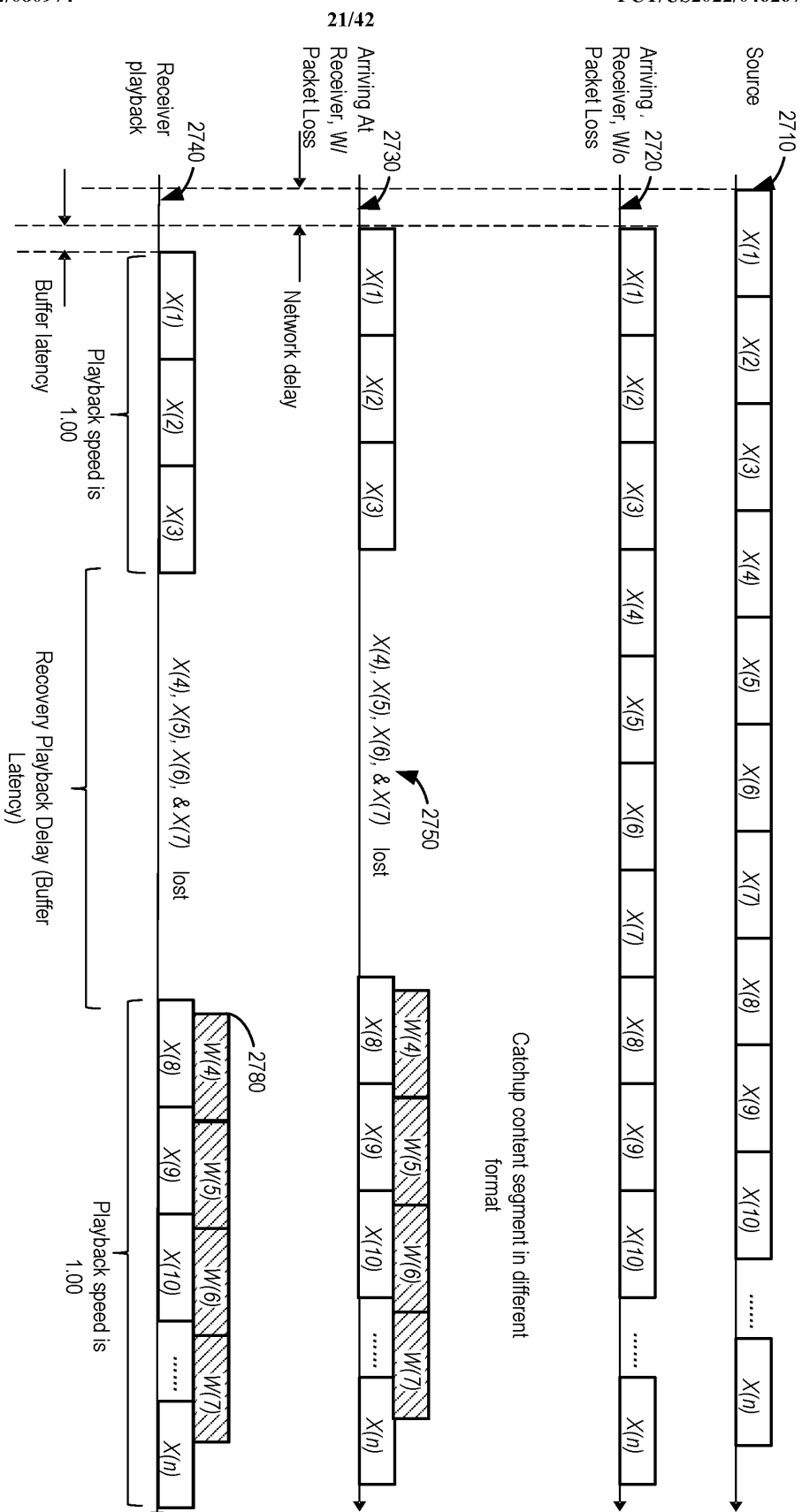


Fig. 27

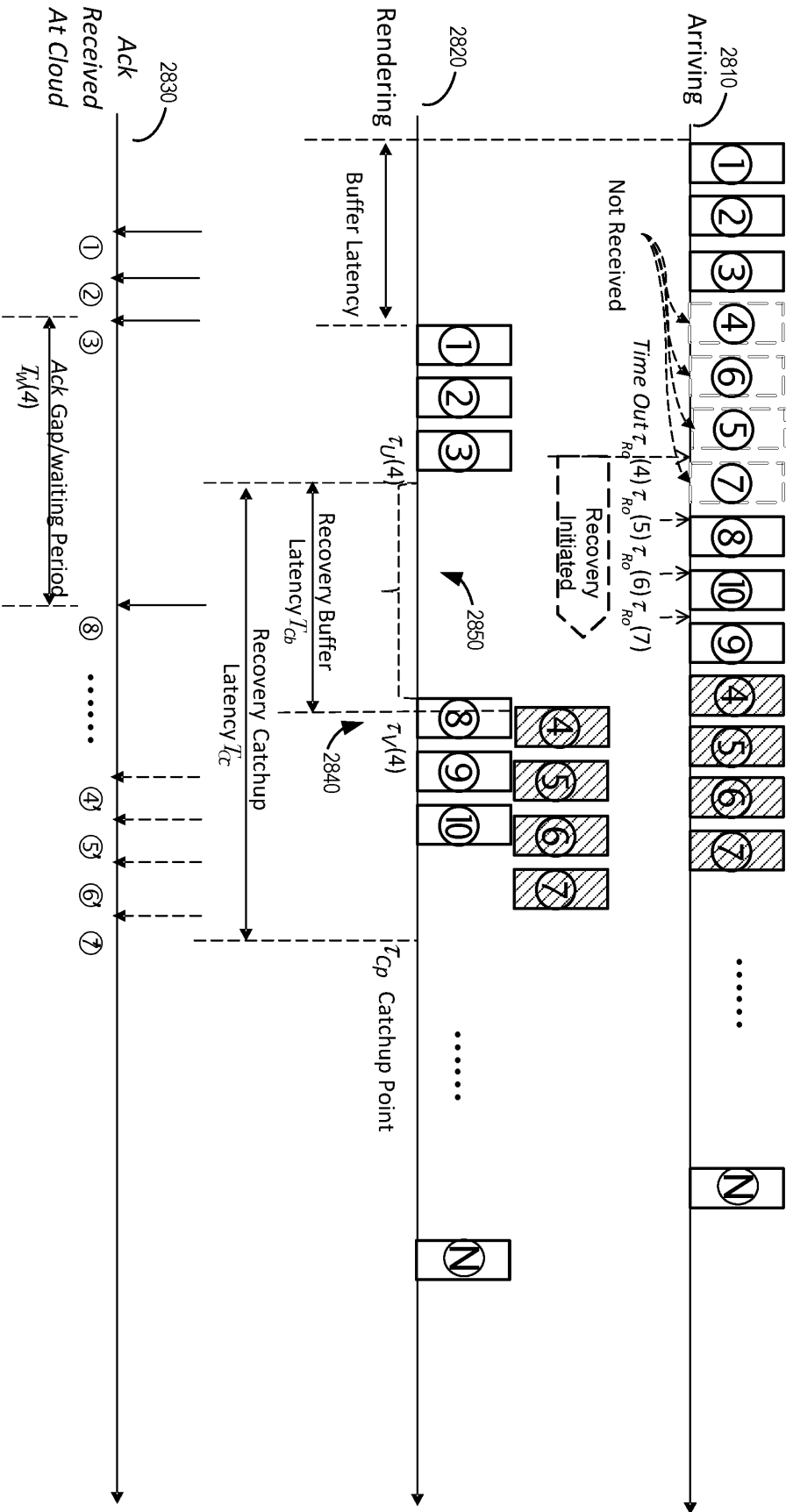


Fig. 28

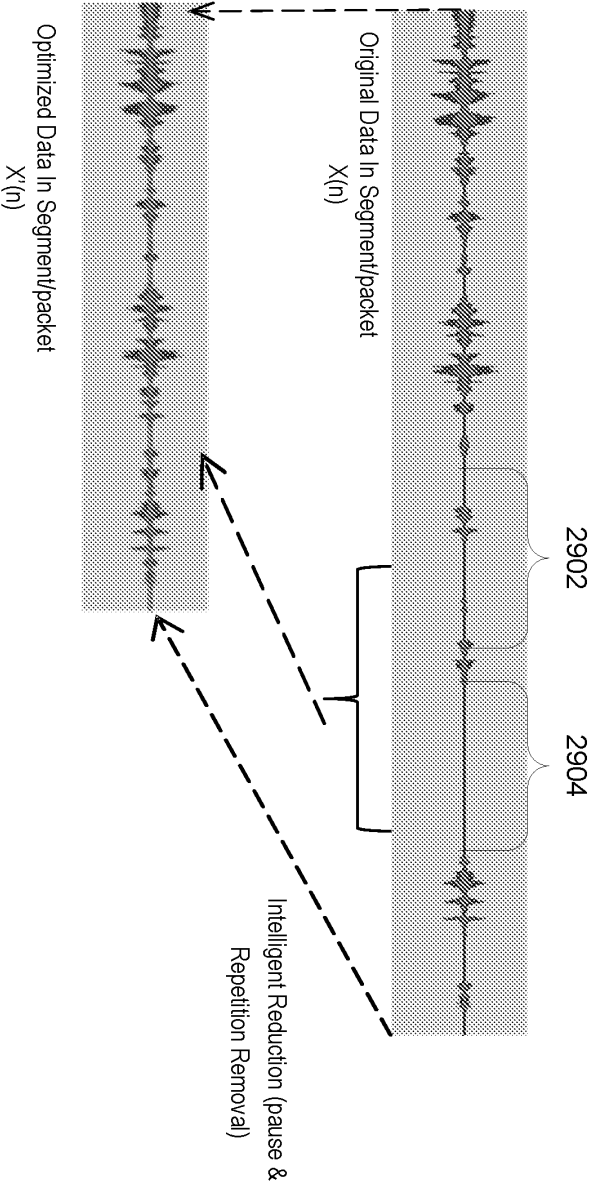


Fig. 29

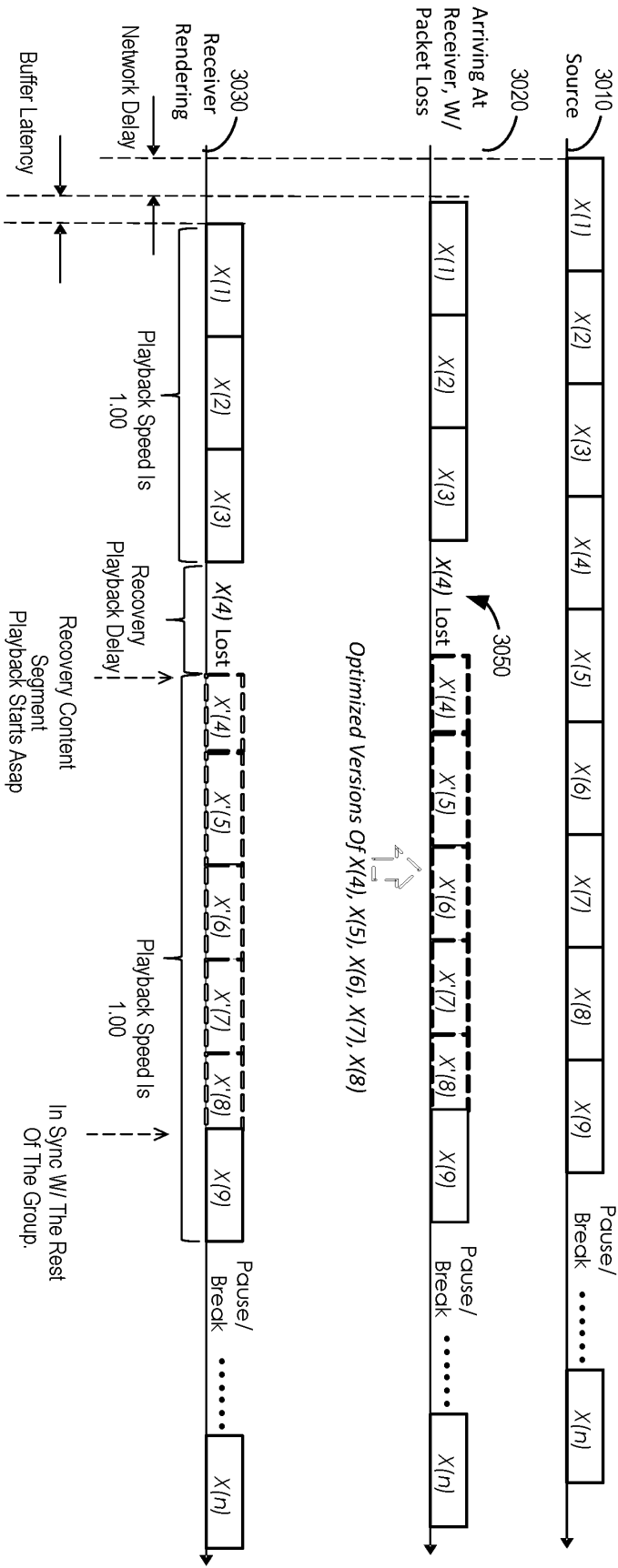


Fig. 30

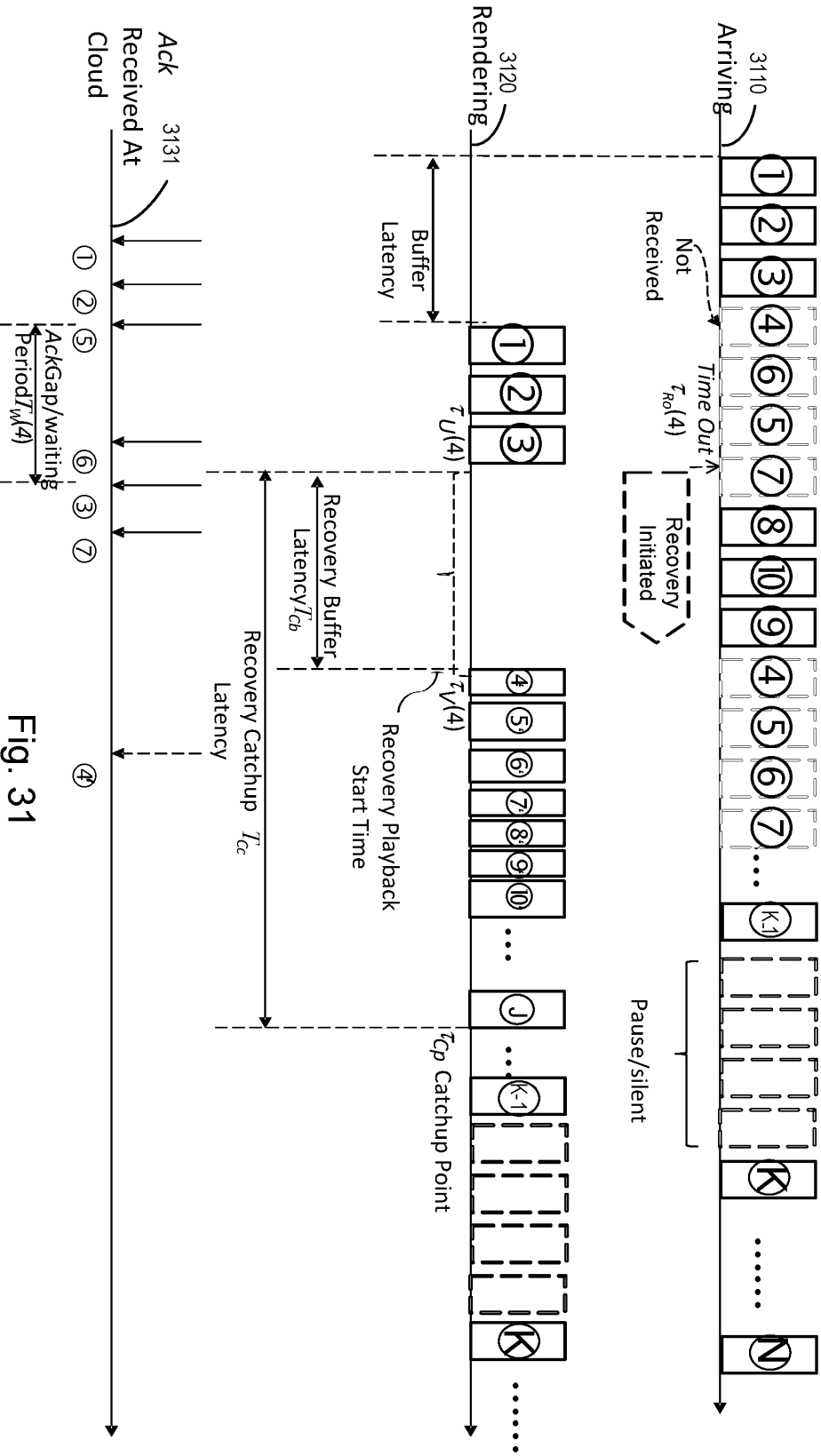


Fig. 31

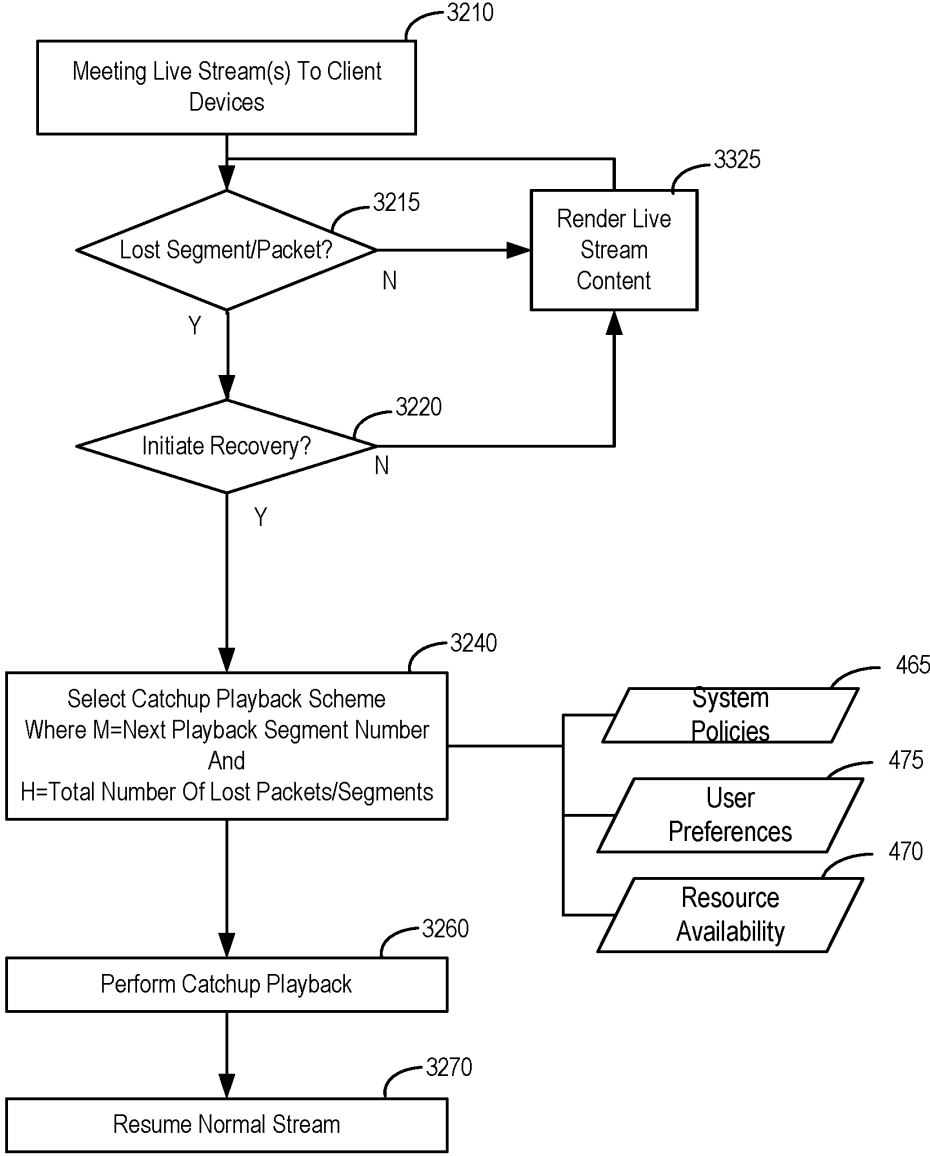


Fig. 32

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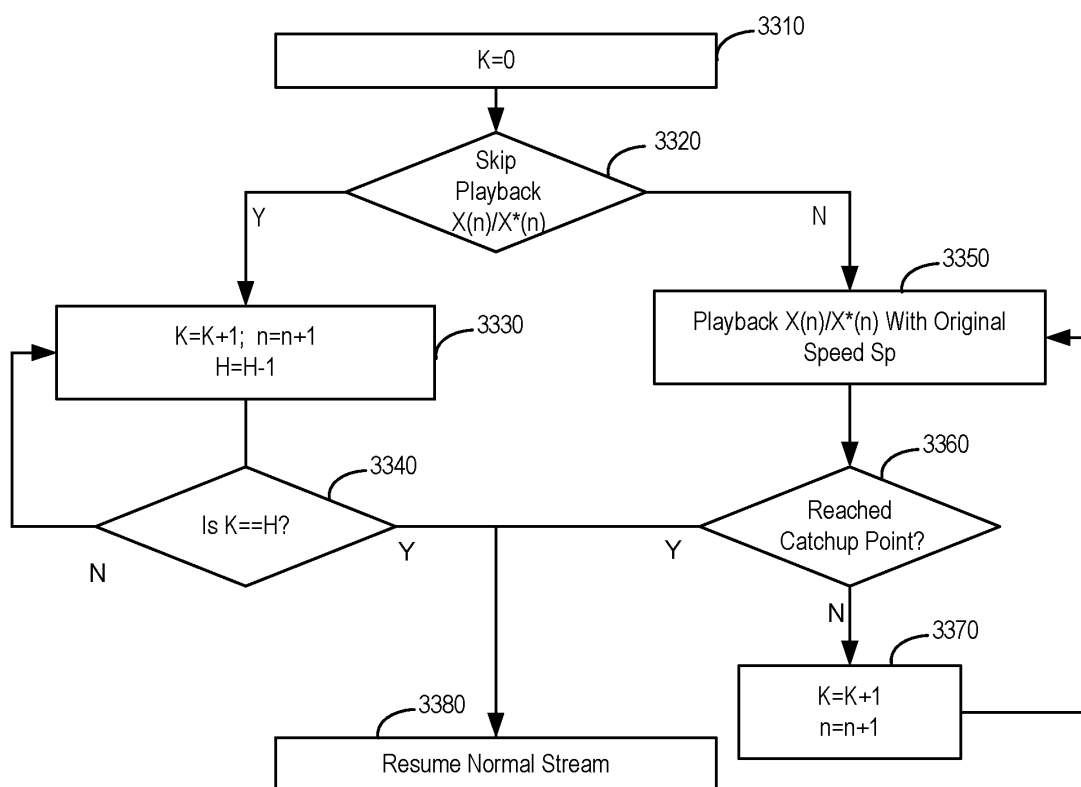


Fig. 33

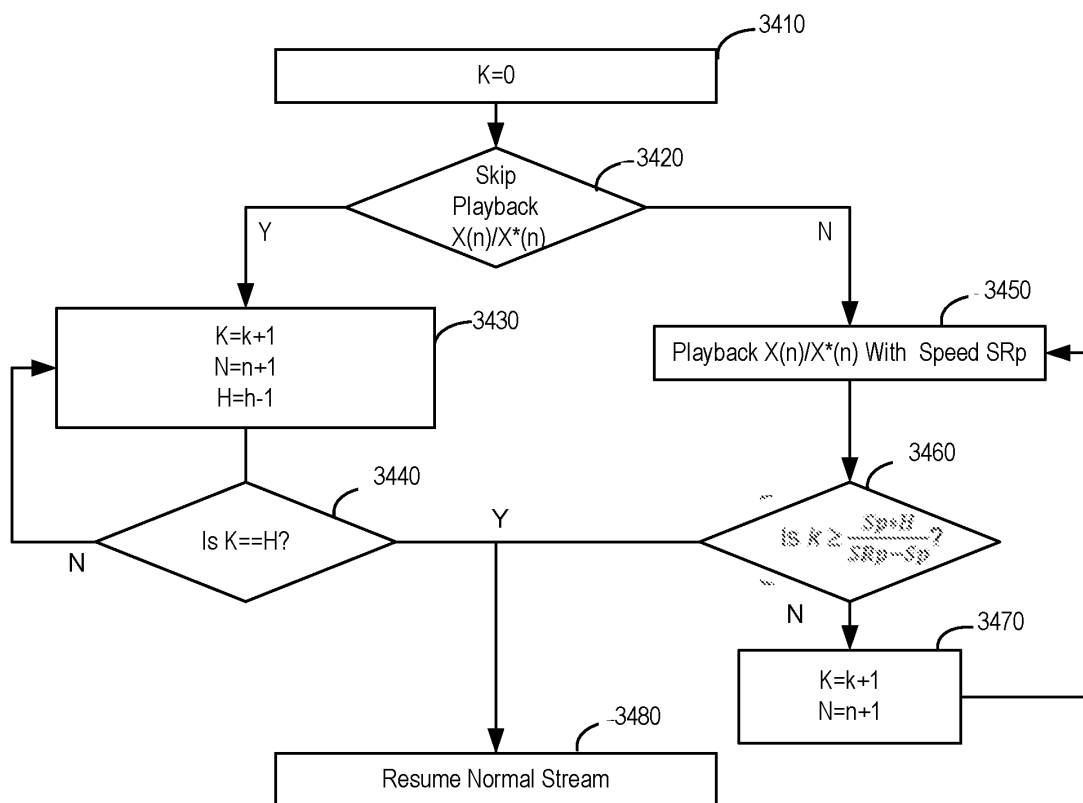


Fig. 34

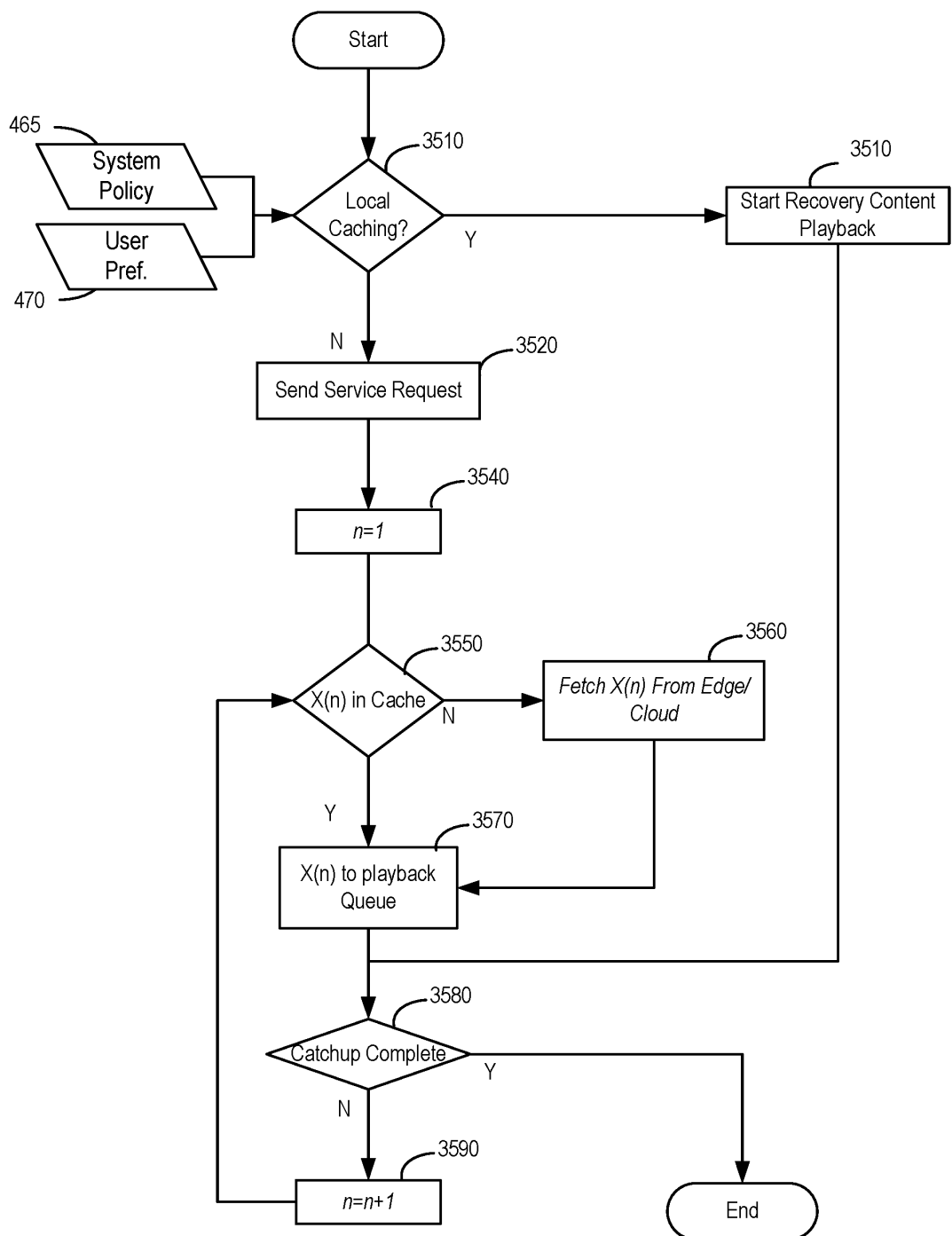


Fig. 35

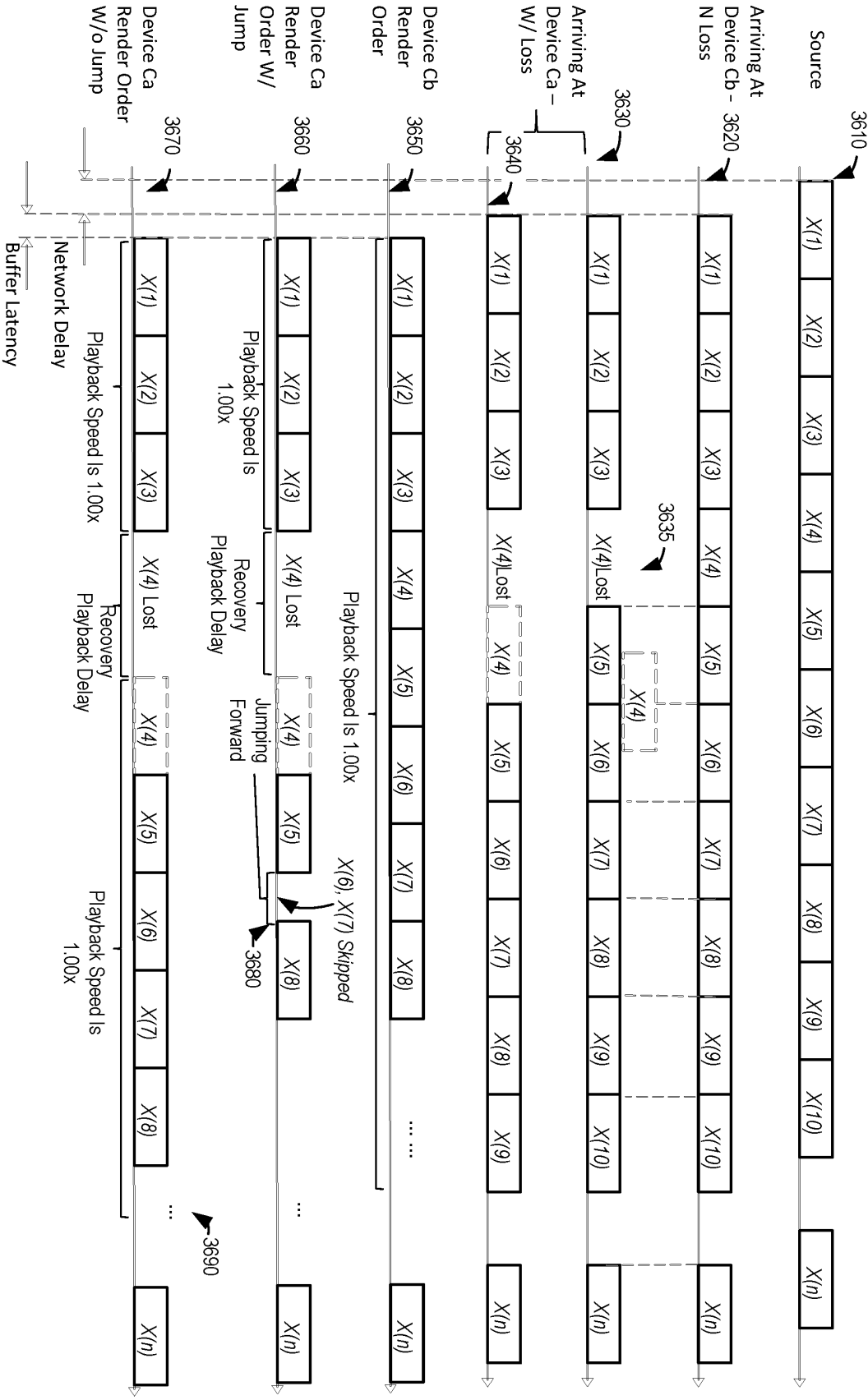


Fig. 36

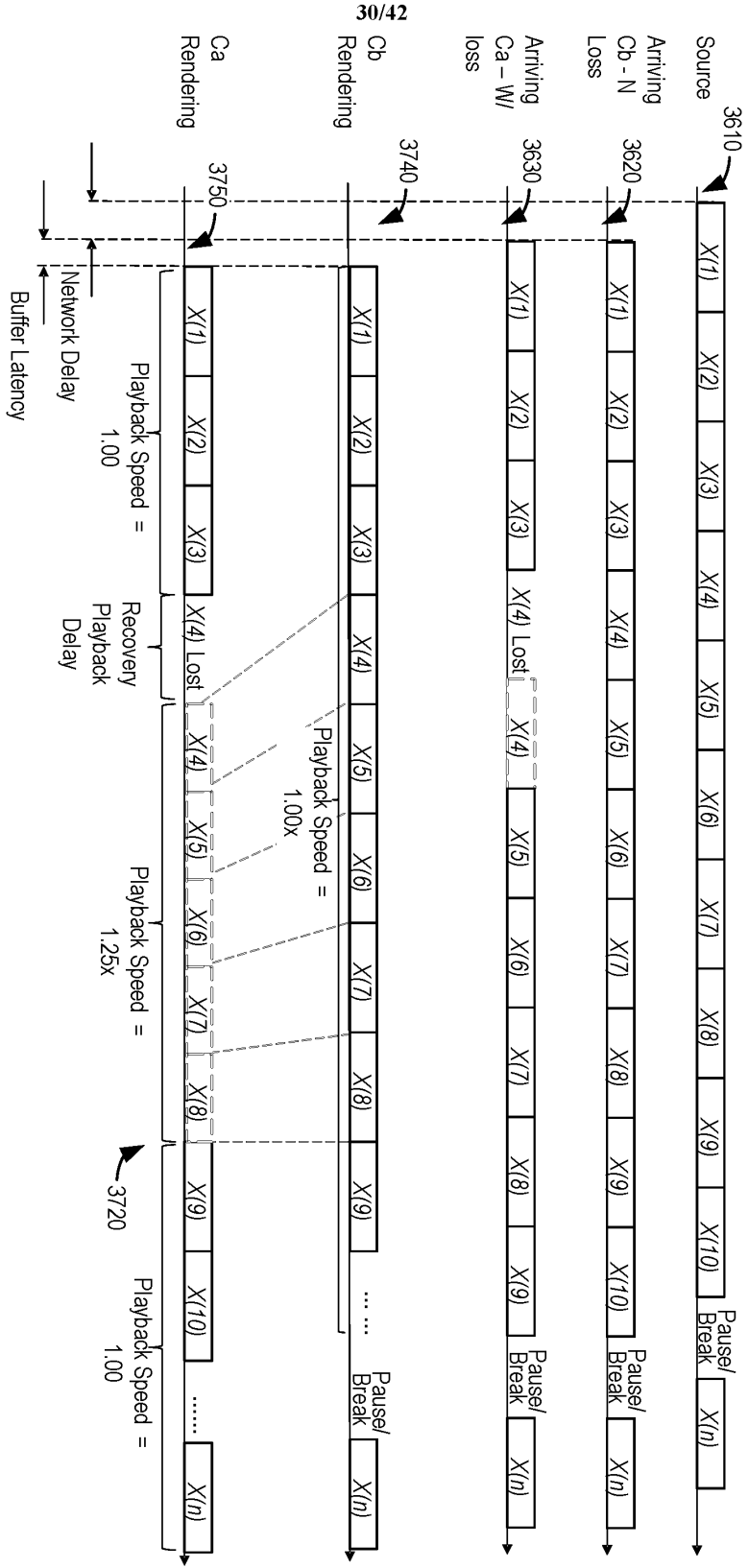
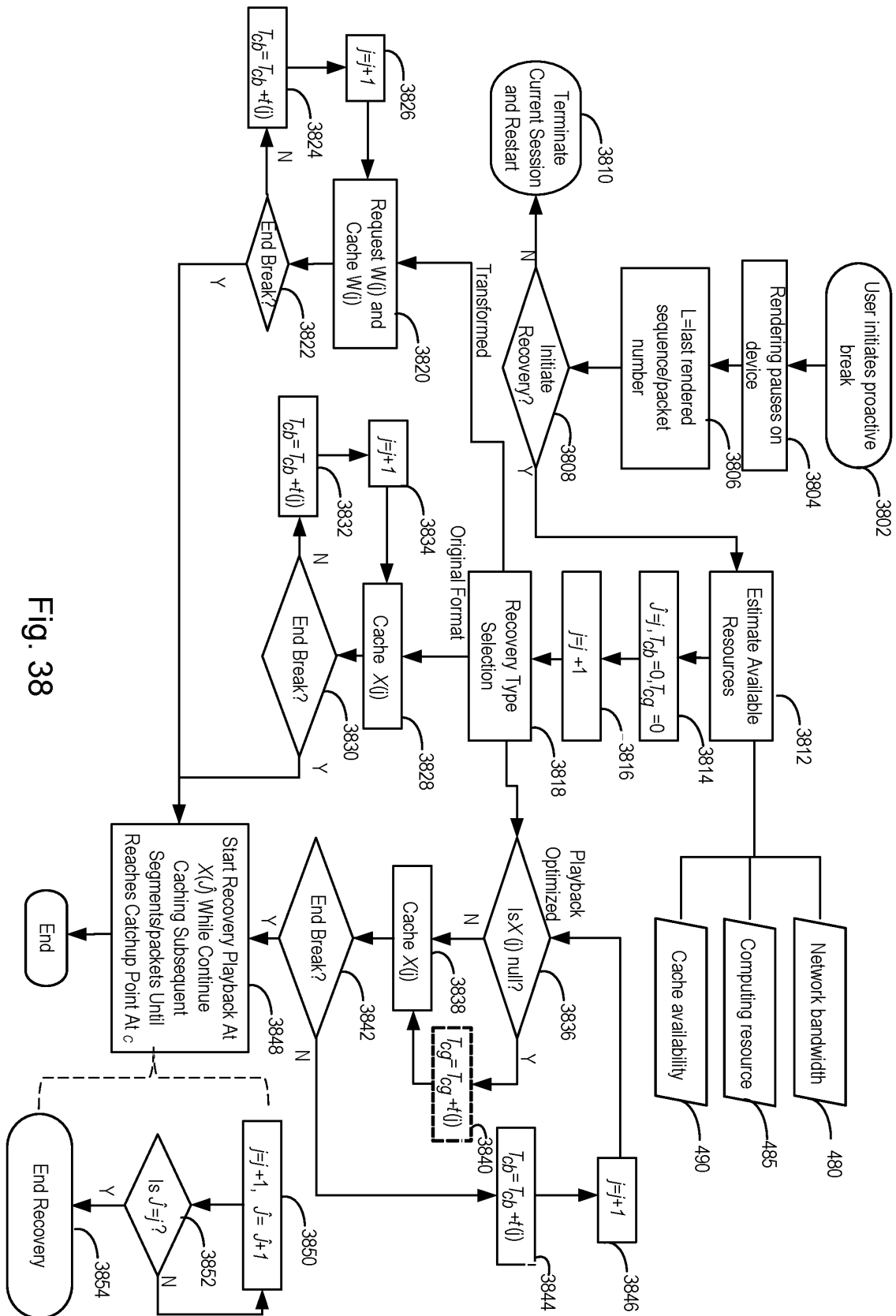


Fig. 37



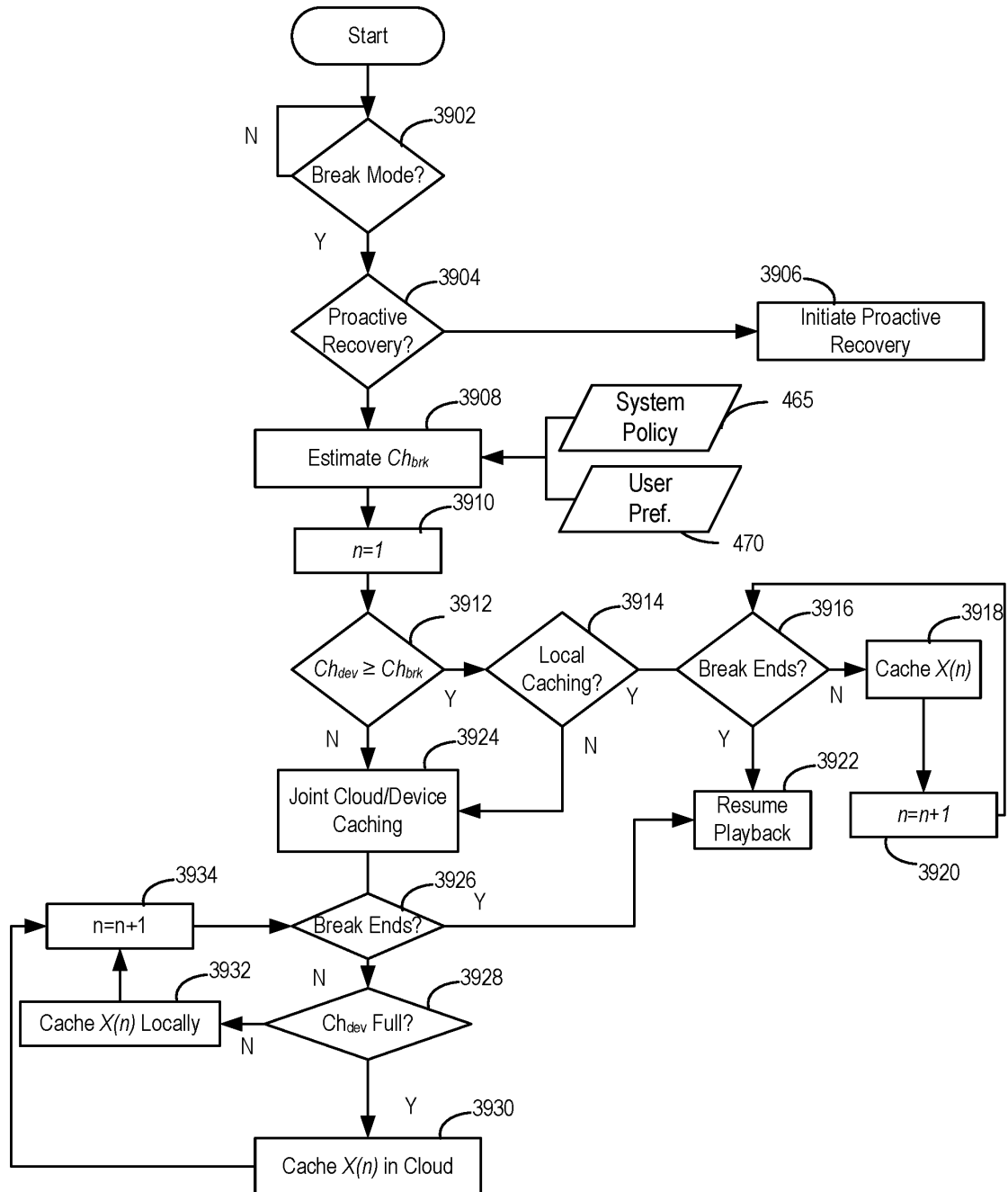


Fig. 39

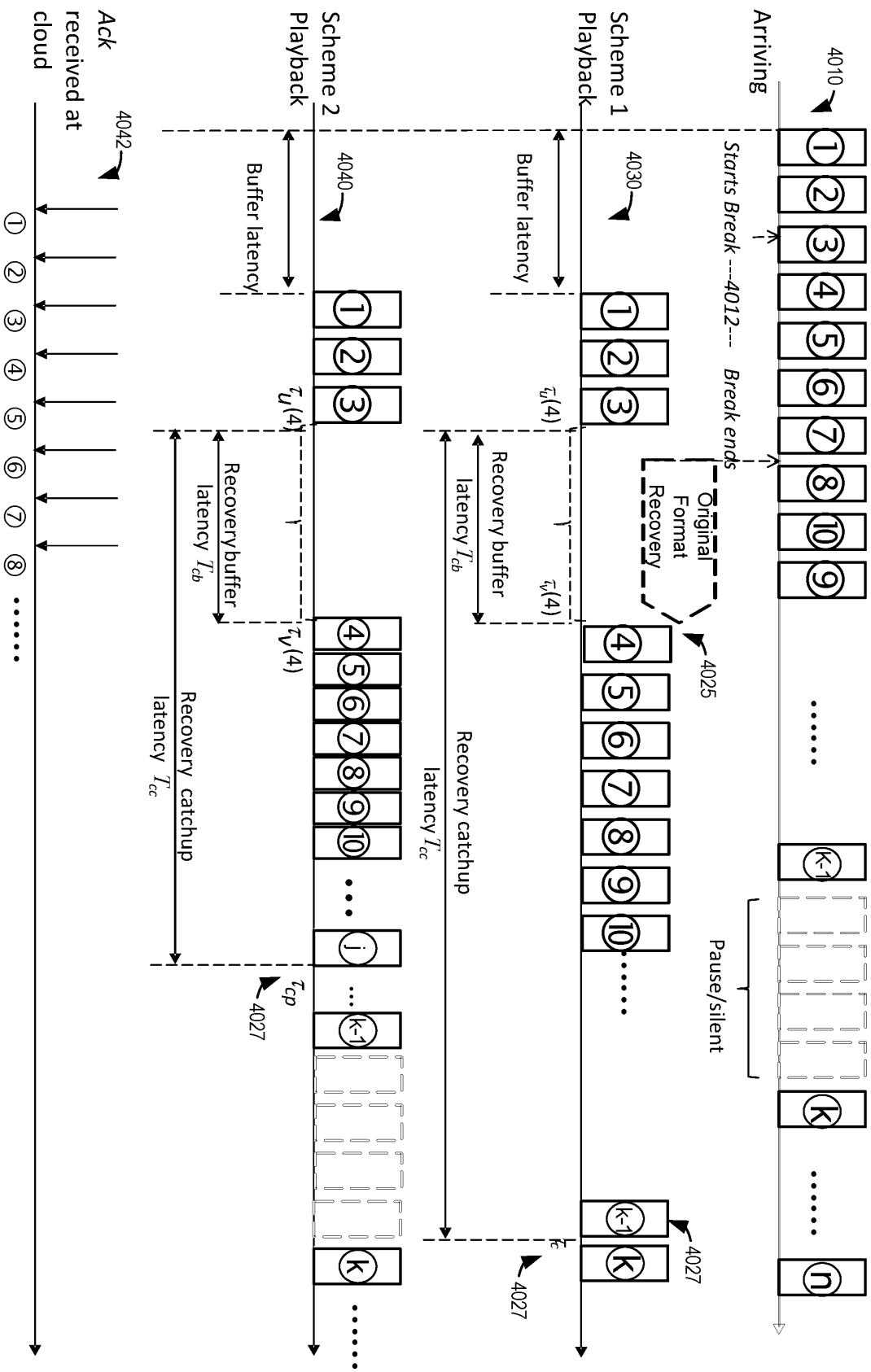


Fig. 40

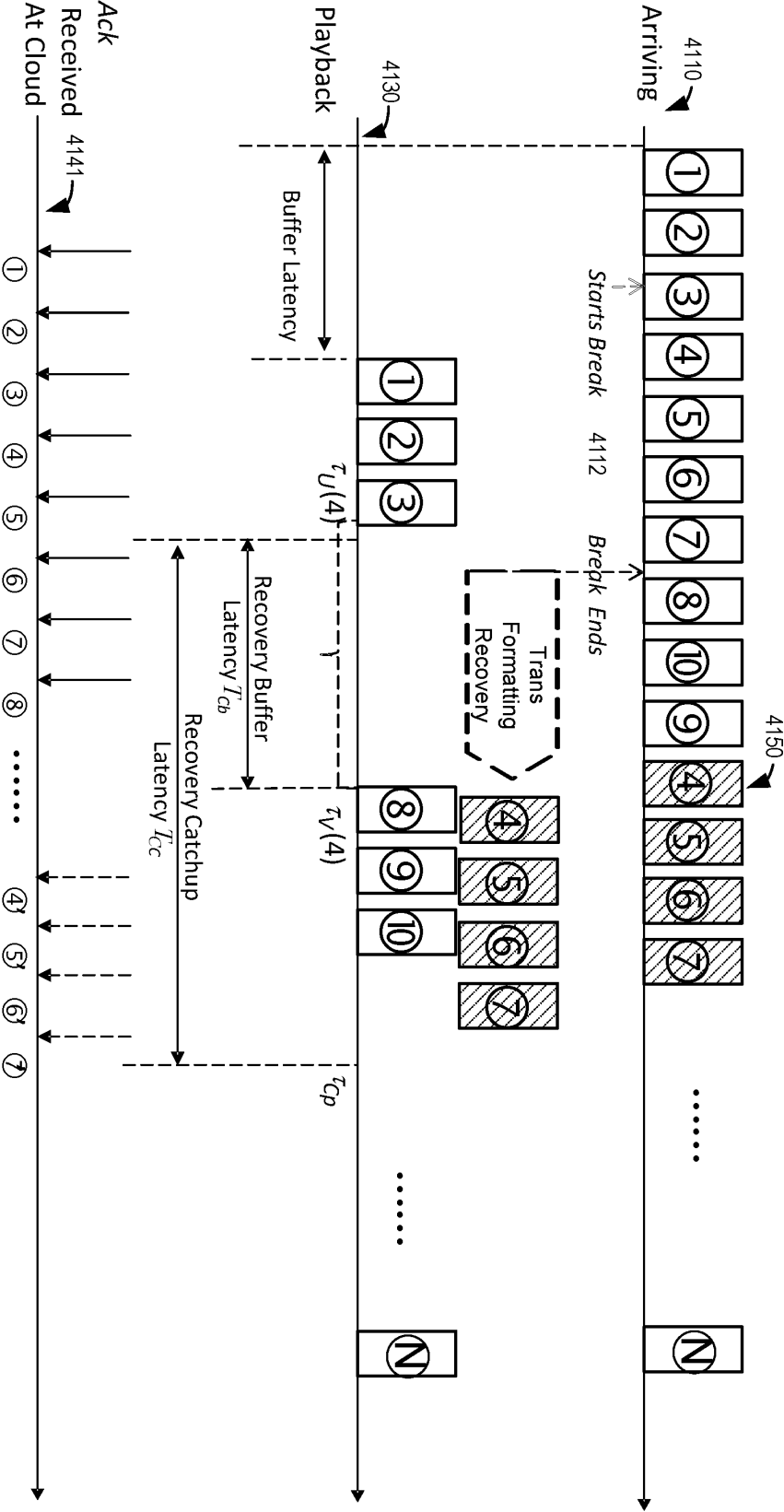


Fig. 41

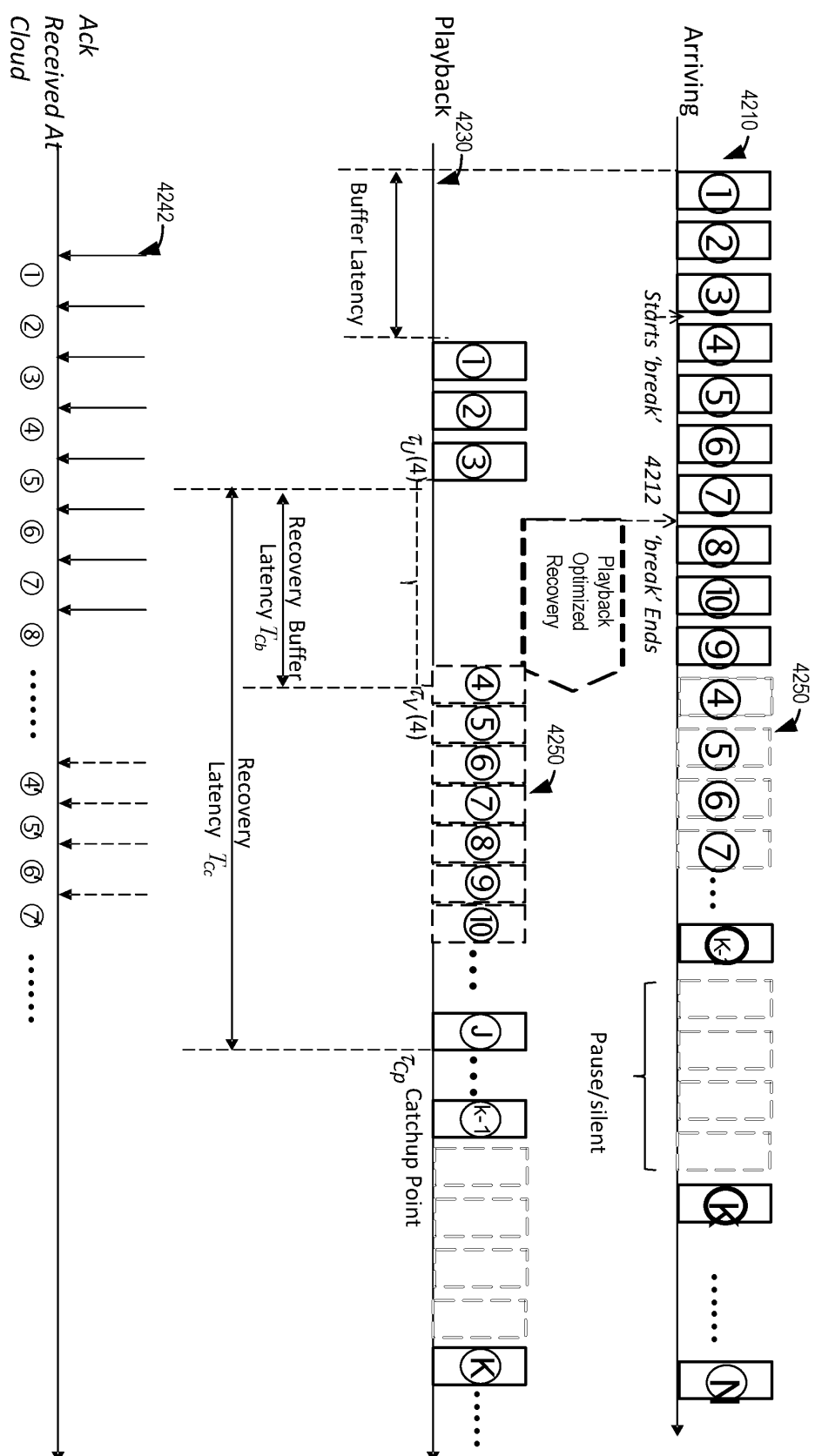


Fig. 42

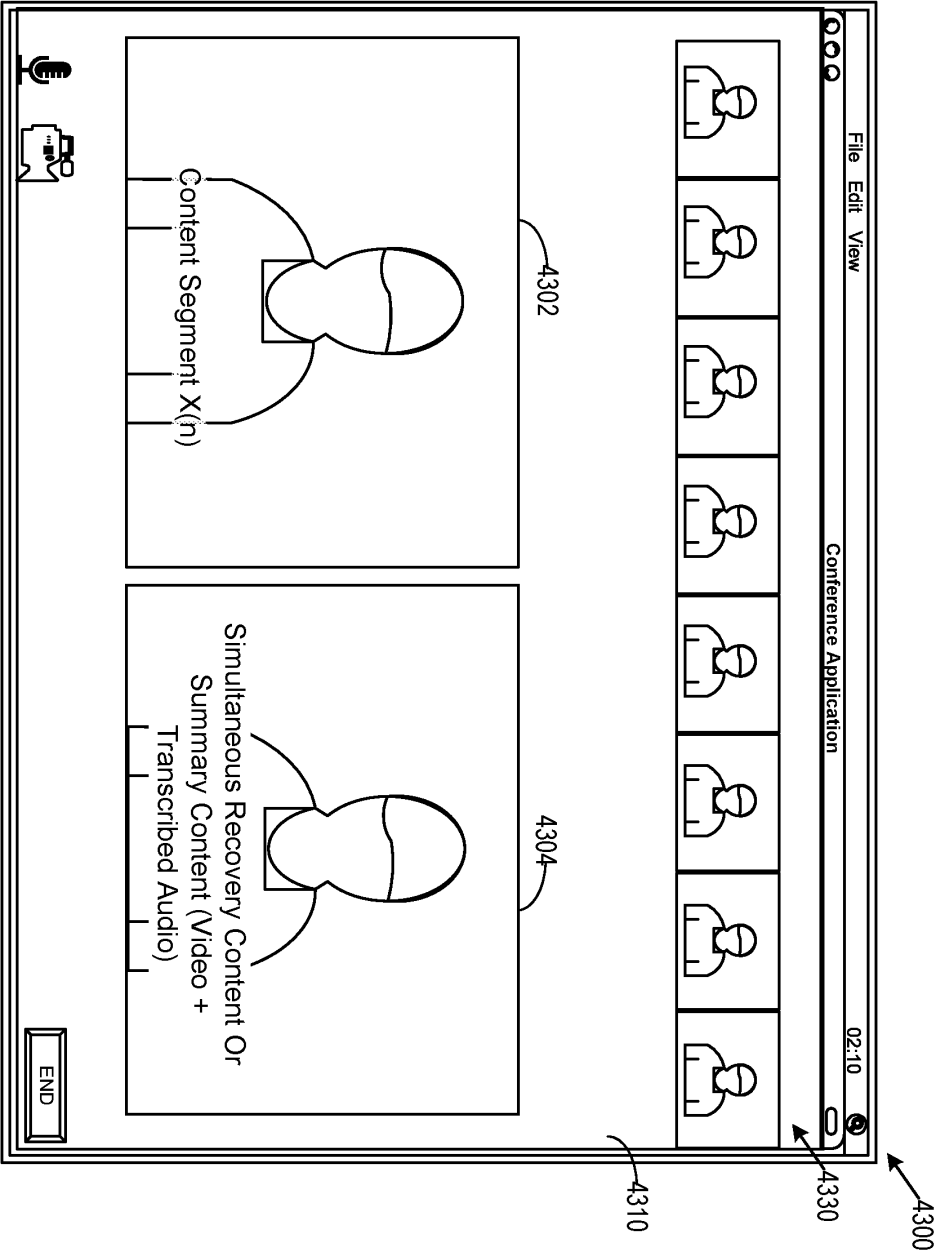


Fig. 43

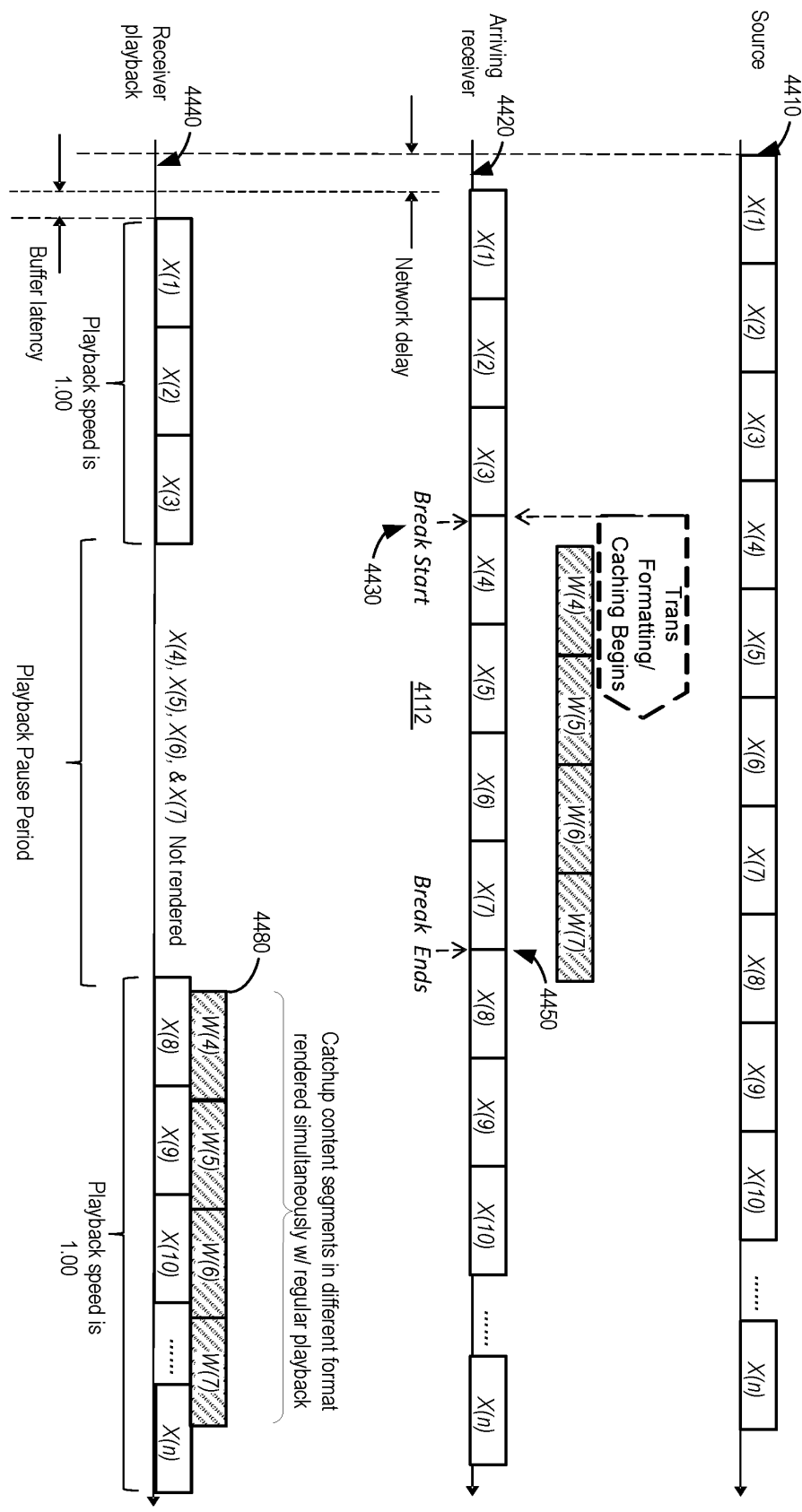


Fig. 44

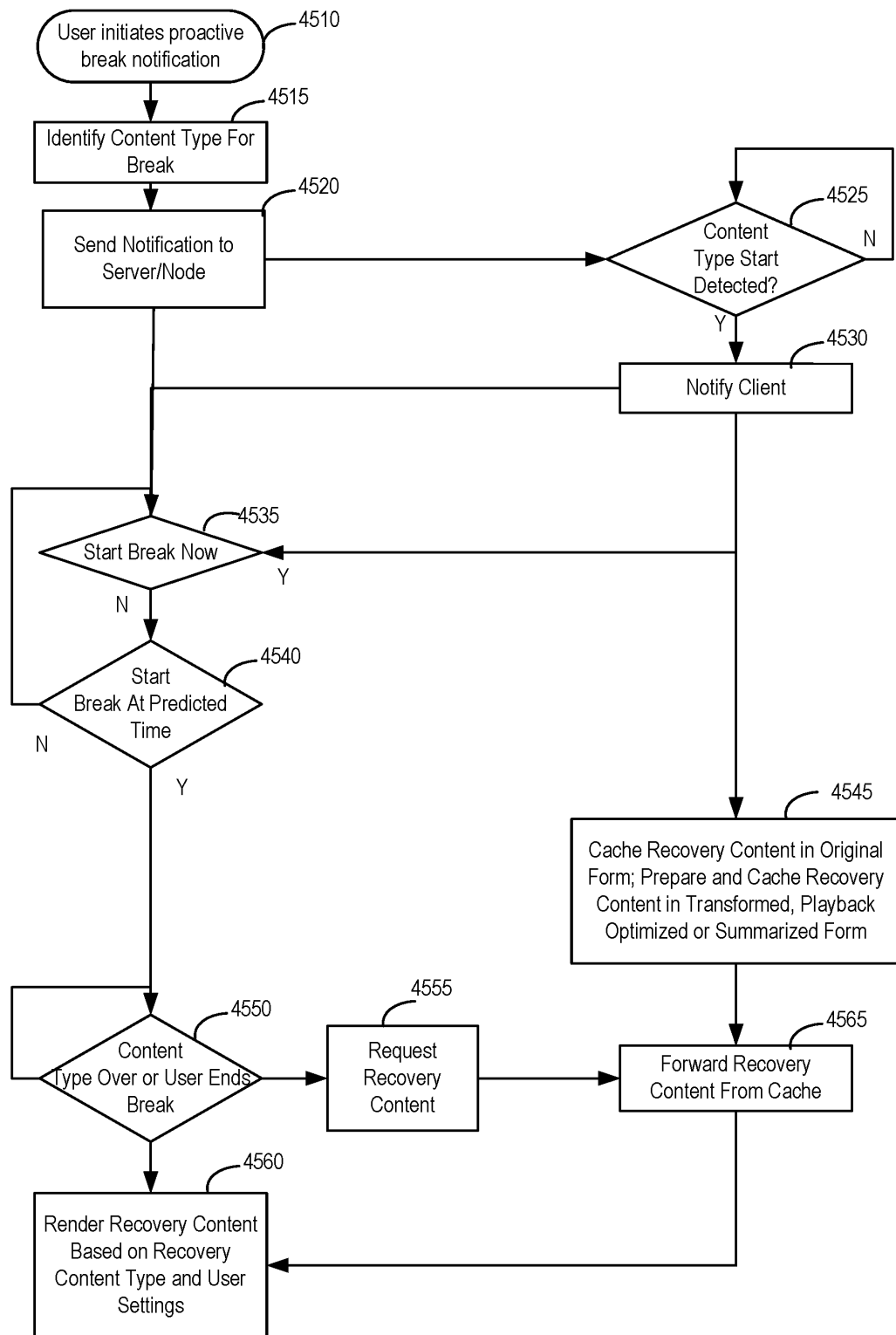


Fig. 45

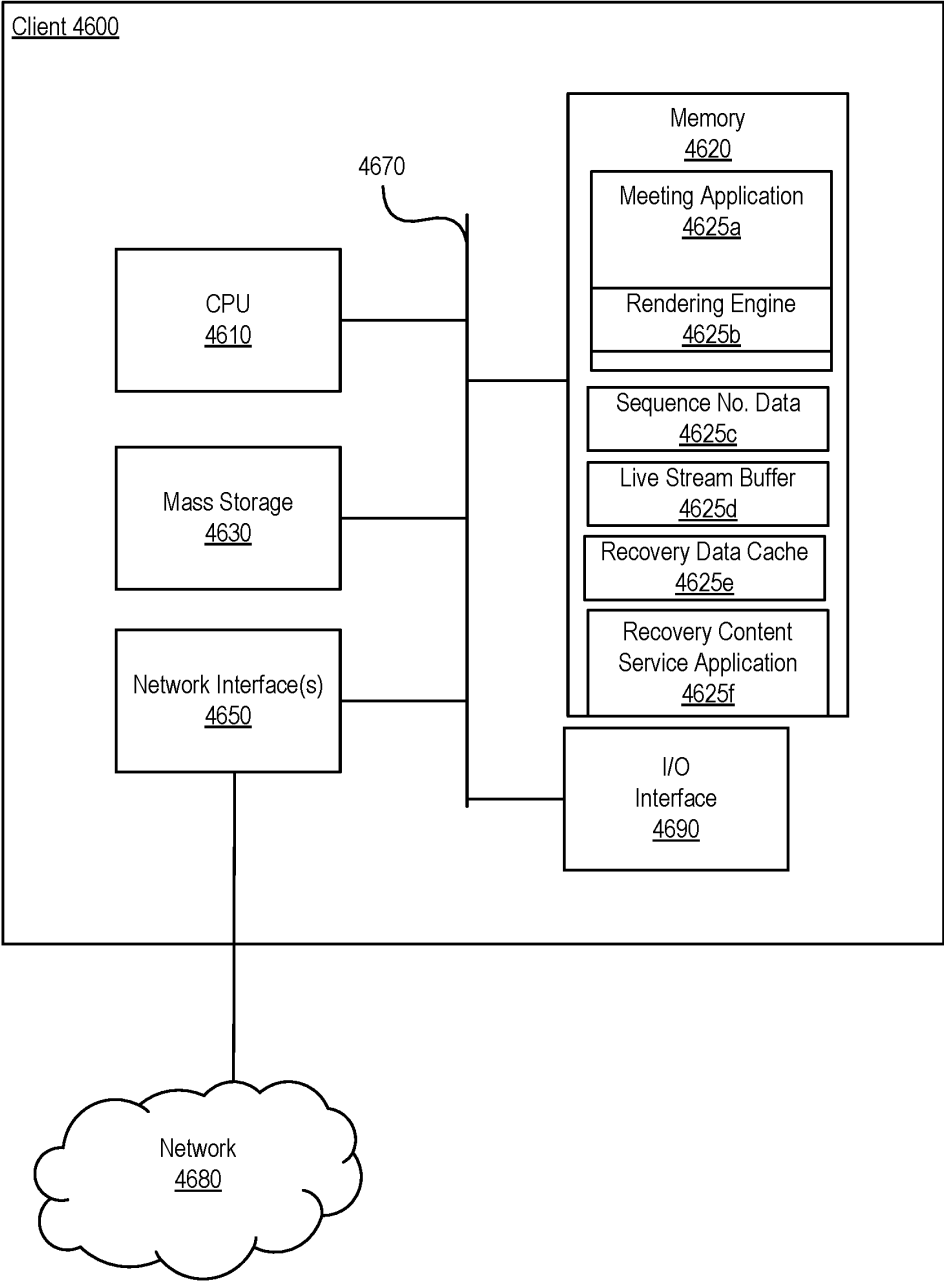


Fig. 46

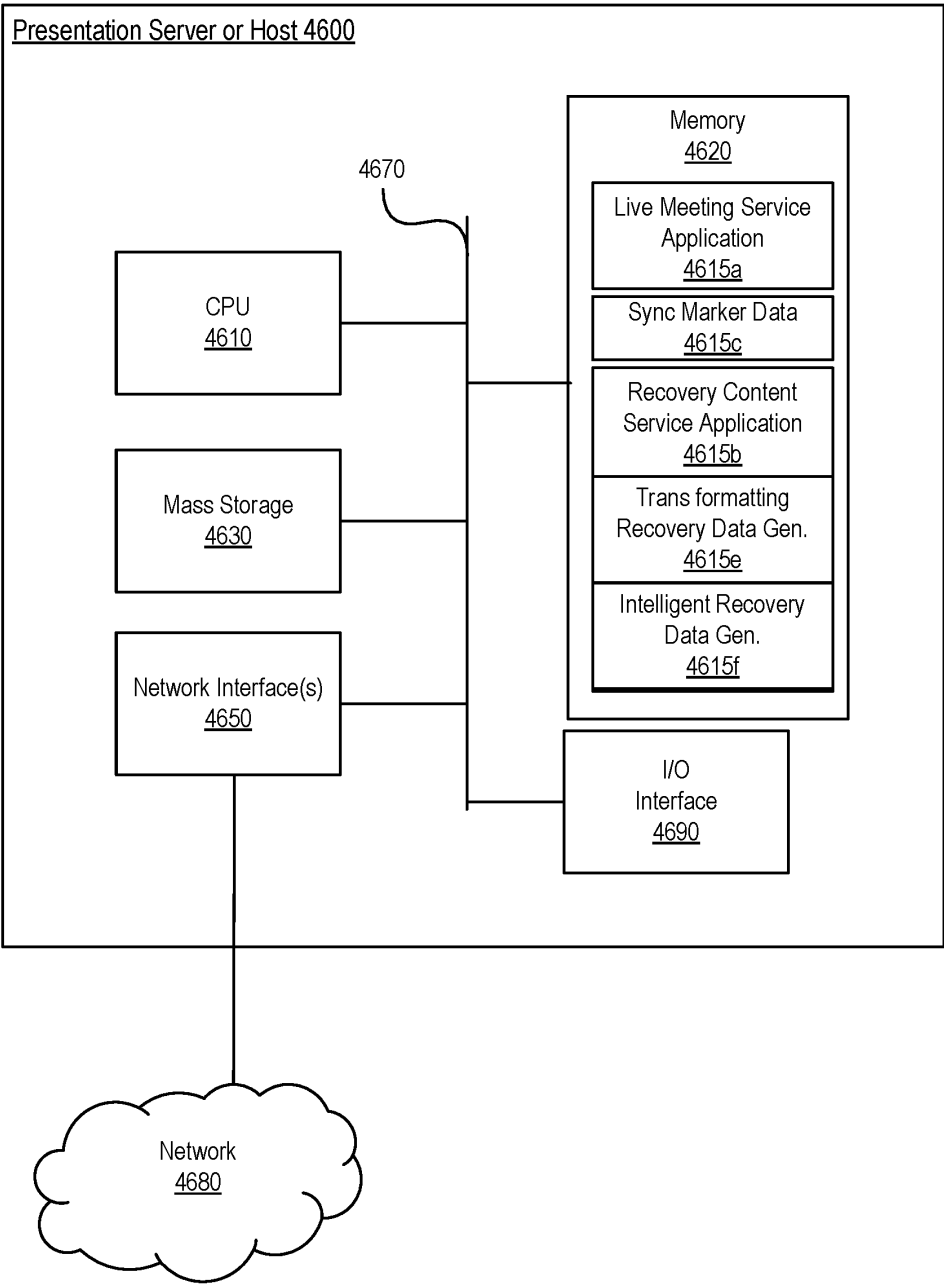


Fig. 47

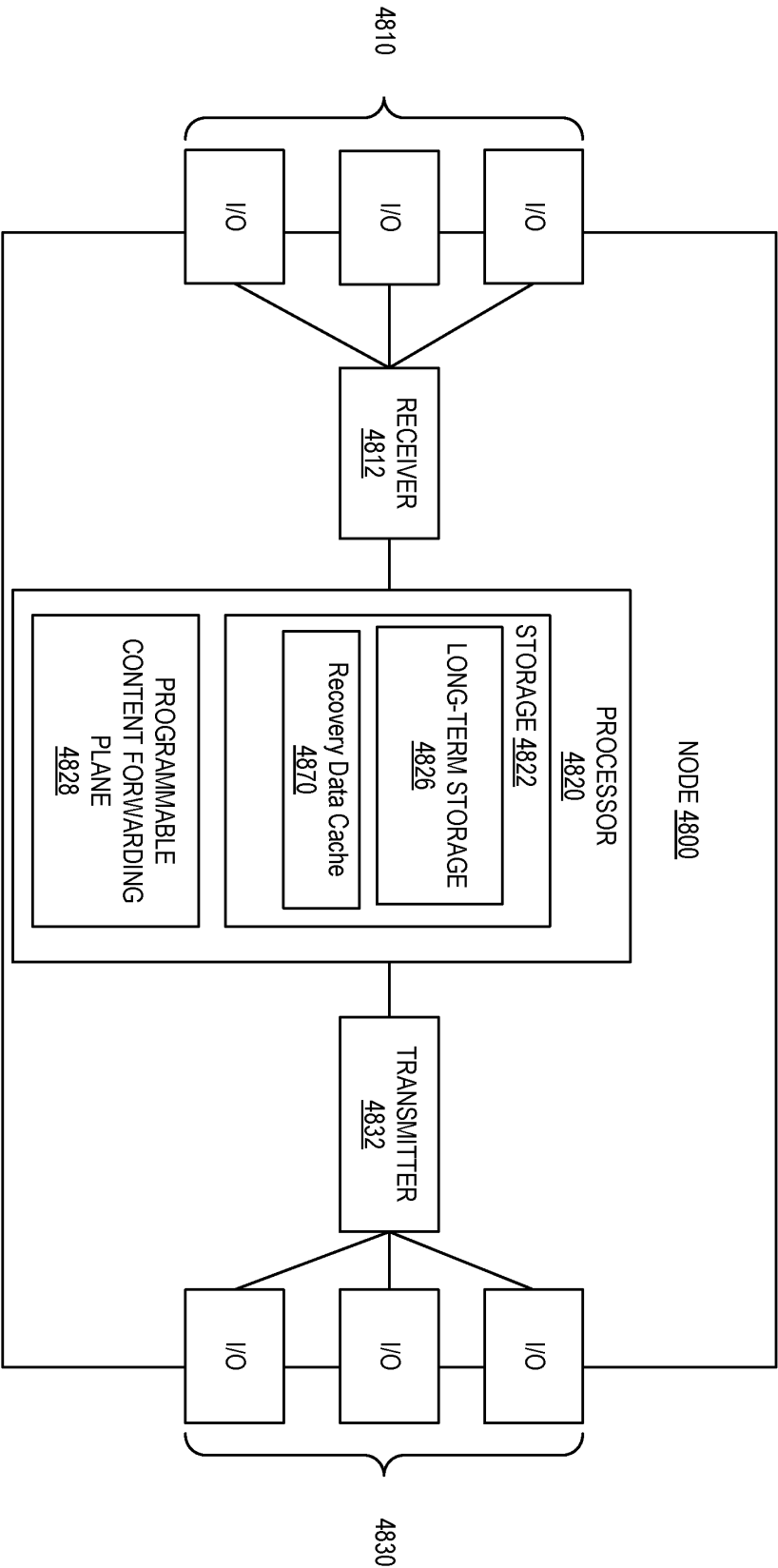


Fig. 48

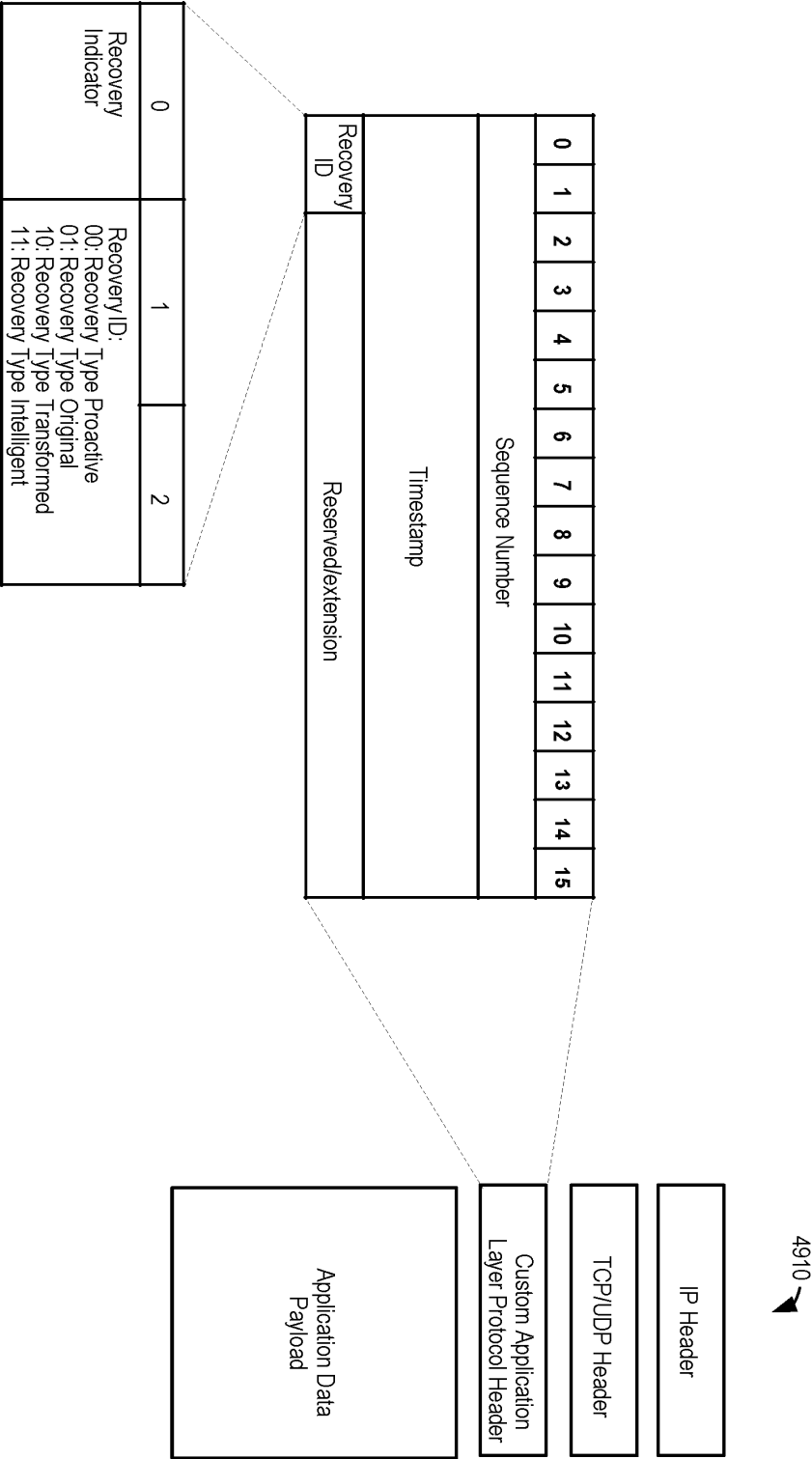


Fig. 49