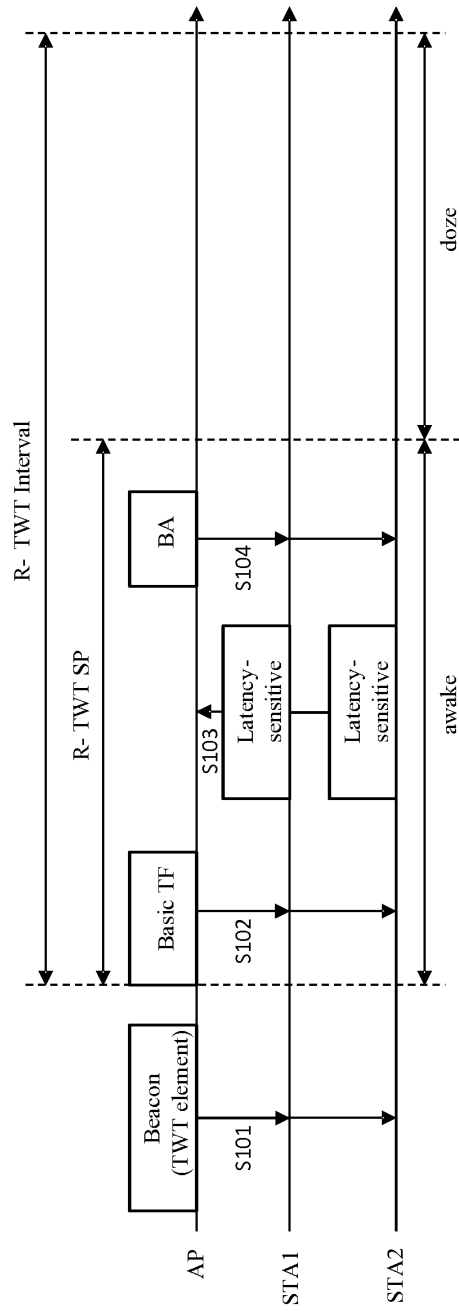
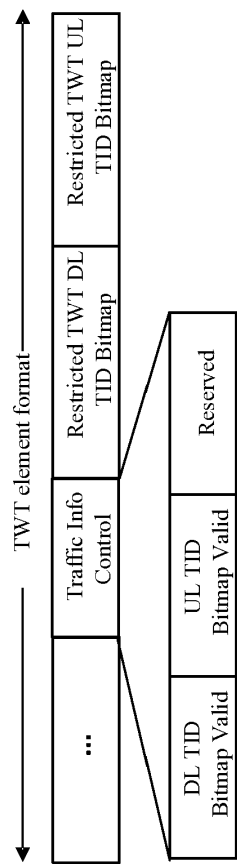


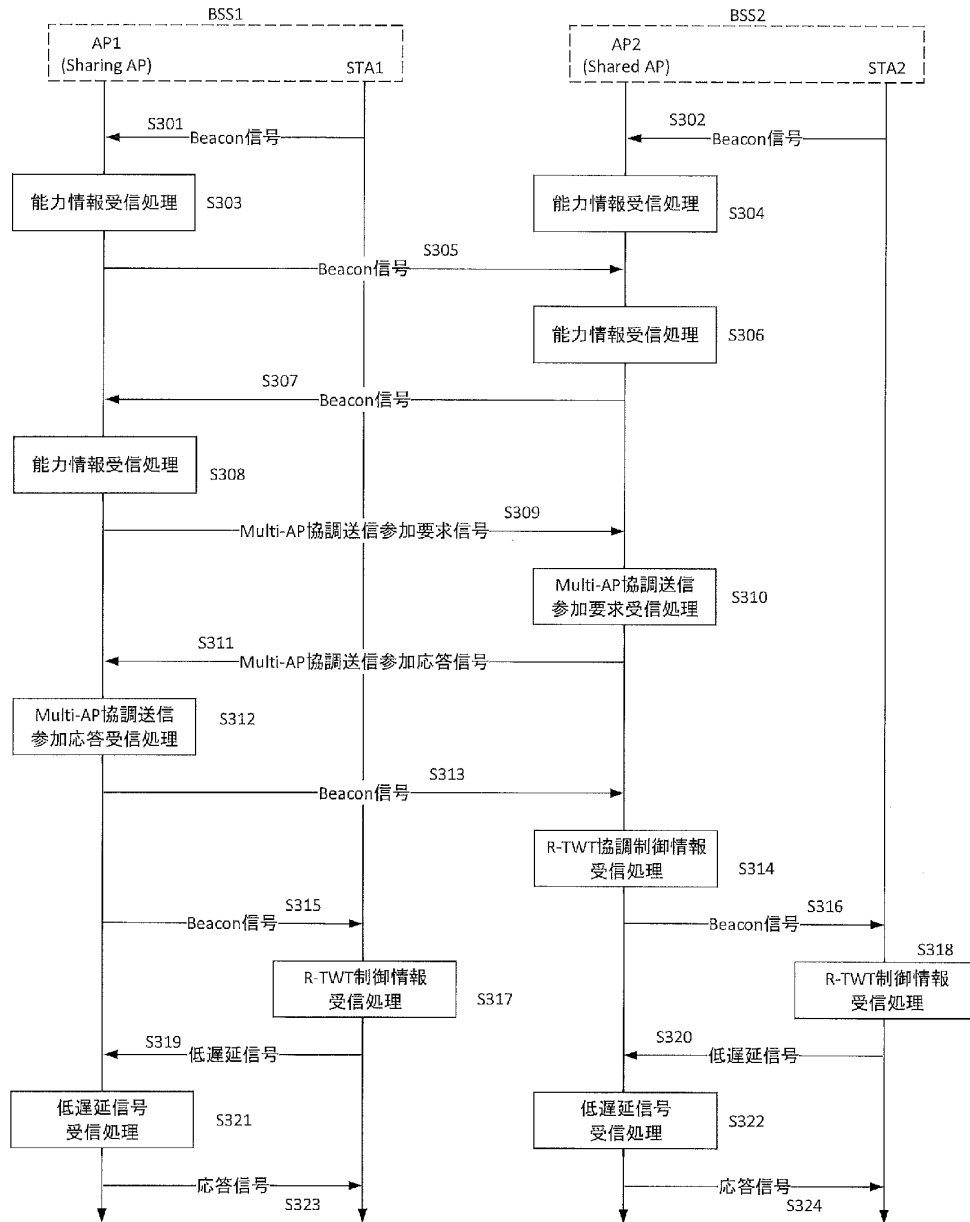
[図1]



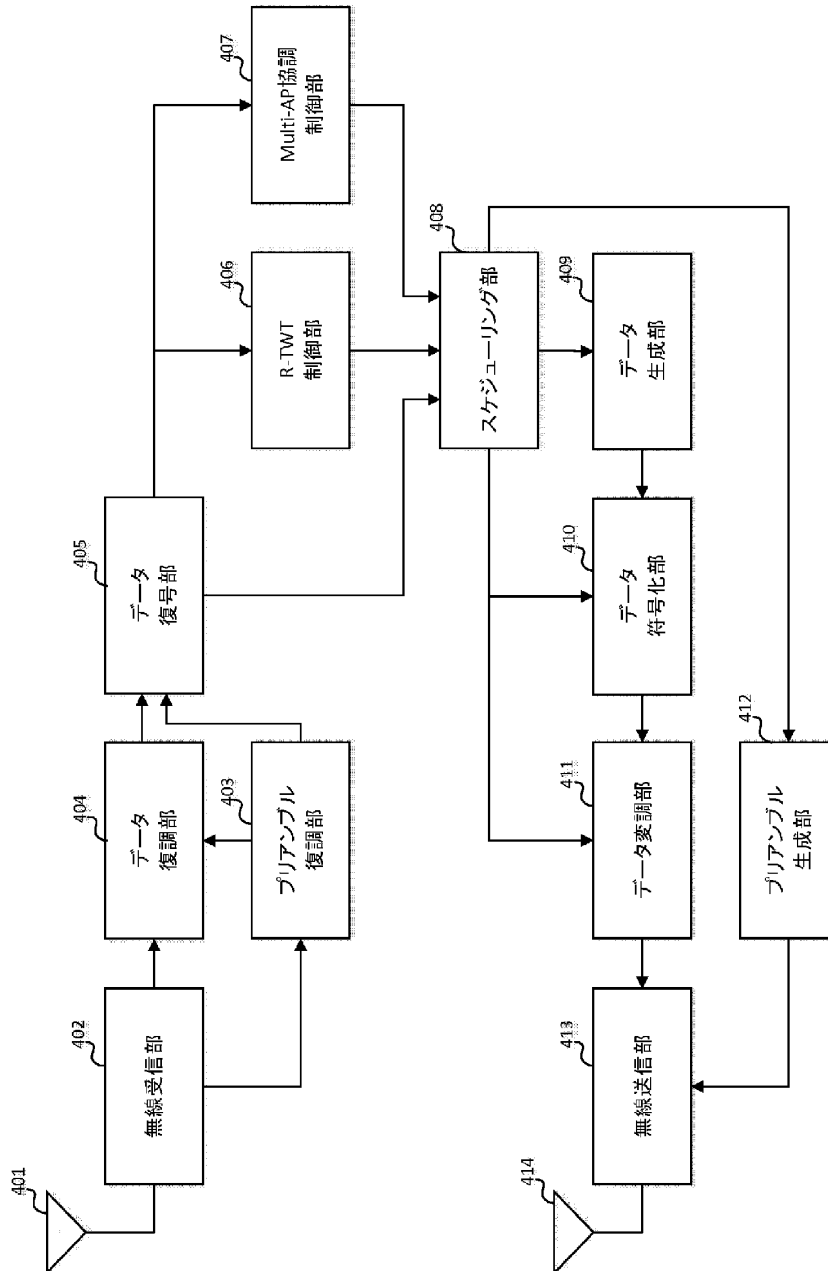
[図2]



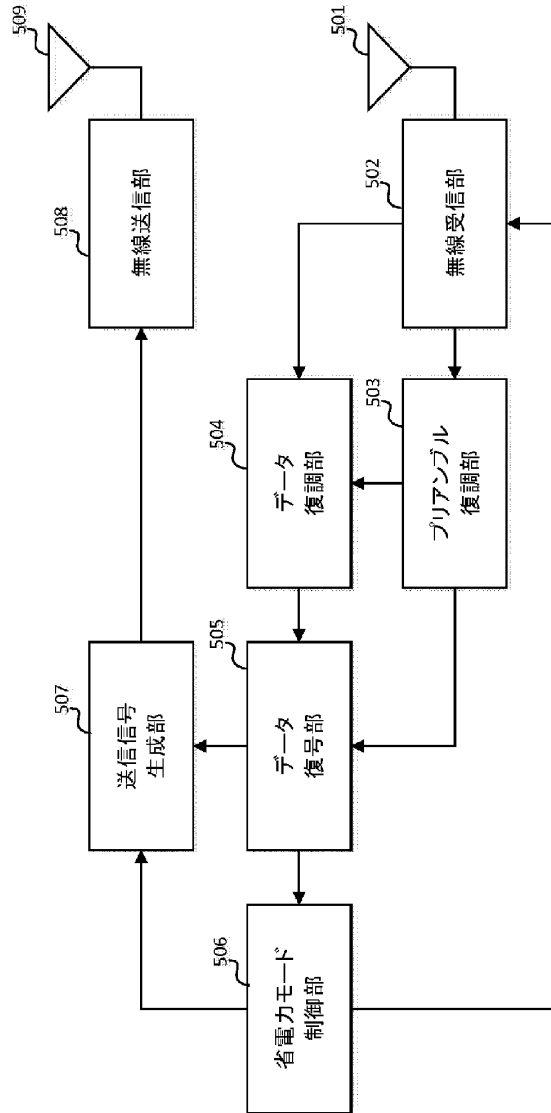
[図3]



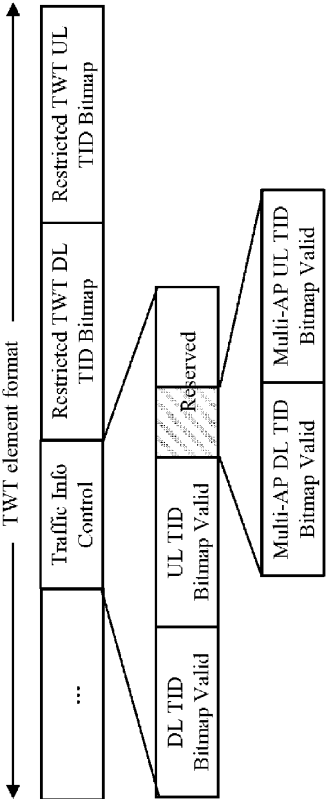
[図4]



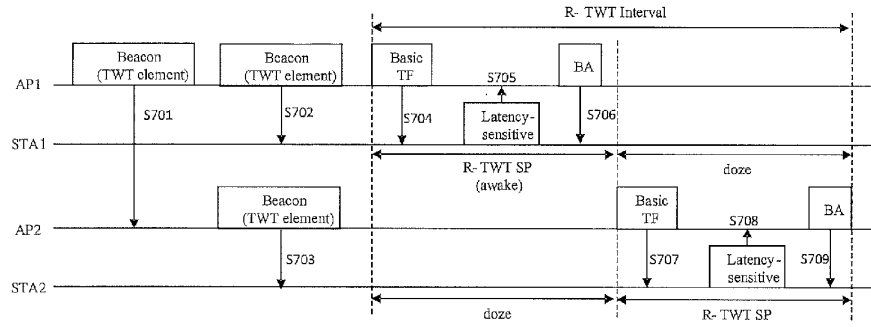
【図5】



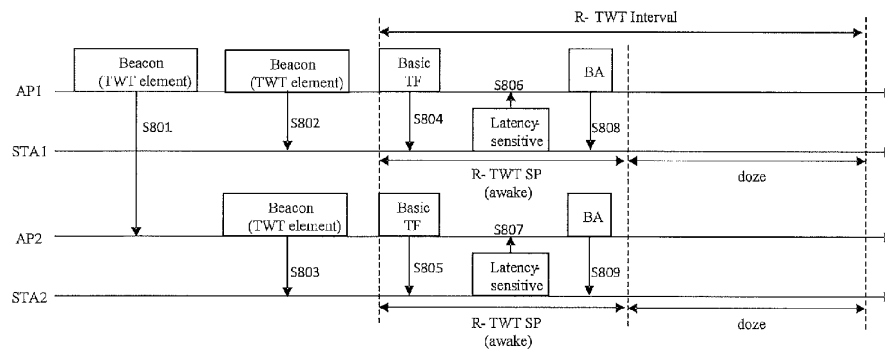
[図6]



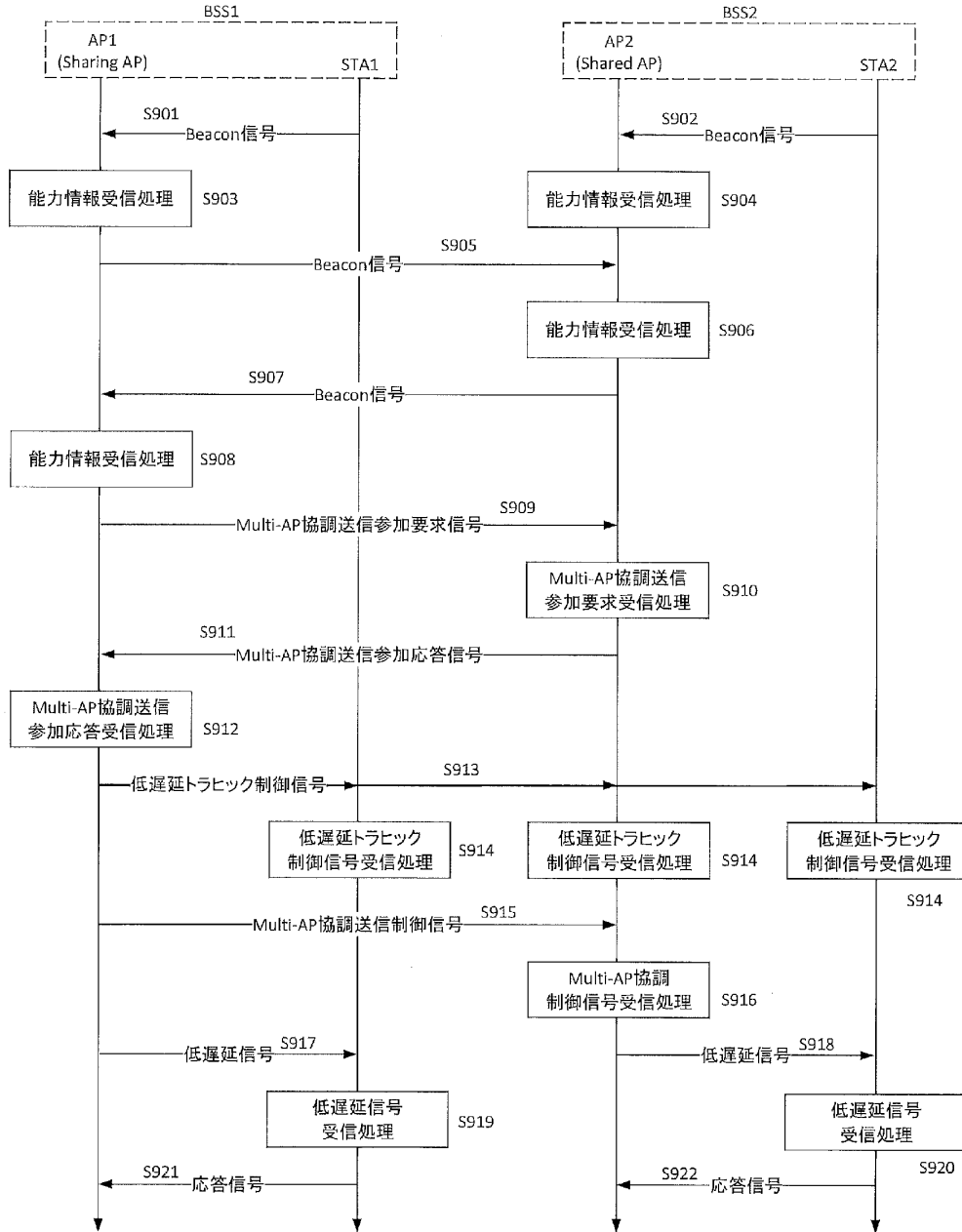
[図7]



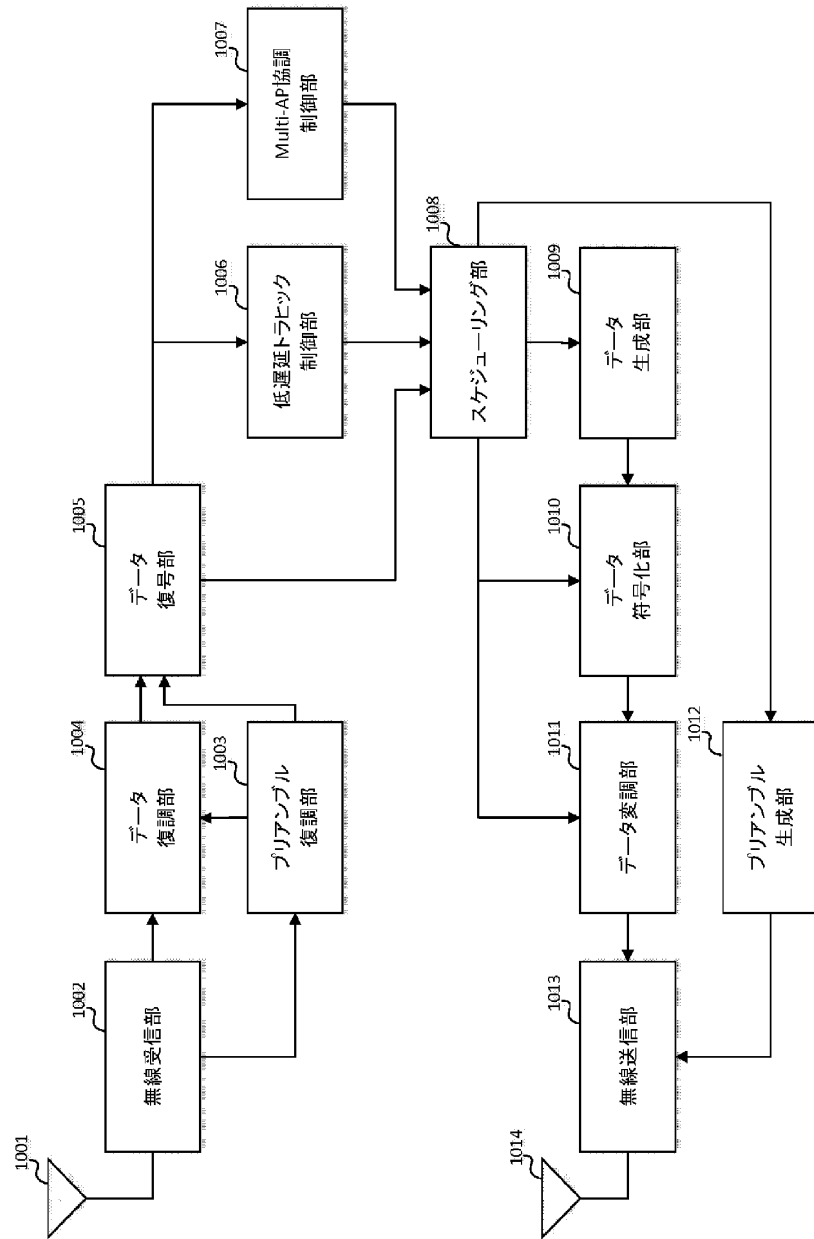
[図8]



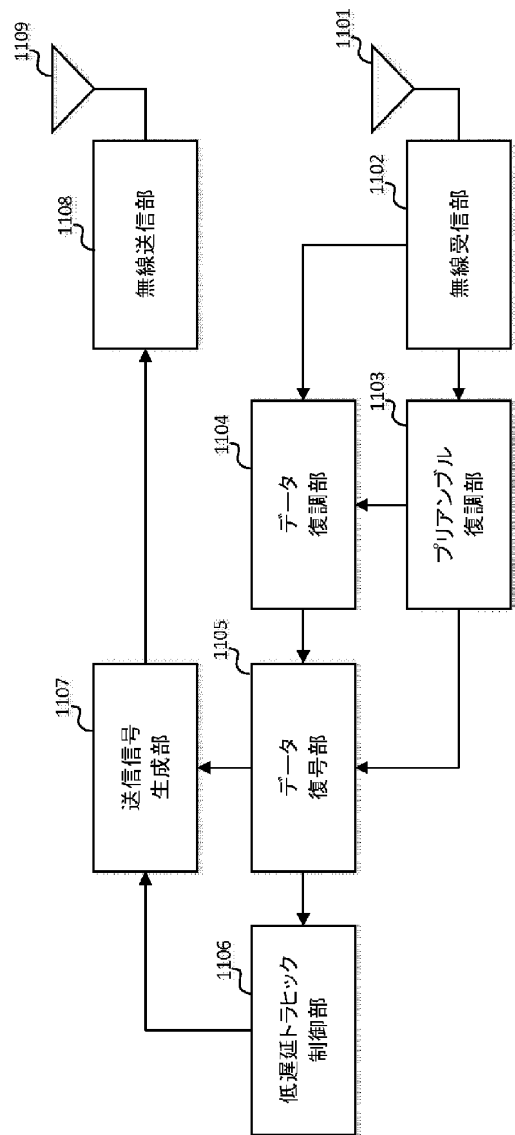
[図9]



【図10】



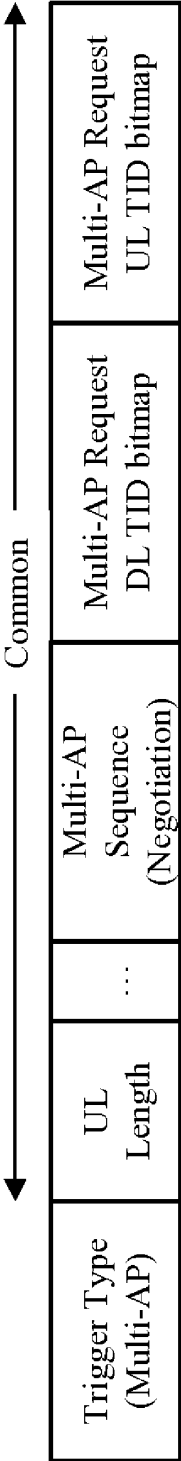
[図11]



[図12]

Element ID	Length	Multi-AP DL TID bitmap	Multi-AP UL TID bitmap
---------------	--------	---------------------------	---------------------------

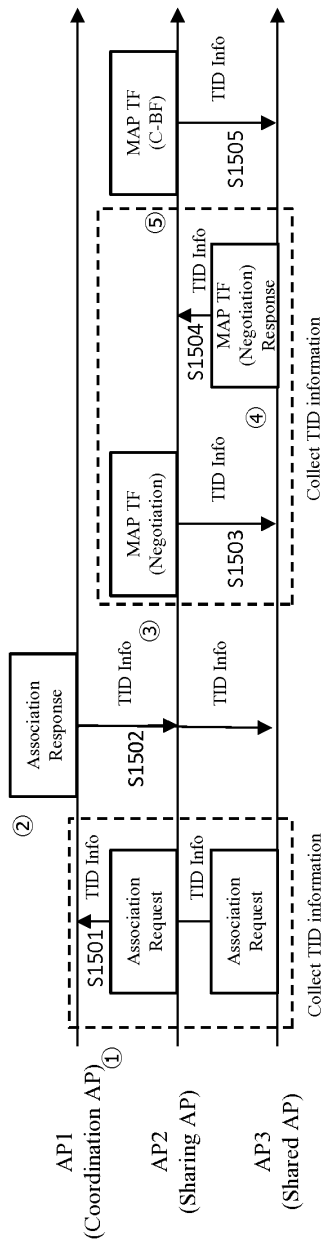
[図13]



[図14]



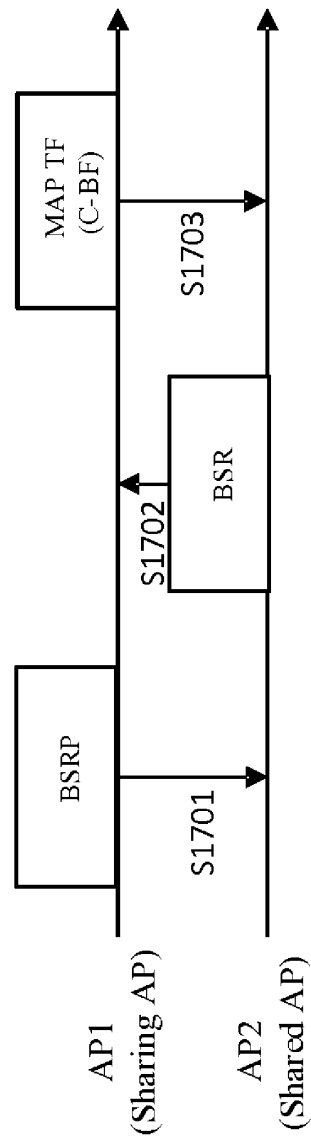
[図15]



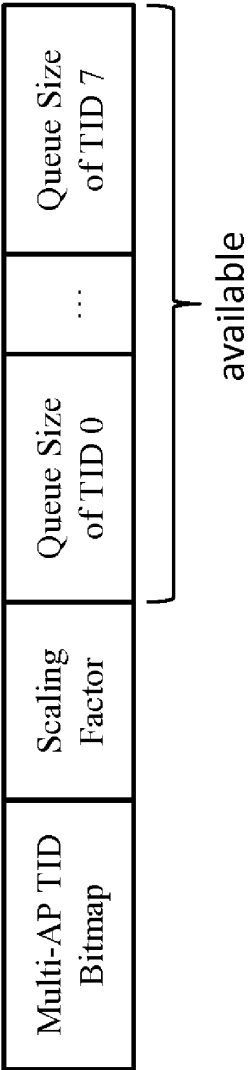
[図16]

ACI Bitmap	Delta TID	ACI High	Scaling Factor	Queue Size High	Queue Size All
---------------	--------------	-------------	-------------------	--------------------	-------------------

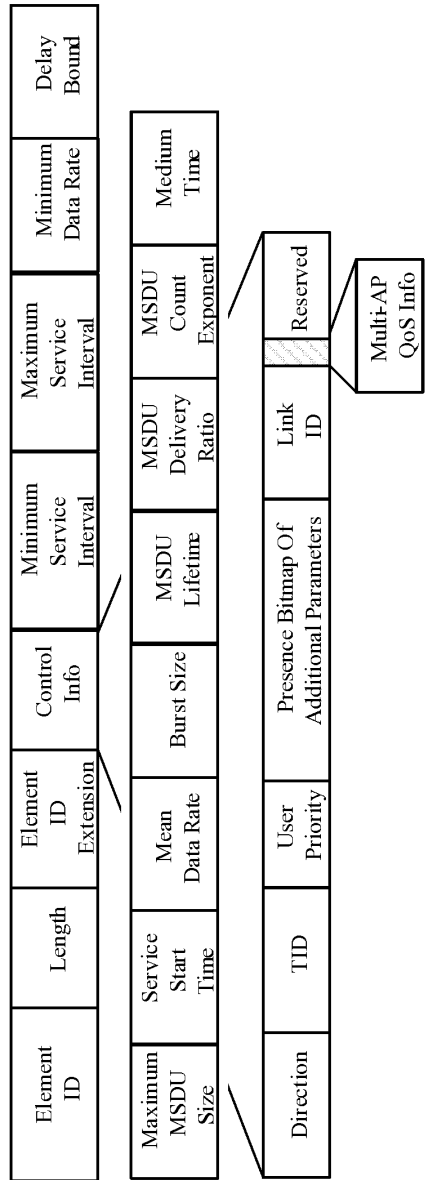
[図17]



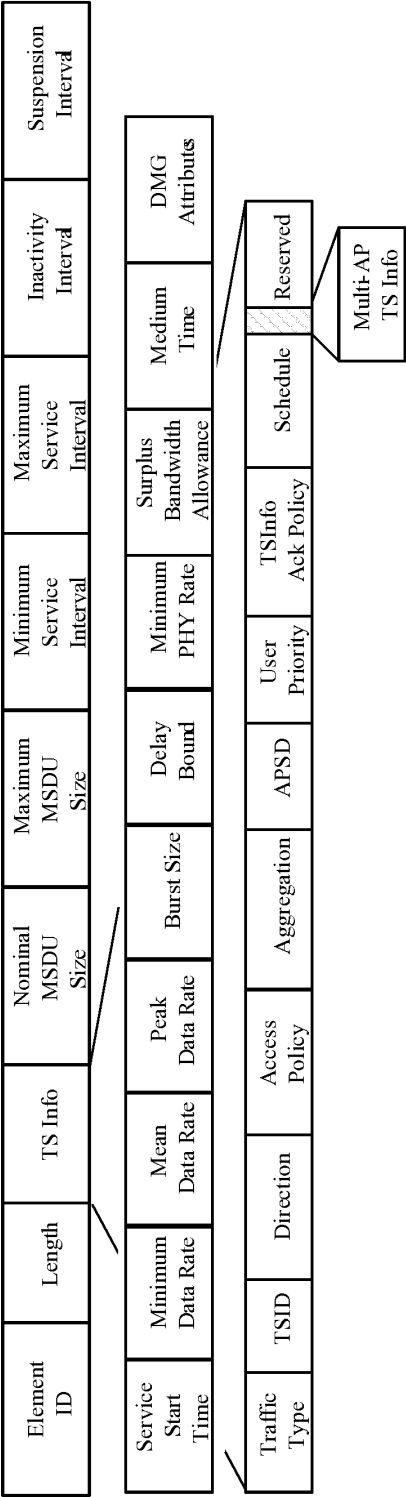
[図18]



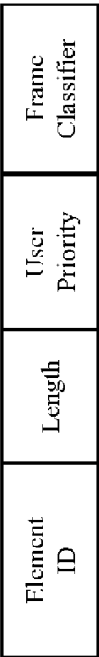
[図19]



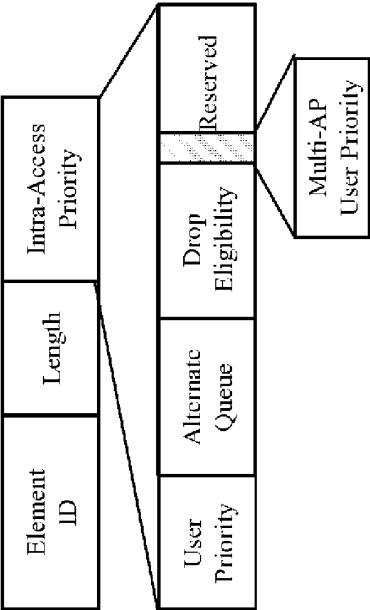
[図20]



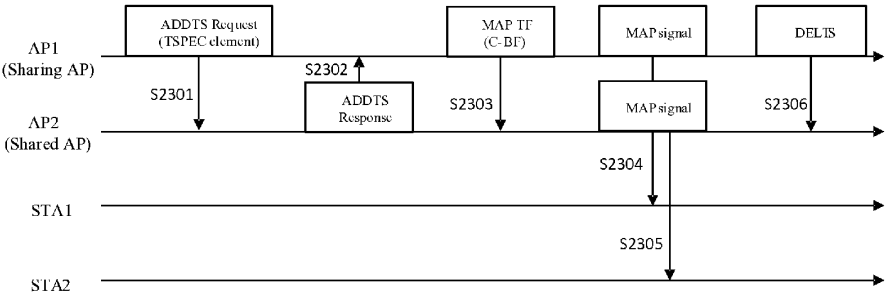
[図21]



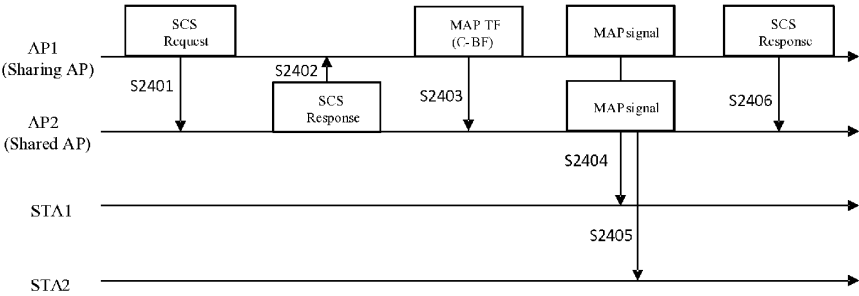
[図22]



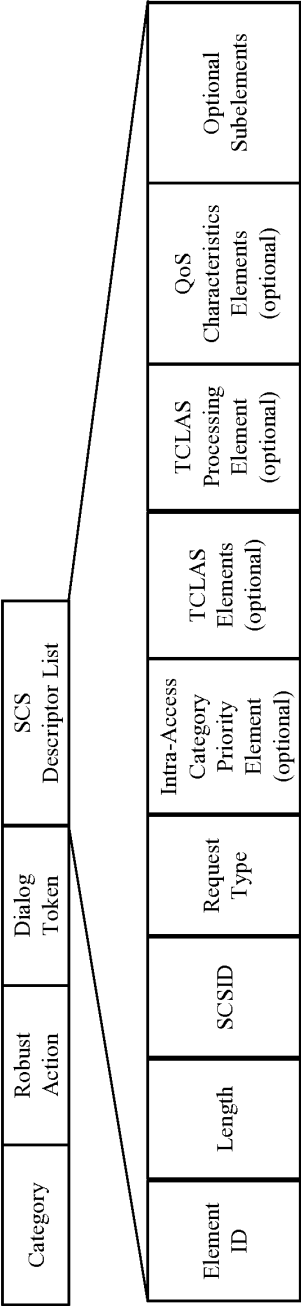
[図23]



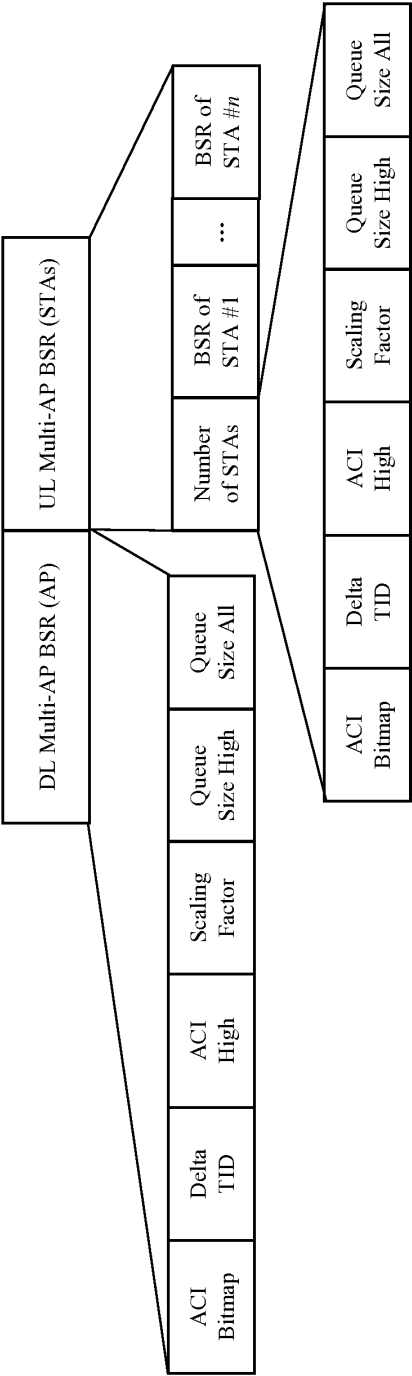
[図24]



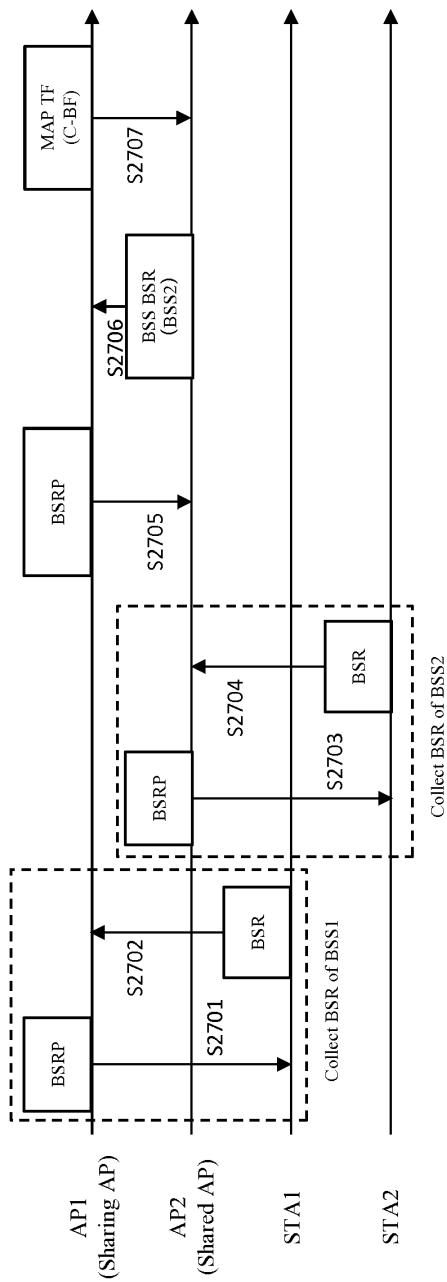
[図25]



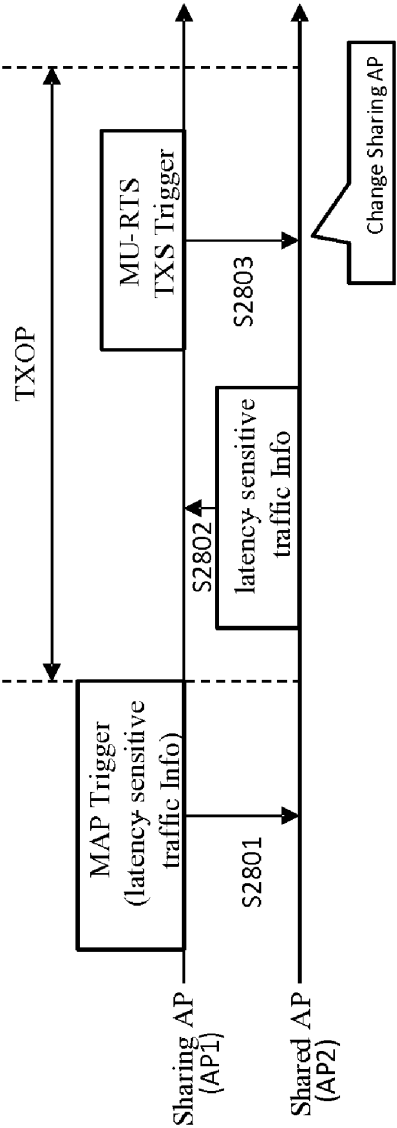
[図26]



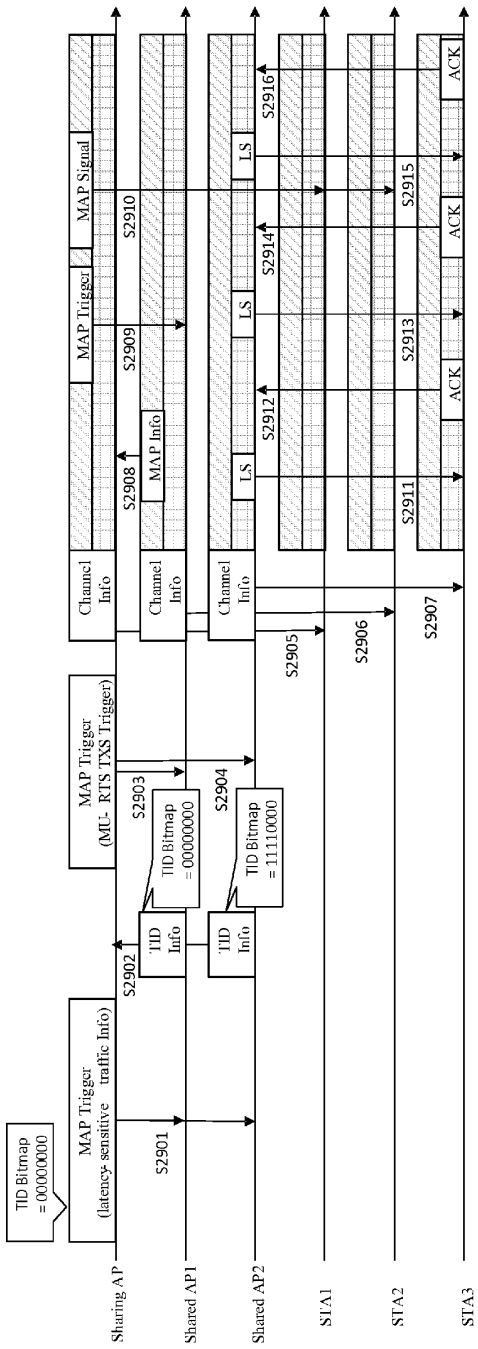
[図27]



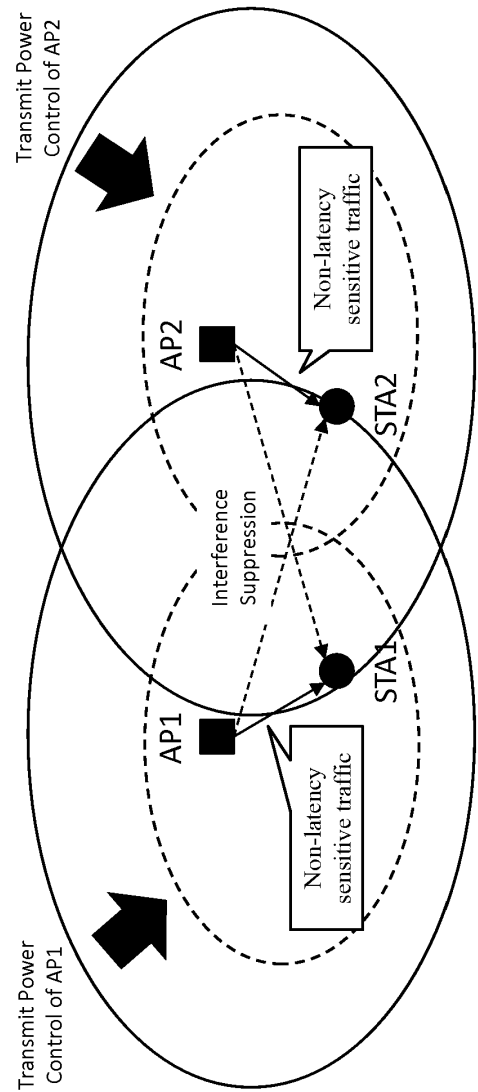
[図28]



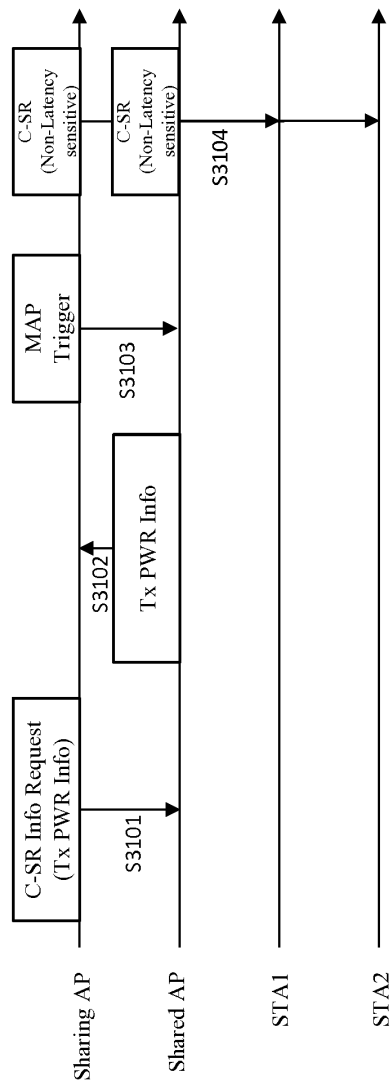
[図29]



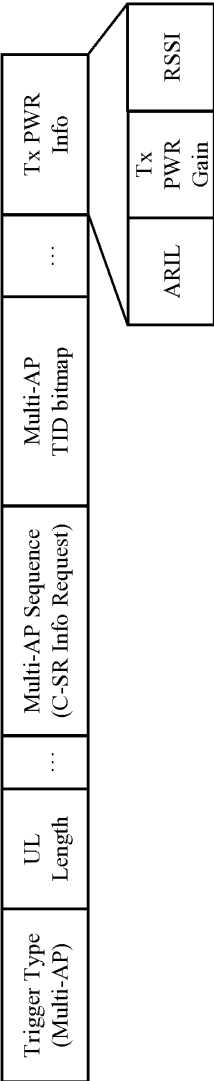
[図30]



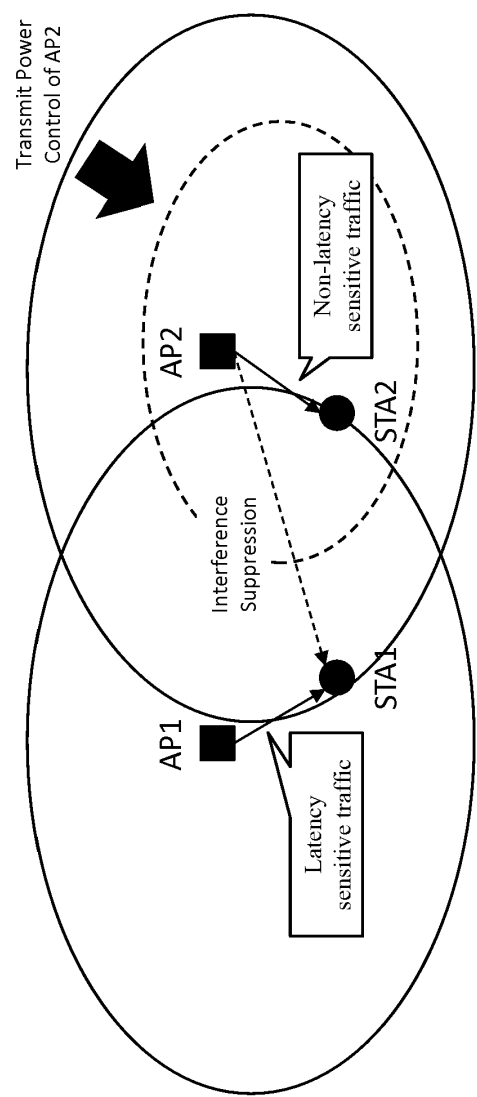
[図31]



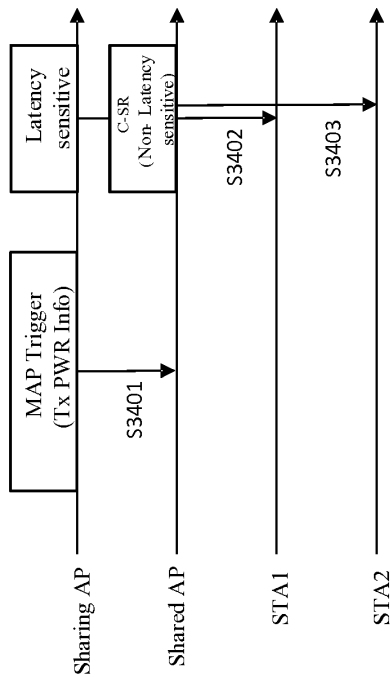
[図32]



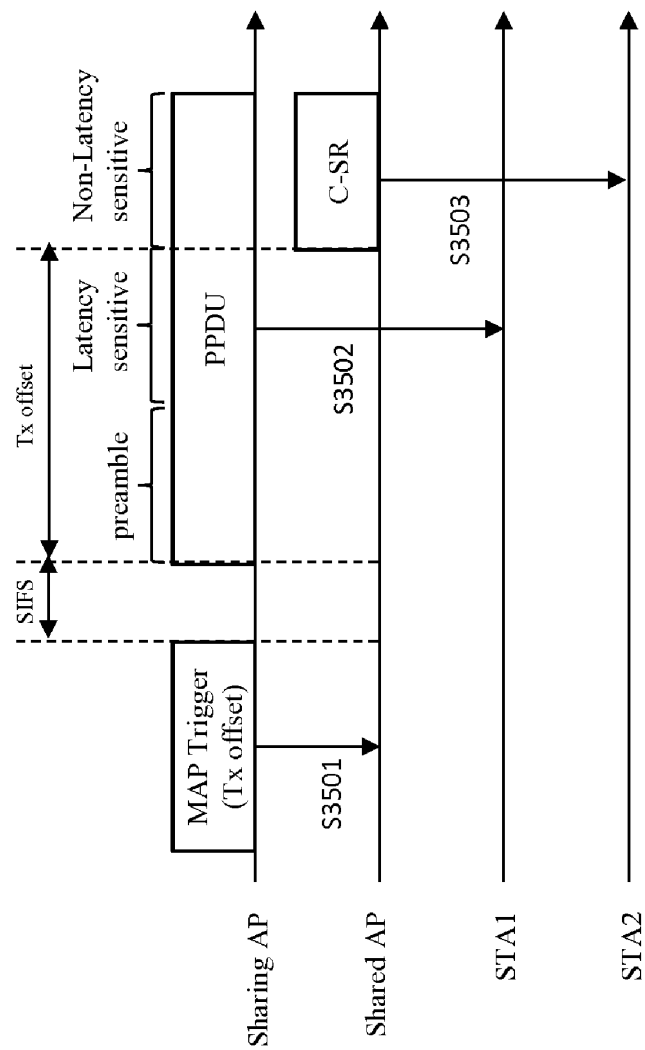
[図33]



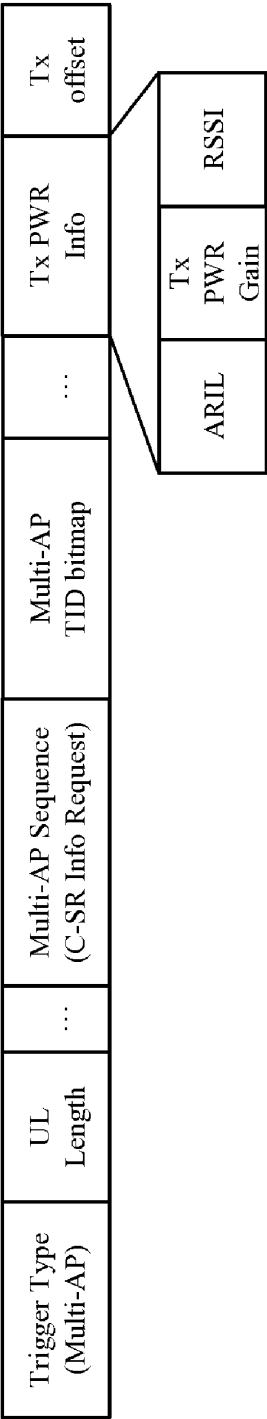
[図34]



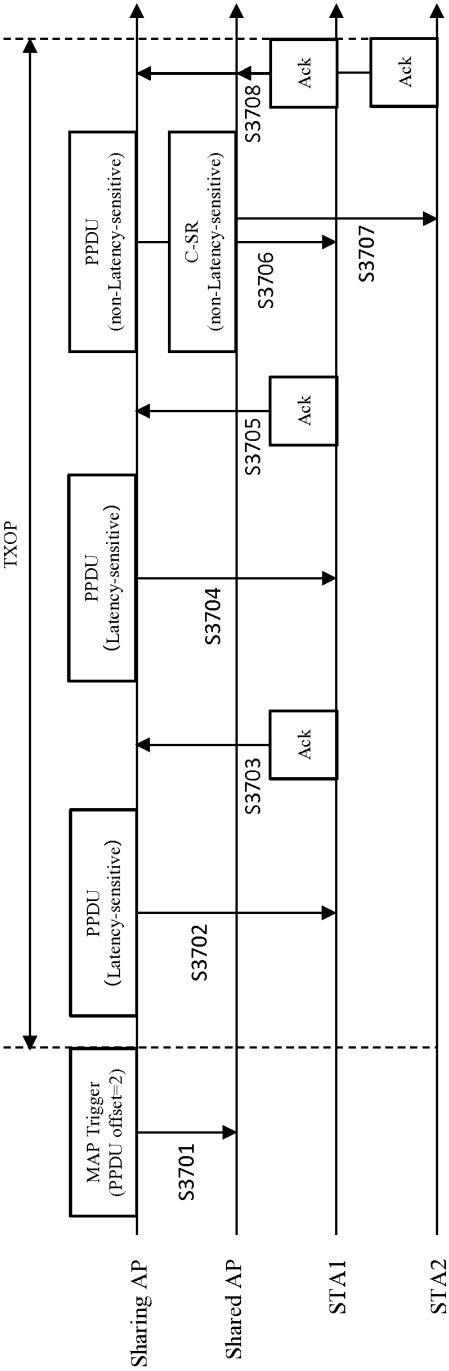
[図35]



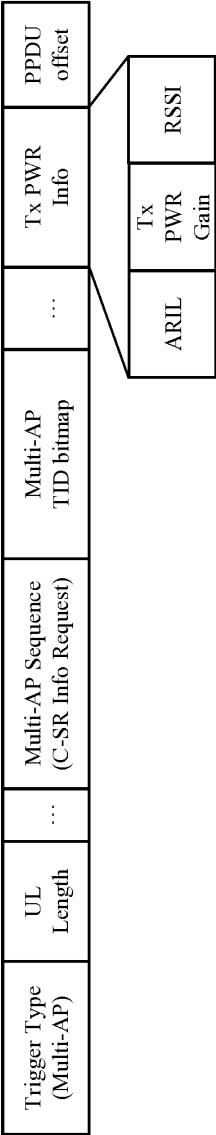
[図36]



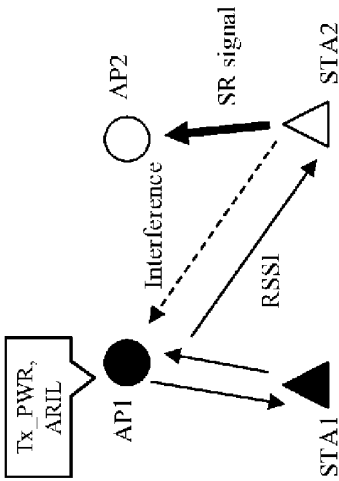
[図37]



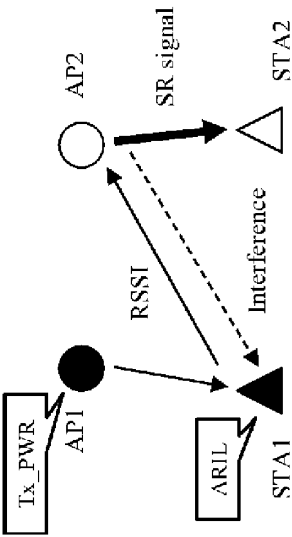
[図38]



[図39]



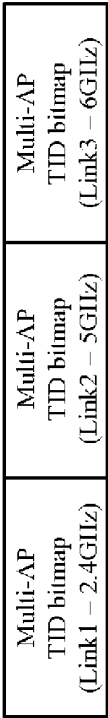
[図40]



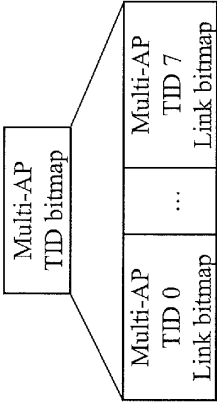
[図41]

Trigger Type (Multi-AP)	UL Length	...	Multi-AP Sequence (C-SR)	Multi-AP TID bitmap	...	PSR
----------------------------	--------------	-----	-----------------------------	------------------------	-----	-----

[図42]



[図43]



[図44]

