

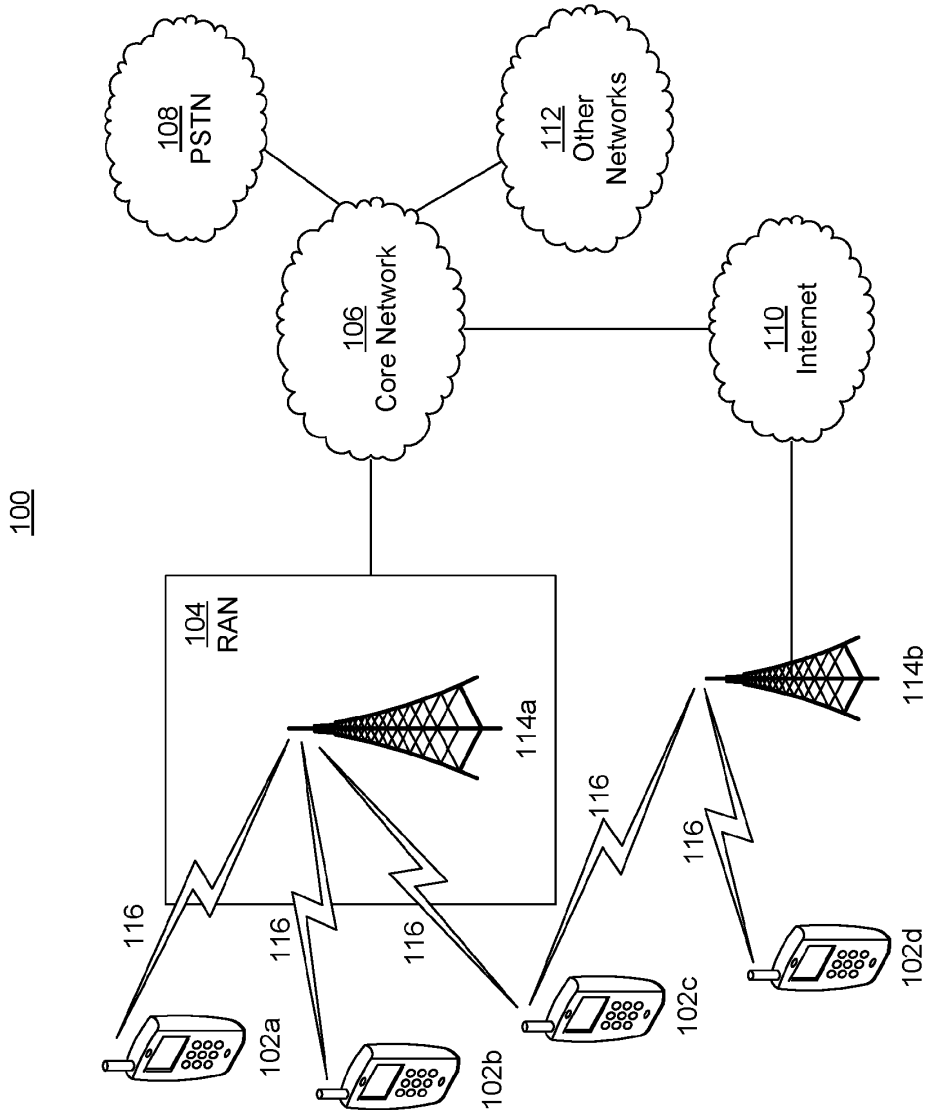


FIG. 1A

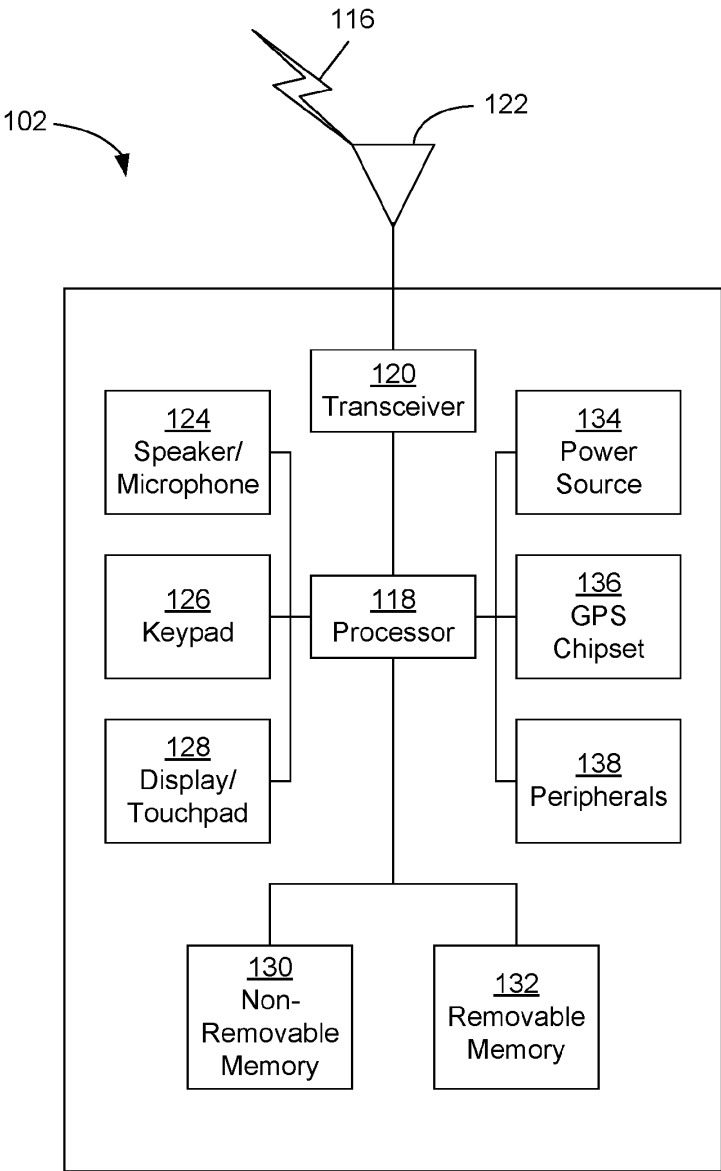


FIG. 1B

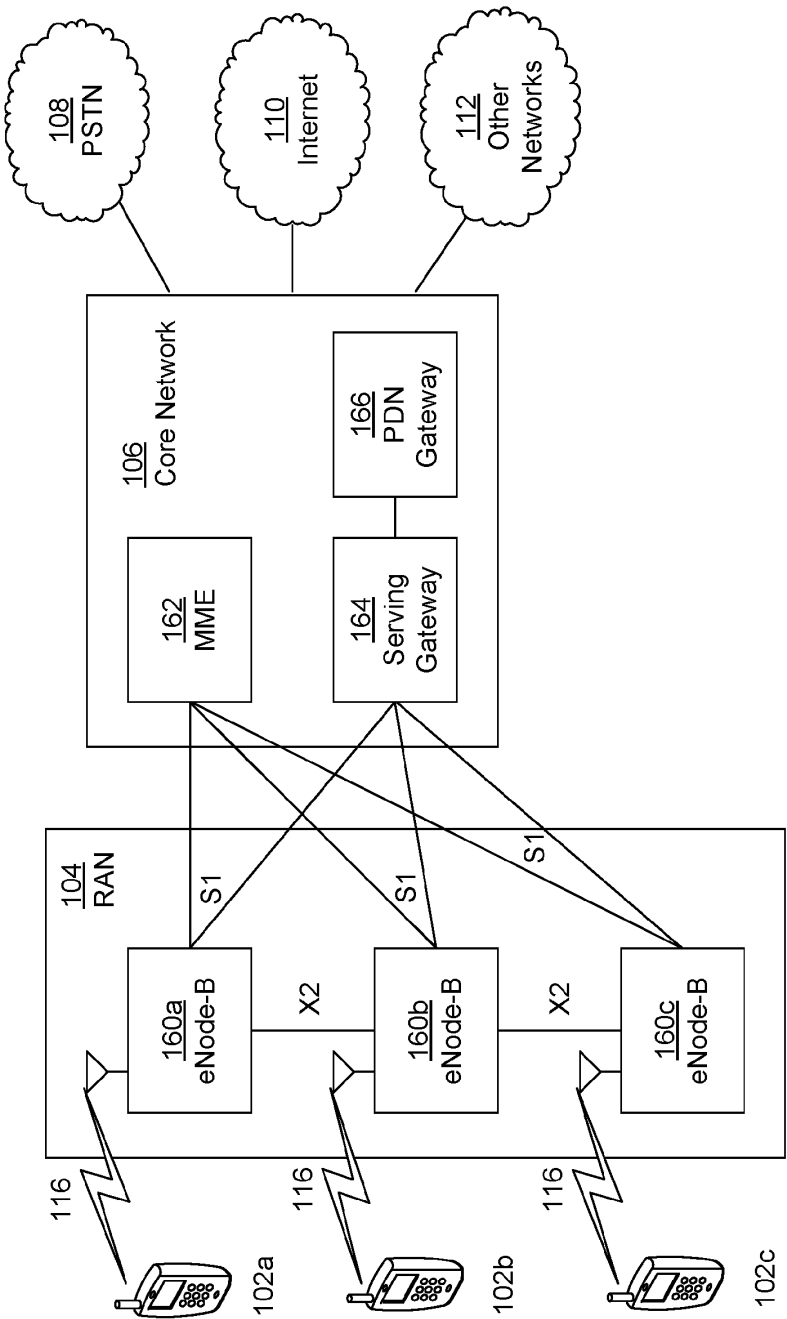


FIG. 1C

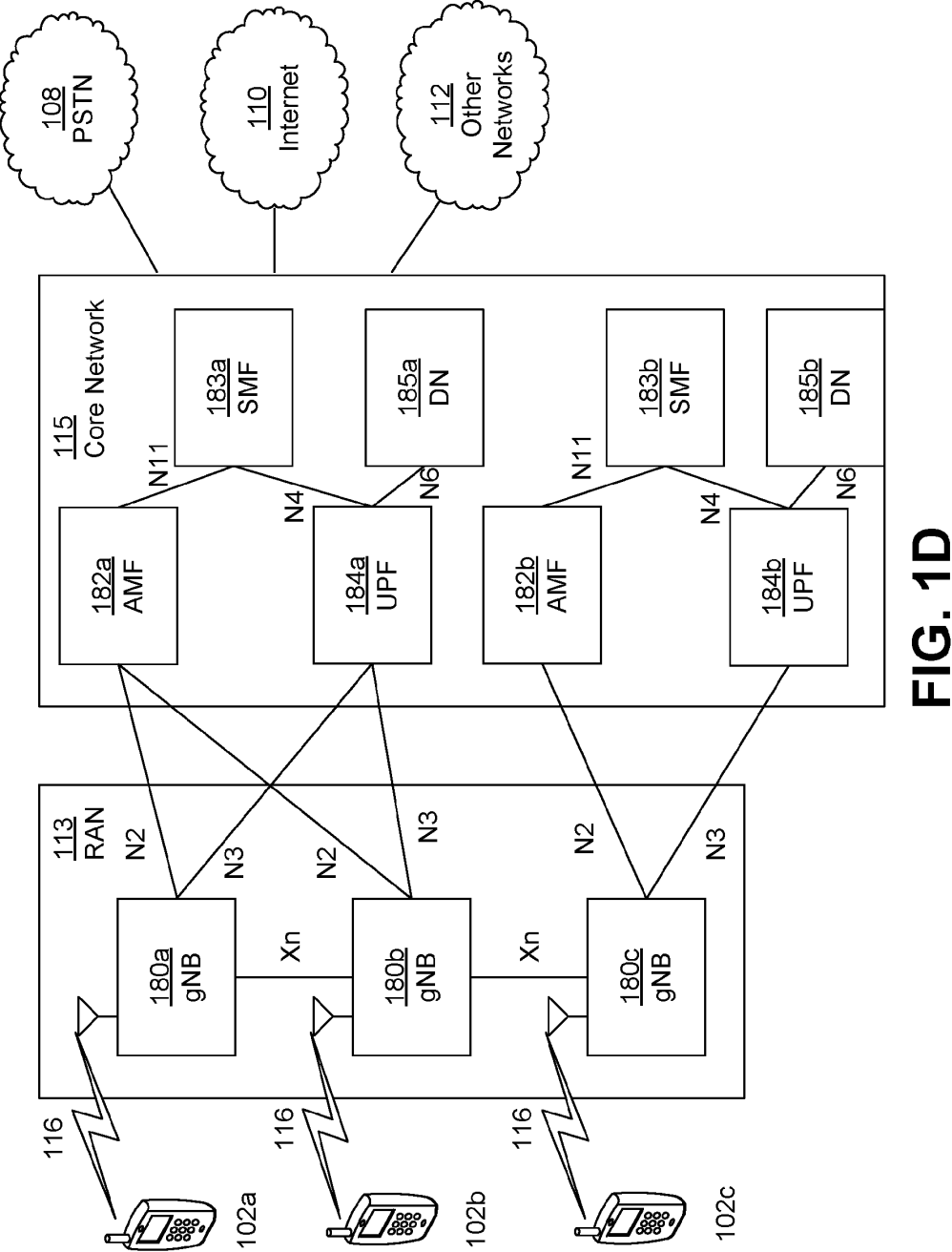


FIG. 1D

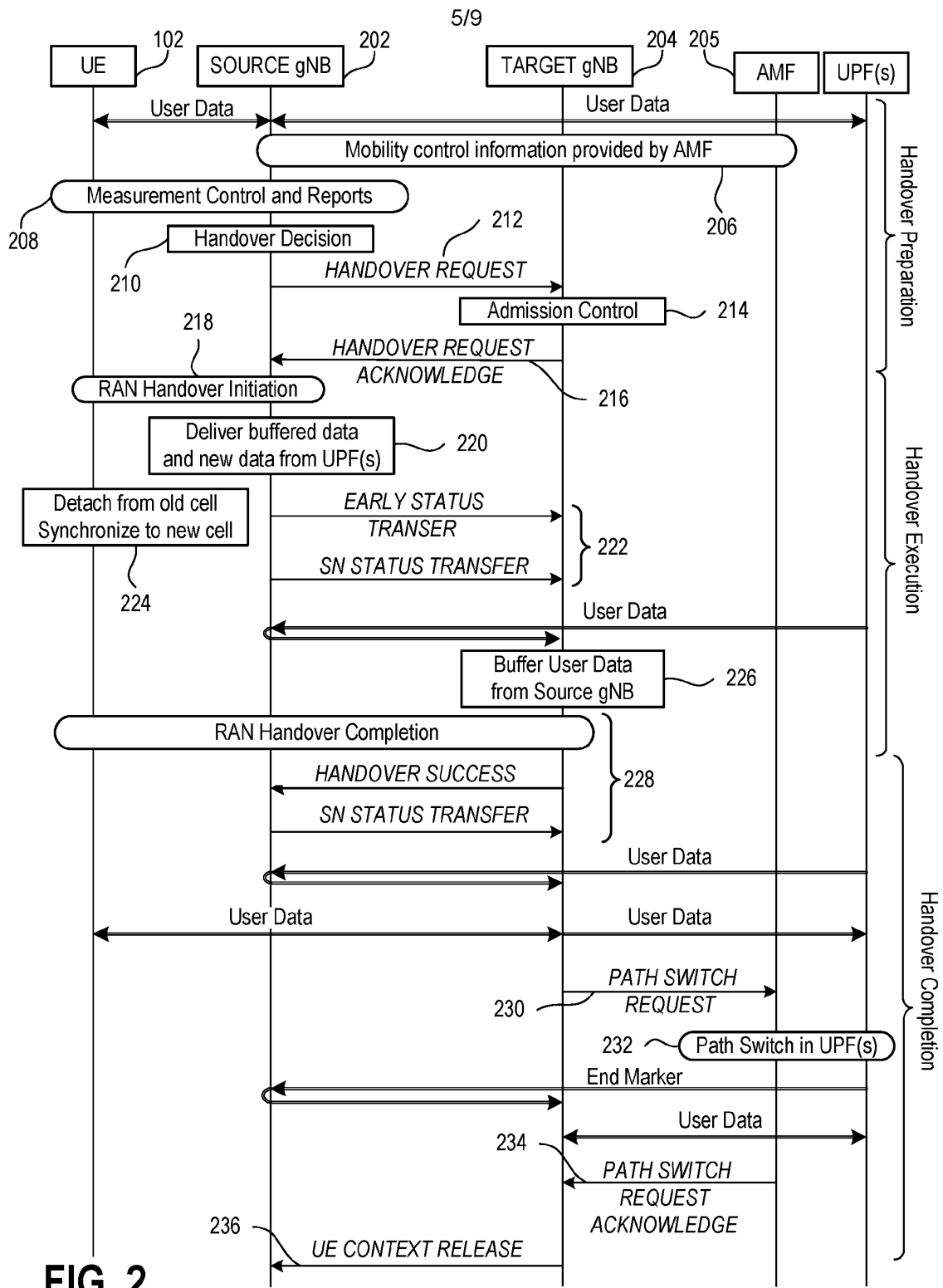


FIG. 2

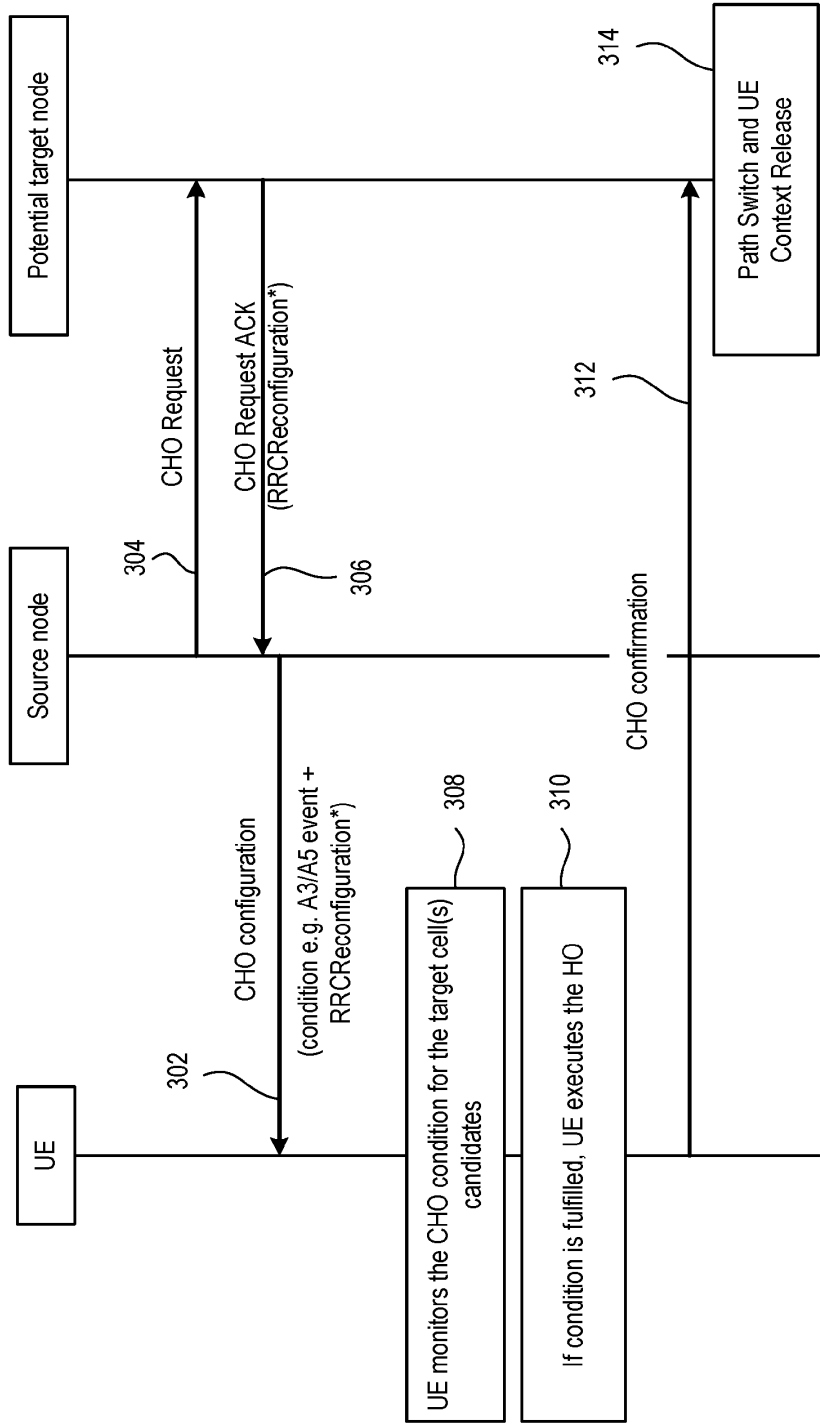


FIG. 3

L1/2 signalling for SCell activation/deactivation (intra-CU).
CHO for PCell switch (intra or inter-CU).
Update the "set" of L1/2 candidates.

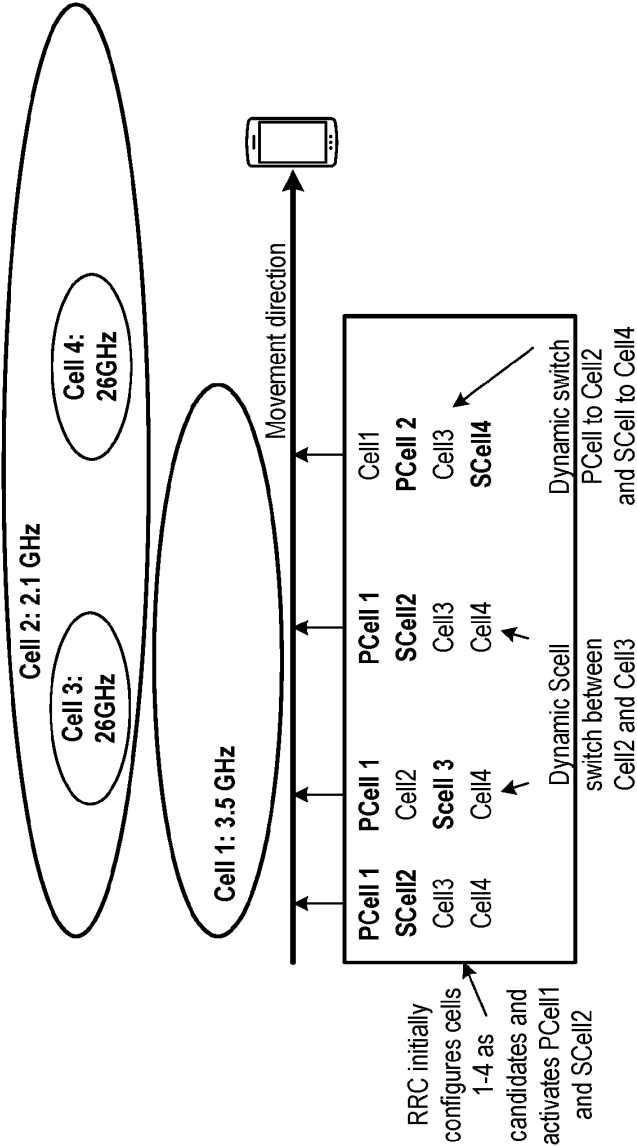


FIG. 4

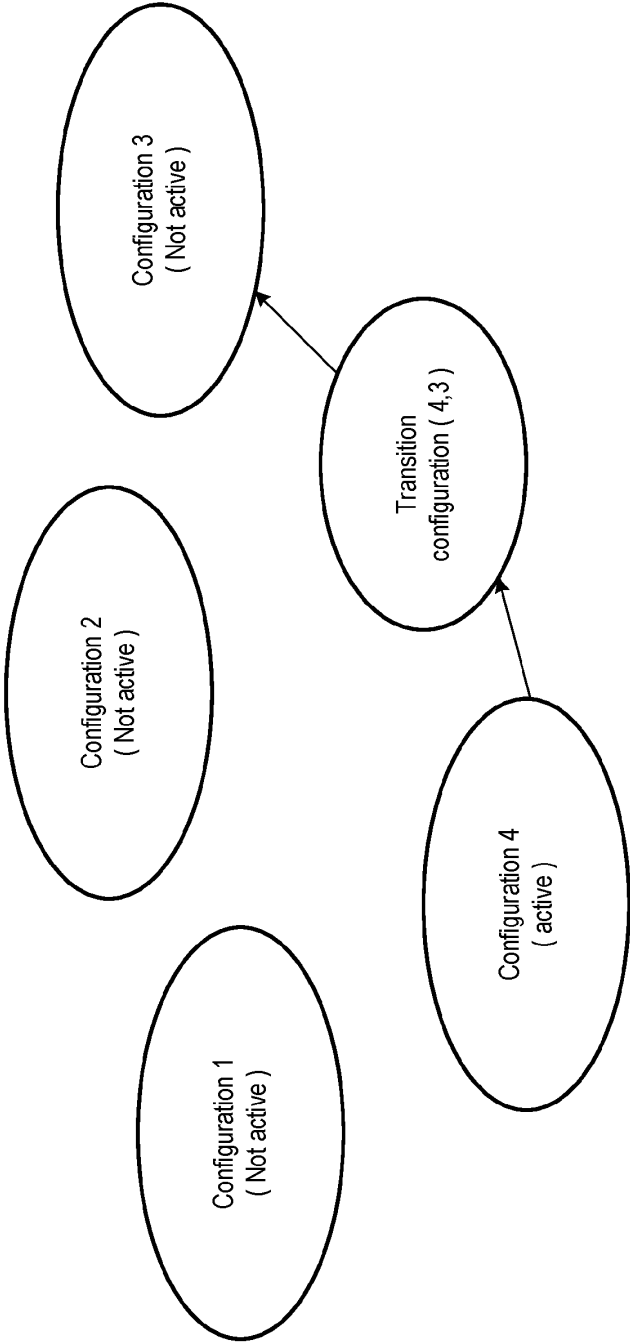


FIG. 5

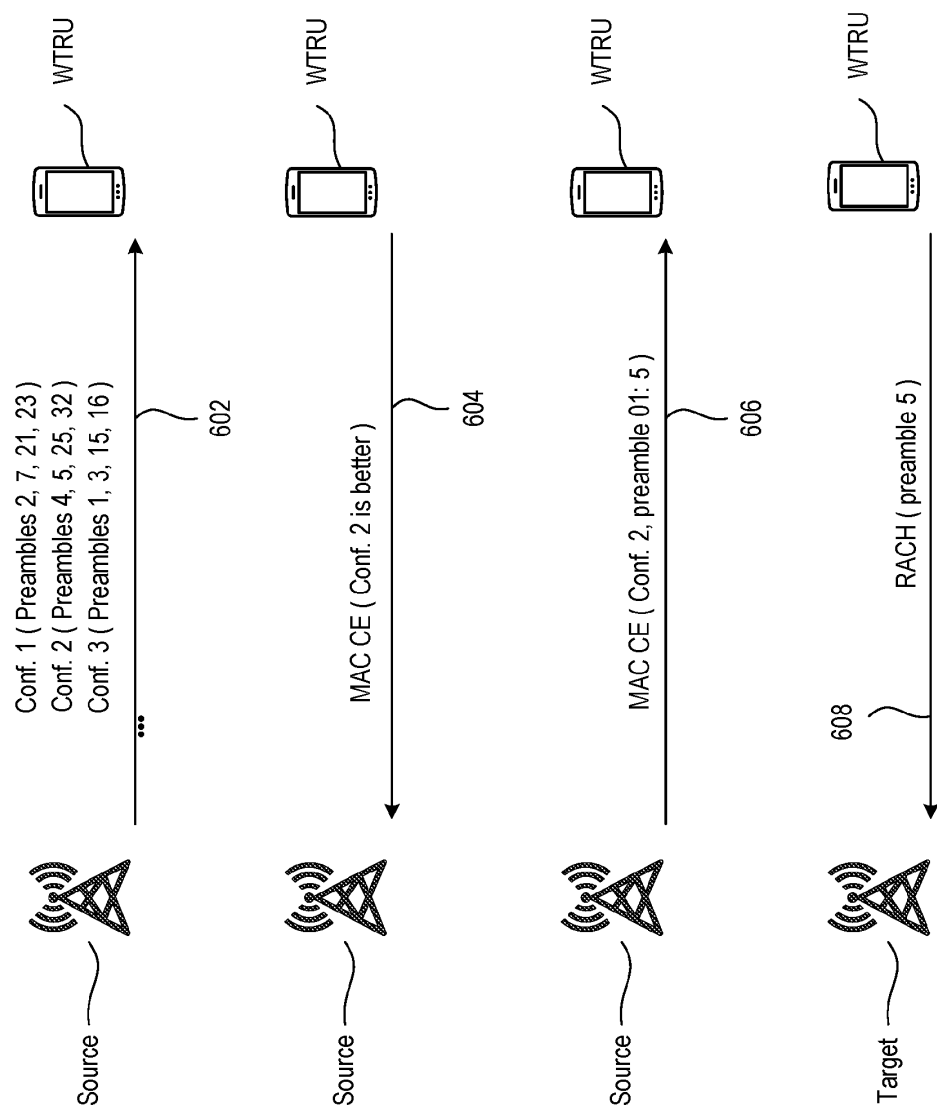


FIG. 6